



Designing Digital Computer Systems with Verilog

By David J. Lilja, Sachin S. Sapatnekar

CAMBRIDGE UNIVERSITY PRESS, United Kingdom, 2007.
 Paperback. Book Condition: New. 240 x 170 mm. Language: English . Brand New Book ***** Print on Demand *****.This book serves both as an introduction to computer architecture and as a guide to using a hardware description language (HDL) to design, model and simulate real digital systems. The book starts with an introduction to Verilog - the HDL chosen for the book since it is widely used in industry and straightforward to learn. Next, the instruction set architecture (ISA) for the simple VeSPA (Very Small Processor Architecture) processor is defined - this is a real working device that has been built and tested at the University of Minnesota by the authors. The VeSPA ISA is used throughout the remainder of the book to demonstrate how behavioural and structural models can be developed and intermingled in Verilog. Although Verilog is used throughout, the lessons learned will be equally applicable to other HDLs. Written for senior and graduate students, this book is also an ideal introduction to Verilog for practising engineers.



READ ONLINE
[4.85 MB]

Reviews

Absolutely essential study publication. Sure, it is enjoy, nonetheless an amazing and interesting literature. I realized this book from my dad and i suggested this pdf to find out.

-- **Justus Abbott**

This publication may be worthy of a read through, and a lot better than other. It is among the most incredible book we have read through. Your daily life period will be change when you total reading this article publication.

-- **Garett Baumbach**