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Design for Task 1

	Initialized Variables	Remarks
L ⊑ ⊔ ⊥	P(Var. × Lab.²) ⊆ U ∅	Finite and satisfies Ascending Chain Condition Partially ordered by subset inclusion May analysis Bottom is empty set
E F	$ \{ (x,?) \mid x \in FV(S.) \} $ $ \{ init(S.) \} $ $ flow(S.) $	Extremal value Because forward analyses Forward analyses
F f _i		

LLVM and Clang Versions Used

Ilvm-3.5-dev clang-3.5

Implementation of Task 2

Step 1: Read the IR file to extract Module M from IR, extract Function main from Module M.

Step 2: Initialize Analysis Map and Traversal Stack, traverse the CFG in Depth First Order (Similar to StackSet.cpp)

Step 3: Upon entry (and popping) of each Basic Block from stack, iterate through the Basic Block's set of instructions to check for initialized variables using *isa*<*Storelnst*>(*I*), push instruction to Analysis Map if it's a store instruction.

Step 4: Extract last instruction of Basic Block to get successor blocks

Step 5: For each successor block, increase value of depth by 1 and push to traversal stack, and then push all initialized variables of parent Basic Block to the successor block.

Step 6: Print result of Analysis map

Implementation of Task 3

Algorithm used is MOP. The implementation for tasks 2 and 3 are pretty much the same, with just a small tweak for Step 5 in Task 3

Step 5: For each successor block, iterate through parent Basic Block's set of initialized variables (instructions). For each instruction, check if the instruction exists in successor block's list of instructions in Analysis map. If does not exist, set *hasChanged* to true and push

instruction to successor's Analysis map. After iterating through all instructions, if *hasChanged* is true, then push successor block to traversal stack.

Build and Run Pass

Check out README.md in folder