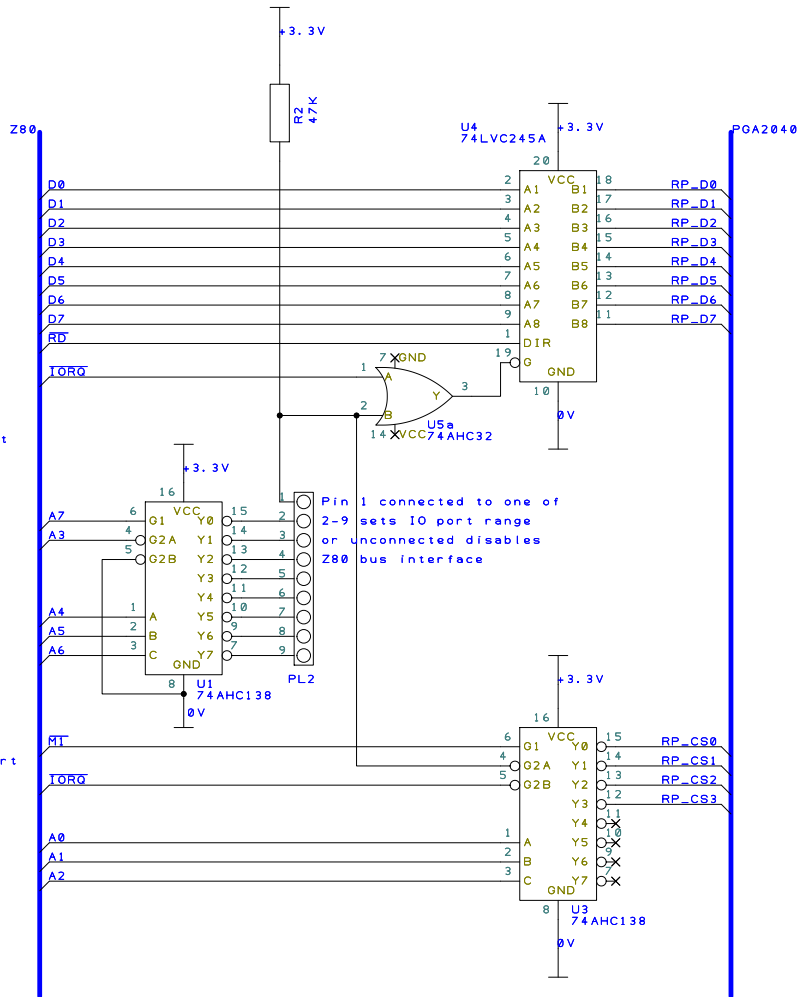


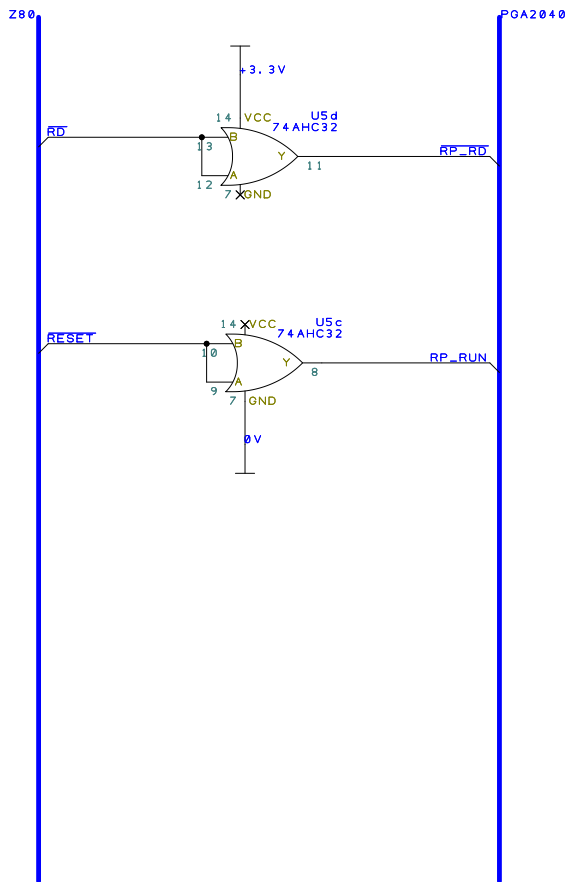
RC2014 Bus Header

U5a lacks /M1 as input. No matter because /RD is high during interrupt acknowledgement so U4 might output (needlessly) but in a harmless direction.

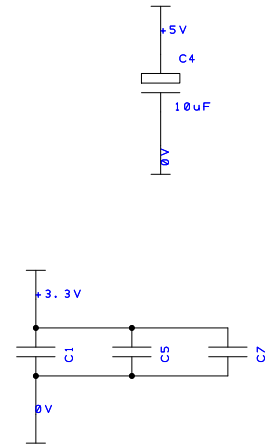
U3 has /M1 and /IORQ as input so /RP-CSn only assert appropriately.



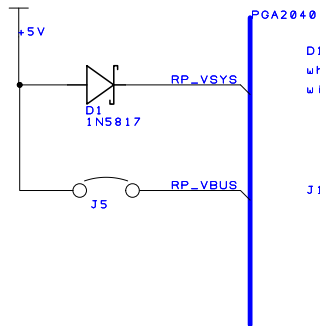
Address Decoding and Data Bus Transceiver



Level Shifting

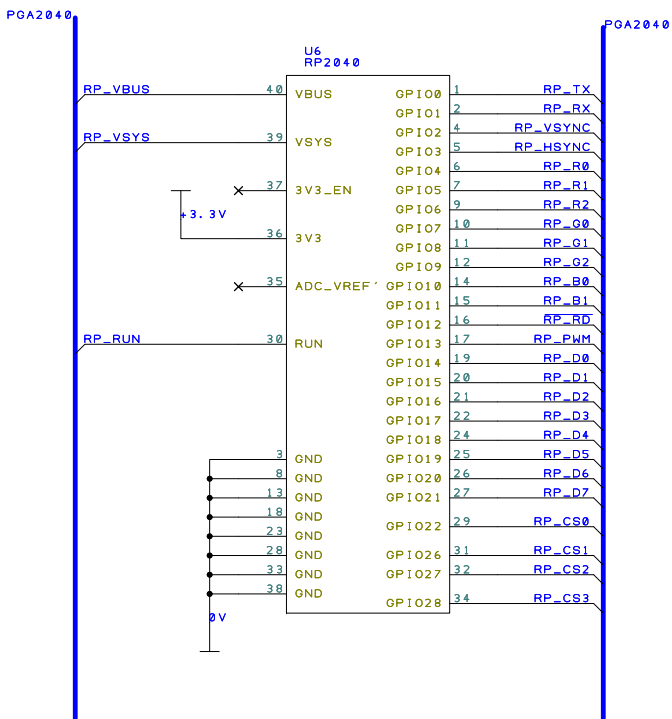


Power

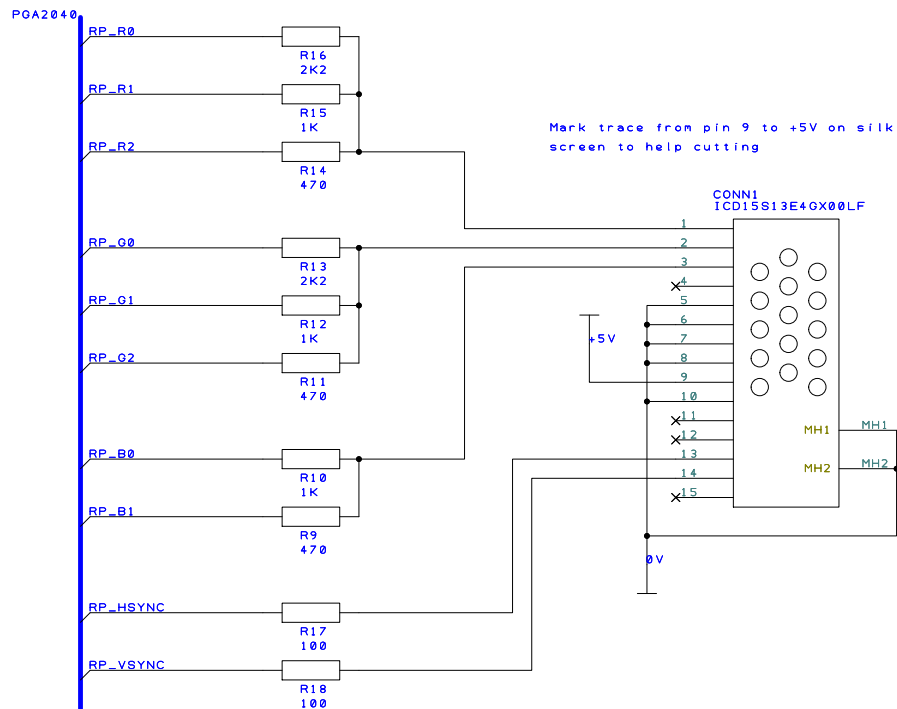


D1 allows safe RP2040 firmware update while installed in unpowered RC2014 backplane without USB powering whole computer.

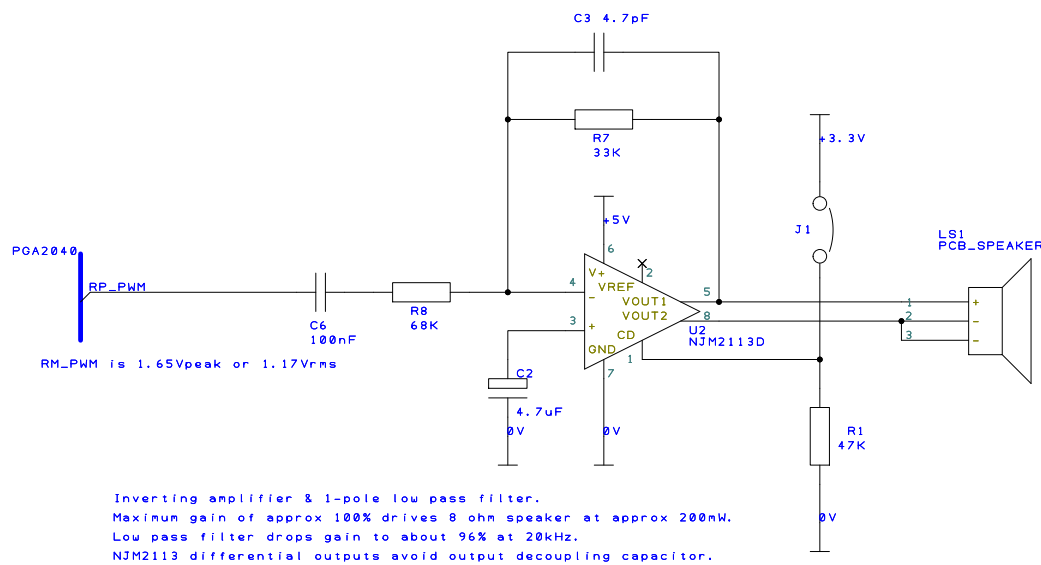
J1 provides power to USB recepticle when used as a USB host



Daughter Board

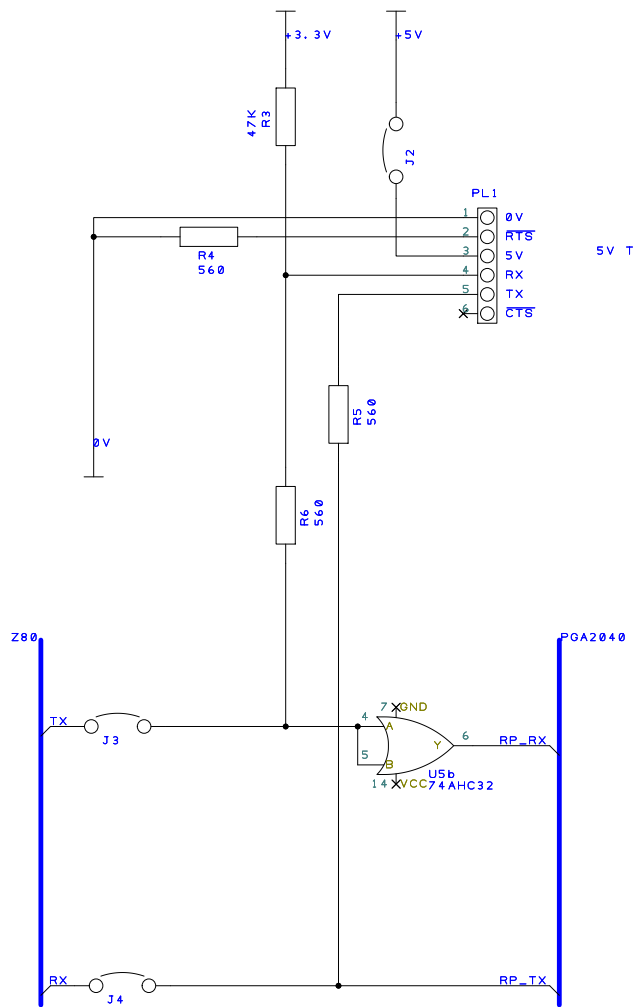


Video Output



Inverting amplifier & 1-pole low pass filter. Maximum gain of approx 100% drives 8 ohm speaker at approx 200mW. Low pass filter drops gain to about 96% at 20kHz. NJM2113 differential outputs avoid output decoupling capacitor.

Audio Output



Serial