





D	0	Task Mode	Task Name	Duration	Start	Finish	Predecesso	ec 14, '14 Jan 18, '15 Feb 22, '15 Mar 29, '15 May 3, '15   Jun 7, '15   Jul 12   F   S   S   M   T   W   T   F   S   S   M   T   W   T   F
40		*	4.3.2 Integration building and testing	2 days	Tue 4/7/15	Wed 4/8/15	31	Ali, Meng, Pedro, Robert  Ali, Meng, Pedro, Robert
41		*	4.3.3 Whole board RJ-45 communication build and test	8 days	Thu 4/9/15	Sun 4/19/15	40,29	
42		*	4.4 Enclosure design	6 days	Mon 4/20/15	Sat 4/25/15		Ali, Meng, Pedro, Robert
43		*	5 Assembly	13 days	Sun 4/26/1	5Tue 5/12/1	27	
44		*	5.1 PCB assembly and test	9 days	Sun 4/26/15	Wed 5/6/15	27	Ali, Meng, Pedro, Robert
45	•	*	5.3 Enclosure Cut and Assemble	4 days	Mon 5/4/15	Thu 5/7/15	42	Ali, Meng, Pedro, Rober
46		*	5.4 Final tests and verification	3 days	Fri 5/8/15	Tue 5/12/15	45,44	Ali, Meng, Pedro, Robe
47	<b></b>		6 Final Presentation	6 days	Wed 5/13/15	Wed 5/20/15	43	Ali, Meng, Pedro, Rol

