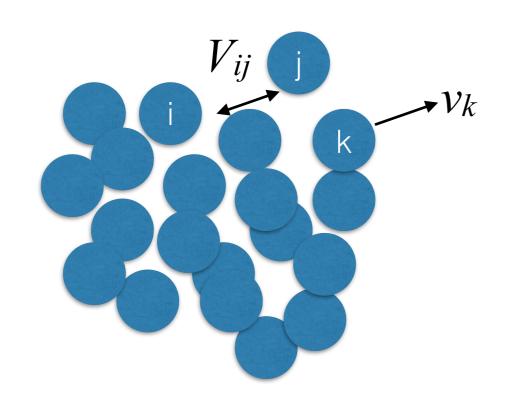
Scientific programming with graphics processing units

Session 2: molecular dynamics

Eugene DePrince Florida State University

Accelerating molecular dynamics



particles positions are updated in time according to Newton's 2nd law: f=ma

for lennard-jones particles, the potential is

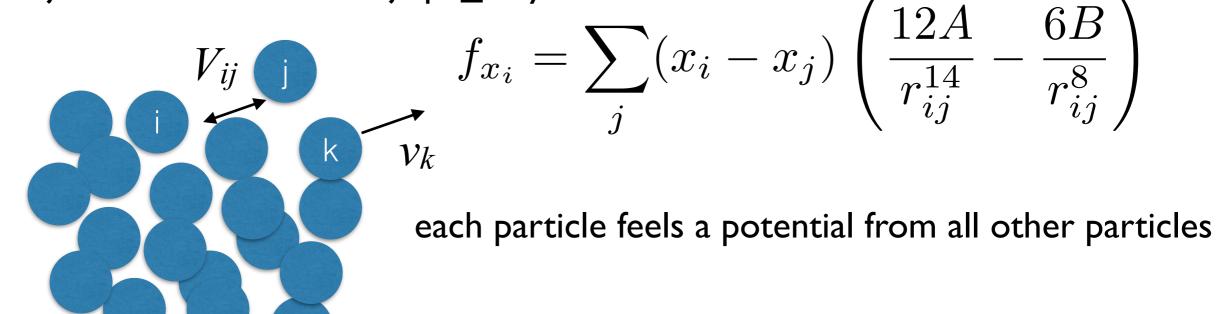
$$V_{ij} = \frac{A}{r_{ij}^{12}} - \frac{B}{r_{ij}^6}$$

and the corresponding forces are

$$f_{x_i} = -\sum_{j} \frac{\partial V_{ij}}{\partial x_i} \qquad f_{y_i} = -\sum_{j} \frac{\partial V_{ij}}{\partial y_i}$$
$$f_{x_i} = \sum_{j} (x_i - x_j) \left(\frac{12A}{r_{ij}^{14}} - \frac{6B}{r_{ij}^{8}} \right)$$

CPU and GPU code for this section can be found in

S2I2/Ij/main.cu and S2I2/Ij/cpu_only.cc



for CPU and GPU code, it makes sense to parallelize over particles and evaluate force contribution from all other particles

technically $f_i = -f_j$, but we will do twice as much work to make parallelizing the code easier

OMP CPU version

parallelize over particles

```
#pragma omp parallel for schedule(dynamic) num_threads (nthreads)
for (int i = 0; i < n; i++) {
    double xi = x[i];
    double yi = y[i];
    double zi = z[i];
    double fxi = 0.0;
    double fyi = 0.0;
    double fzi = 0.0;
    for (int j = 0; j < n; j++) {
        if ( i == j ) continue;
        double dx = xi - x[j];
        double dy = yi - y[j];
        double dz = zi - z[j];
```

OMP CPU version

```
double r2 = dx*dx + dy*dy + dz*dz;
        double r6 = r2*r2*r2;
        double r8 = r6*r2;
        double r14 = r6*r6*r2;
        double f = A12 / r14 - B6 / r8;
        fxi += dx * f;
       fyi += dy * f;
        fzi += dz * f;
    fx[i] = fxi;
    fy[i] = fyi;
    fz[i] = fzi;
}
```

pretty straightforward! later, we'll improve this by

- (I) only including contributions from nearby particles and
- (2) including periodic boundary conditions

CUDA version: host code

```
// pointers to gpu memory
double * gpu_x;
double * gpu_y;
double * gpu_z;
double * gpu_fx;
double * gpu_fy;
double * gpu_fz;
// allocate GPU memory
cudaMalloc((void**)&gpu_x,n*sizeof(double));
cudaMalloc((void**)&gpu_y,n*sizeof(double));
cudaMalloc((void**)&gpu z,n*sizeof(double));
cudaMalloc((void**)&gpu_fx,n*sizeof(double));
cudaMalloc((void**)&gpu_fy,n*sizeof(double));
cudaMalloc((void**)&gpu_fz,n*sizeof(double));
// copy particle positions to GPU
cudaMemcpy(gpu_x,x,n*sizeof(double),cudaMemcpyHostToDevice);
cudaMemcpy(gpu_y,y,n*sizeof(double),cudaMemcpyHostToDevice);
cudaMemcpy(gpu_z,z,n*sizeof(double),cudaMemcpyHostToDevice);
```

CUDA version: host code cont.

```
// threads per block should be multiple of the warp
// size (32) and has max value cudaProp.maxThreadsPerBlock
int threads_per_block = NUM_THREADS;
int maxblocks
                      = MAX_BLOCKS;
long int nblocks_x = n / threads_per_block;
long int nblocks_y = 1;
if ( n % threads_per_block != 0 ) {
   nblocks_x = (n + threads_per_block - n % threads_per_block)/threads_per_block;
if (nblocks_x > maxblocks){
   nblocks_y = nblocks_x / maxblocks + 1;
   nblocks_x = nblocks_x / nblocks_y + 1;
// a two-dimensional grid: nblocks_x by nblocks_y
dim3 dimgrid (nblocks_x,nblocks_y);
ForcesOnGPU<<<dimgrid,threads_per_block>>>
    (n,gpu_x,gpu_y,gpu_z,gpu_fx,gpu_fy,gpu_fz,A12,B6);
```

try writing your own cuda kernel for this function

CPU version: S2I2/gpu/lj/cpu_only.cc

- I. remember, each thread executes the same code
- 2. each thread should handle one particle (index i from CPU code)
- 3. don't forget to check your index bounds!

CUDA version: device code

```
// evaluate forces on GPU
__global__ void ForcesOnGPU(int n, double * x, double * y, double * z,
         double * fx, double * fy, double * fz, double A12, double B6) {
    int blockid = blockIdx.x*gridDim.y + blockIdx.y;
    int i = blockid*blockDim.x + threadIdx.x;
   if ( i >= n ) return;
   double xi = x[i];
   double yi = y[i];
    double zi = z[i];
   double fxi = 0.0;
   double fyi = 0.0;
   double fzi = 0.0;
   for (int j = 0; j < n; j++) {
        if ( j == i ) continue;
        double dx = xi - x[j];
       double dy = yi - y[j];
        double dz = zi - z[j];
```

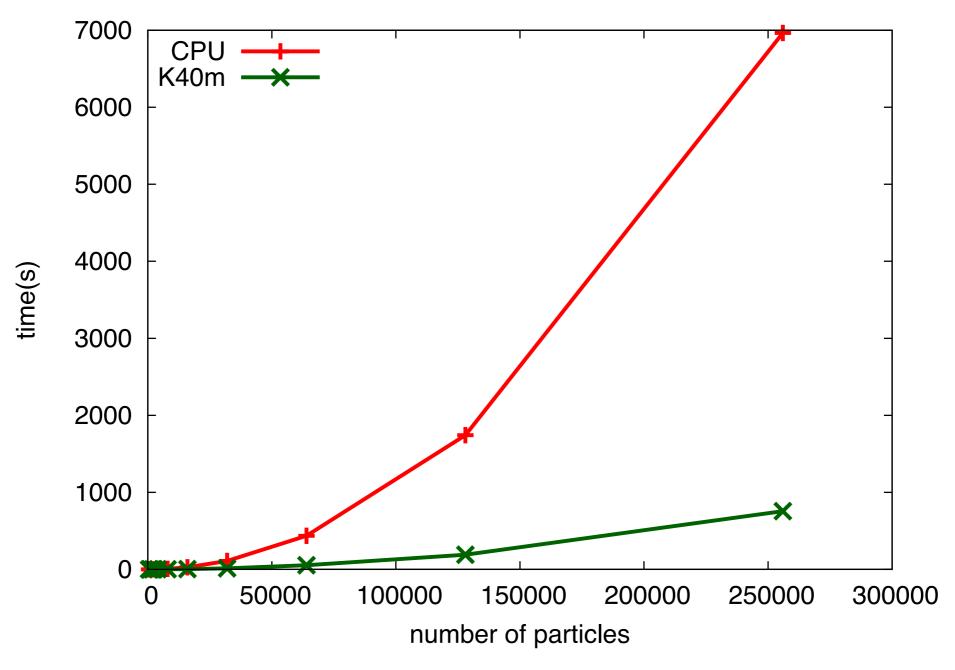
CUDA version: device code

```
double r2 = dx*dx + dy*dy + dz*dz;
   double r6 = r2*r2*r2;
   double r8 = r6*r2;
   double r14 = r6*r6*r2;
   double f = A12 / r14 - B6 / r8;
   fxi += dx * f;
   fyi += dy * f;
   fzi += dz * f;
}
fx[i] = fxi;
fy[i] = fyi;
fz[i] = fzi;
```

CUDA version: device code

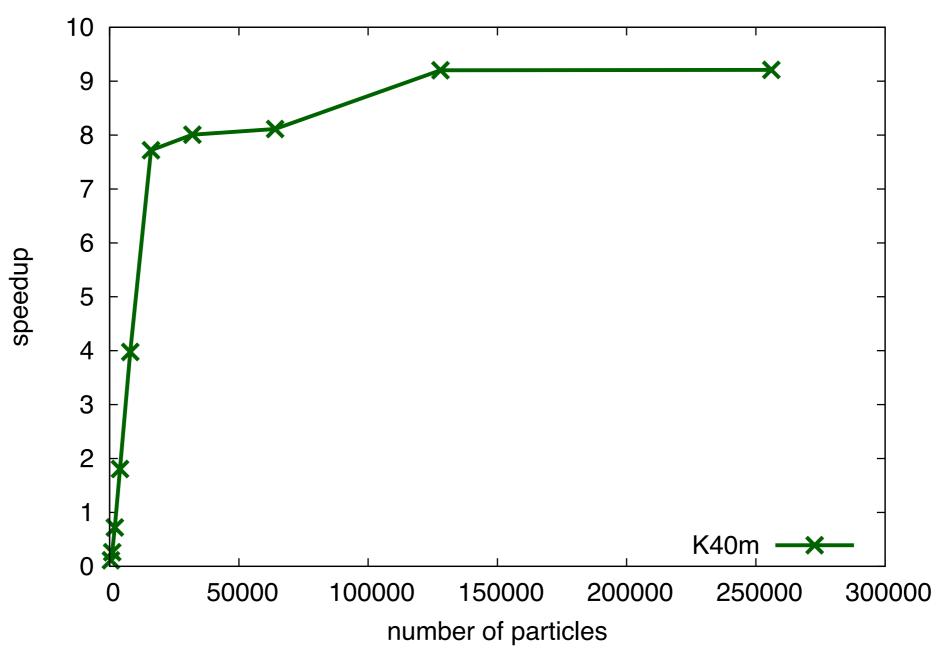
```
double r2 = dx*dx + dy*dy + dz*dz;
   double r6 = r2*r2*r2;
   double r8 = r6*r2;
   double r14 = r6*r6*r2;
   double f = A12 / r14 - B6 / r8;
   fxi += dx * f;
   fyi += dy * f;
   fzi += dz * f;
}
fx[i] = fxi;
fy[i] = fyi;
fz[i] = fzi;
```

so, how'd we do?



100 force evaluations, no data transfersCPU = 2 8-core intel sandy bridge CPUs (blueridge)

so, how'd we do?



GPU slower for small systems, but much more efficient for large systems can we do even better???

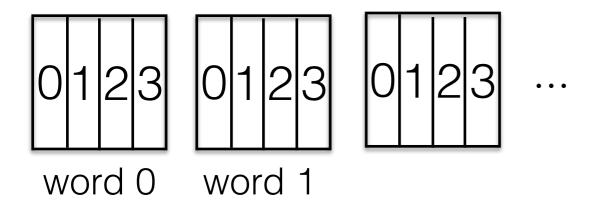
- accessing GPU global memory is very costly

shared memory:

- threads within a block can share information with each other
- much faster access than global memory
- like having complete control over cache
- access over warps (32 threads, with consecutive threadIdx.x)
- 32 threads execute simultaneously (SIMD style)
- branching causes threads to execute in serial (ifs are bad)
- threads in a warp always belong to the same block
- threads of a warp all access shared memory together
- there are fast accesses and slow accesses (bank conflicts)

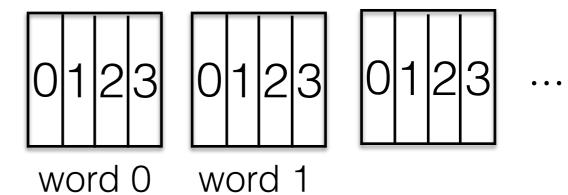
- 64k shared/L called words (
- shared mem

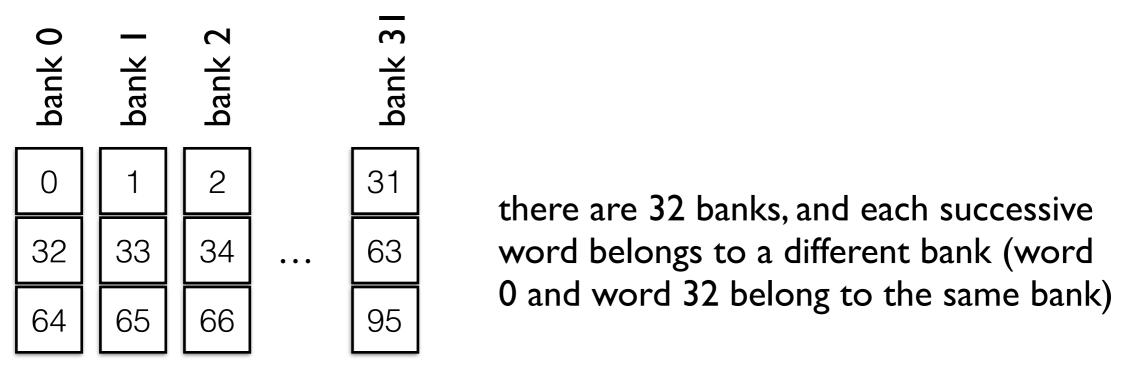
- 64k shared memory per SM, broken into 4 byte sections, called words (1/2 double).



- there are 32 banks, and each successive word belongs to a different bank (word 0 and word 32 belong to the same bank)
- shared memory is read in entire words. ask for a byte, a word is read
- reading a double requires TWO bank requests (since a double is two words)

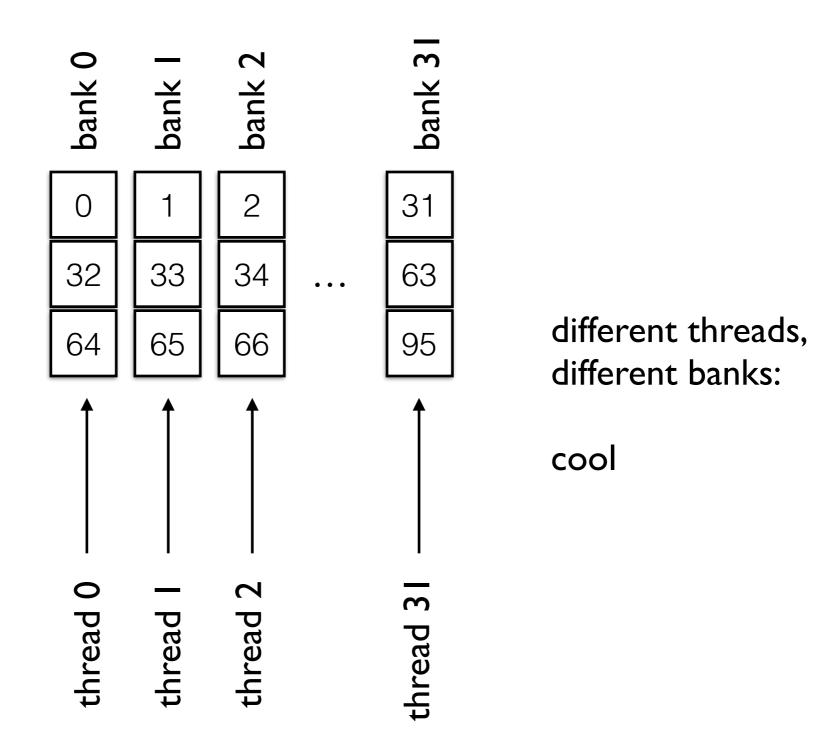
- 64k shared memory per SM, broken into 4 byte sections, called words (1/2 double).



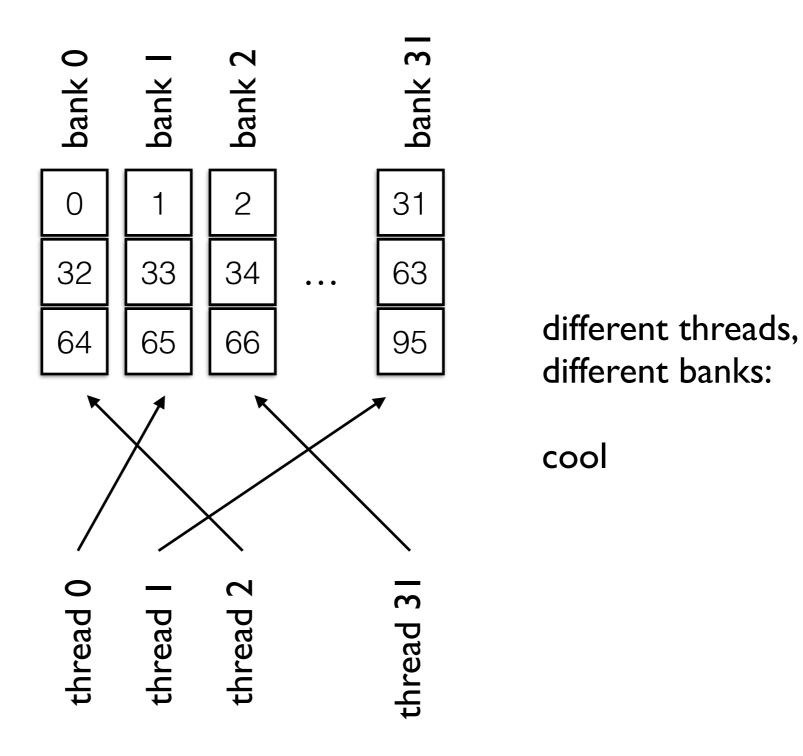


- shared memory is read in entire words. ask for a byte, a word is read
- reading a double requires TWO bank requests (since a double is two words)

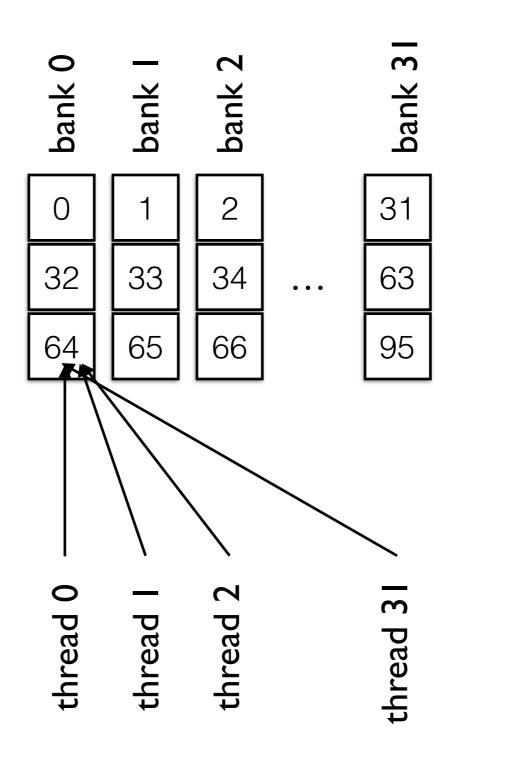
threads in a warp access banks in different ways



threads in a warp access banks in different ways



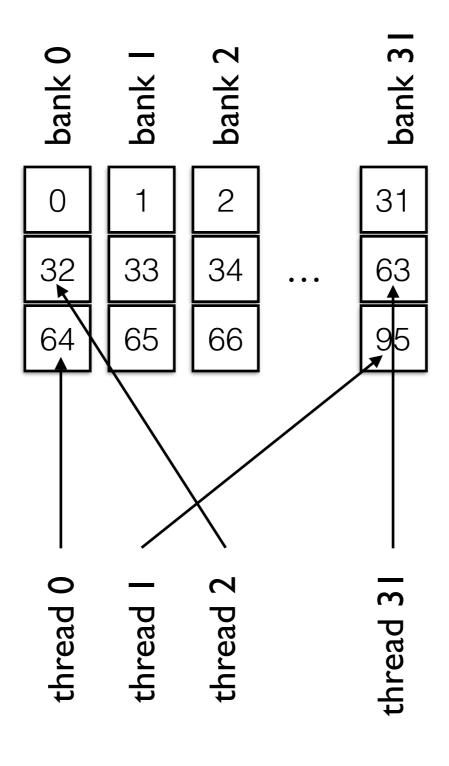
threads in a warp access banks in different ways



different threads, same bank, same word:

cool

threads in a warp access banks in different ways



different threads, same bank, different word:

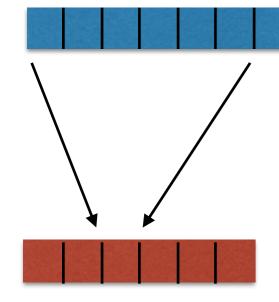
not cool

bank conflict! access to shared memory is serialized

how does this relate to our problem?

```
// evaluate forces on GPU
__global__ void ForcesOnGPU(int n, double * x, double * y, double * z,
         double * fx, double * fy, double * fz, double A12, double B6) {
    int blockid = blockIdx.x*gridDim.y + blockIdx.y;
    int i = blockid*blockDim.x + threadIdx.x;
    if ( i >= n ) return;
   double xi = x[i];
    double yi = y[i];
    double zi = z[i];
    double fxi = 0.0;
    double fyi = 0.0;
    double fzi = 0.0;
                                          we're doing n<sup>2</sup> global memory reads
    for (int j = 0; j < n; j++) {
                                          is this necessary? no!
        if ( j == i ) continue;
        double dx = xi - x[j];
        double dy = yi - y[j];
        double dz = zi - z[j];
```

global memory

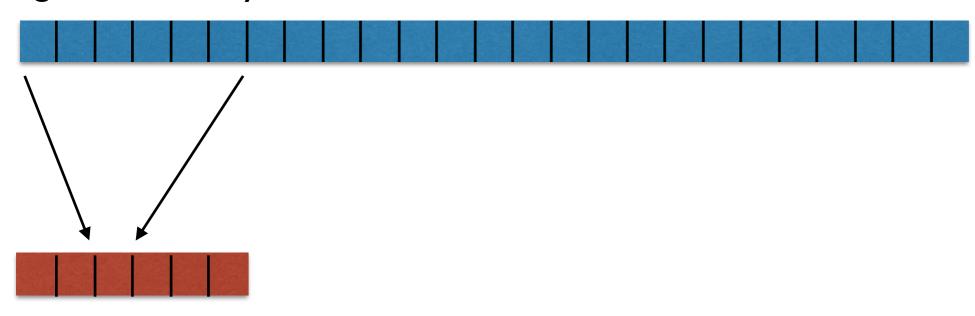


all threads in a warp read subset of position data into shared memory

now, in our inner loop, read from shared memory

result: n reads from global memory, n² reads from shared memory

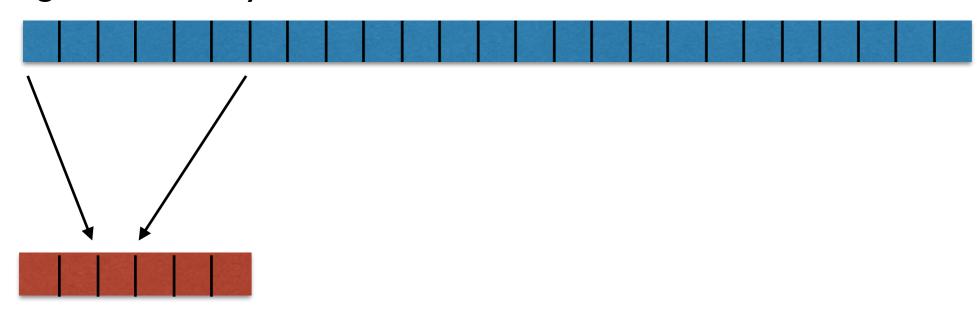
global memory



looping / indexing becomes more complicated, though, because we can't fit all n particles positions in shared memory

need to loop over chunks of data

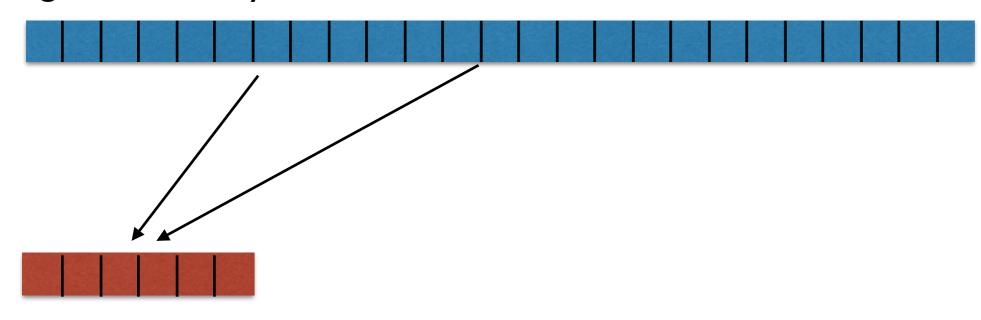
global memory



block I:

particle i sees particles 0-31 (if we have 32 threads per block)

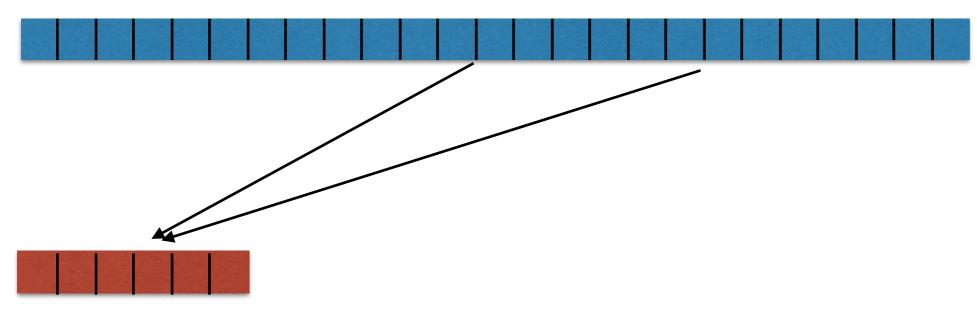
global memory



block 2:

particle i sees particles 32-63 (if we have 32 threads per block)

global memory



block 3:

particle i sees particles 64-95 (if we have 32 threads per block)

and so on!

code:

```
// evaluate forces on GPU, use shared memory
 \_global\_\_ void ForcesSharedMemory(int n, double * x, double * y, double * z,
              double * fx, double * fy, double * fz, double A12, double B6) {
   shared double xj[NUM THREADS];
    _shared__ double yj[NUM_THREADS];
    __shared__ double zj[NUM_THREADS];
                                                           shared memory
   int blockid = blockIdx.x*gridDim.y + blockIdx.y;
               = blockid*blockDim.x + threadIdx.x;
   int i
   double xi = 0.0;
   double yi = 0.0;
   double zi = 0.0;
    if ( i < n ) {</pre>
       xi = x[i];
                                      check index, but DO NOT RETURN
       yi = y[i];
                                      why? we need the extra threads to
       zi = z[i];
    }
                                      manage shared memory
   double fxi = 0.0;
   double fyi = 0.0;
   double fzi = 0.0;
```

```
code:
                                             make sure the next blockDim.x
int j = 0;
                                             particles have indices <= n
while( j + blockDim.x <= n ) {</pre>
    // load xj, yj, zj into shared memory
    xj[threadIdx.x] = x[j + threadIdx.x];
    yj[threadIdx.x] = y[j + threadIdx.x];
    zj[threadIdx.x] = z[j + threadIdx.x];
                                               put a barrier to make sure
    // synchronize threads
                                               all of the memory is loaded
    __syncthreads();
    for (int myj = 0; myj < blockDim.x; myj++) {</pre>
        double dx = xi - xj[myj];
                                                  loop over blockDim.x particles
        double dy = yi - yj[myj];
        double dz = zi - zj[myj];
                                       read from shared memory
```

shared memory

code:

}

trying to prevent branching (avoiding if i==j continue) ... not sure if this really helps

```
double r2 = dx*dx + dy*dy + dz*dz + 10000000.0 * ((j+myj)==i);
   double r6 = r2*r2*r2;
   double r8 = r6*r2;
   double r14 = r6*r6*r2;
   double f = A12 / r14 - B6 / r8;
   // slowest step
    fxi += dx * f;
   fyi += dy * f;
    fzi += dz * f;
// synchronize threads
__syncthreads();
j += blockDim.x;
```

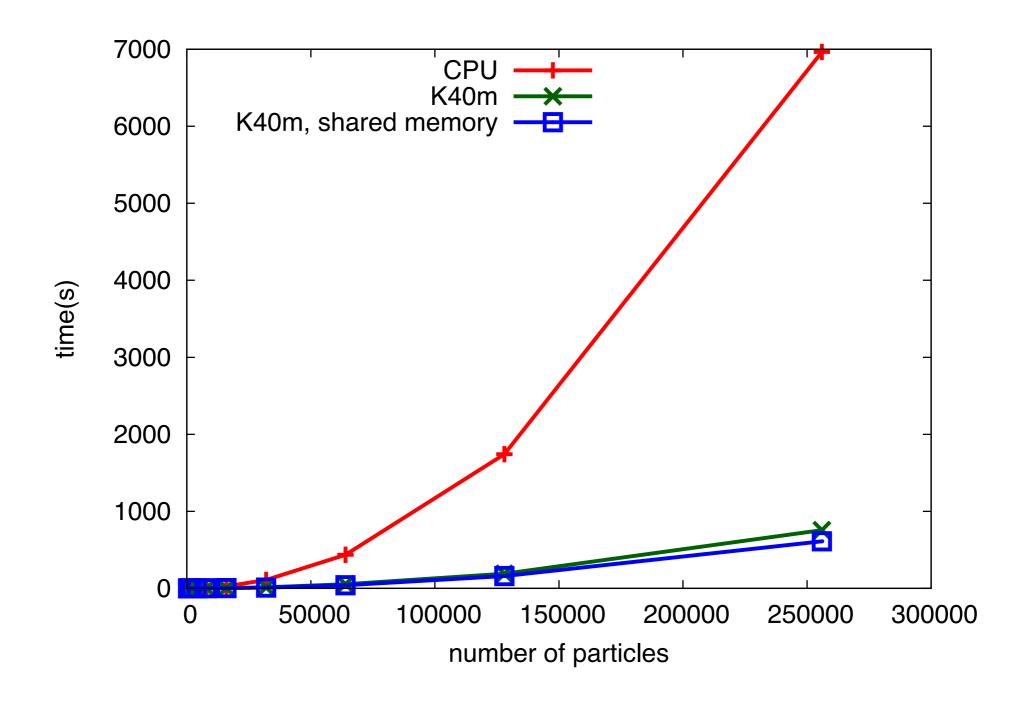
put another barrier before reading next chunk of positions

since n might not be an even multiple of blockDim.x, we might have some leftover work to do

```
int leftover = n - (n / blockDim.x) * blockDim.x;
// synchronize threads
__syncthreads();
                                         code looks the same, just different
                                         chunk size
// last bit
if ( threadIdx.x < leftover ) {</pre>
    // load rj into shared memory
    xj[threadIdx.x] = x[j + threadIdx.x];
    yj[threadIdx.x] = y[j + threadIdx.x];
    zj[threadIdx.x] = z[j + threadIdx.x];
// synchronize threads
__syncthreads();
for (int myj = 0; myj < leftover; myj++) {</pre>
    double dx = xi - xj[myj];
    double dy = yi - yj[myj];
    double dz = zi - zj[myj];
```

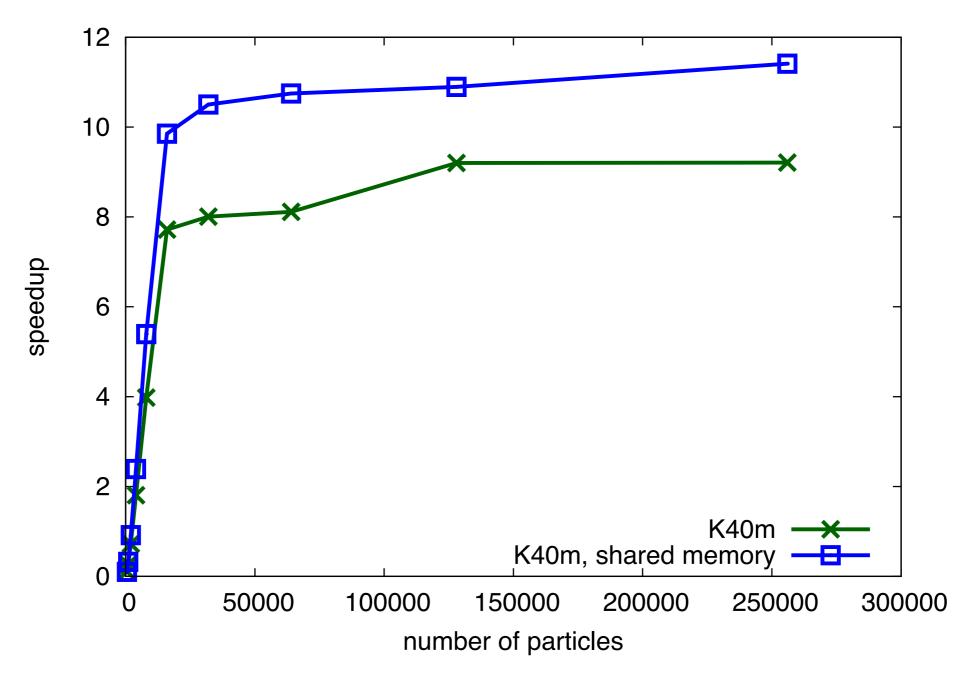
shared memory

so, how'd we do?



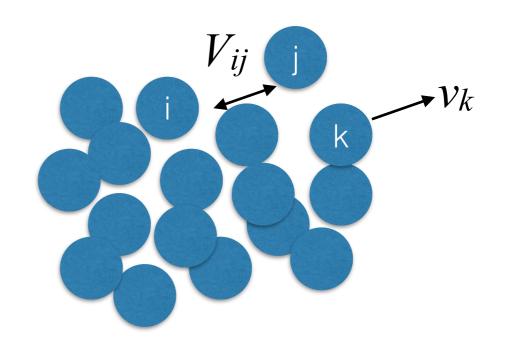
shared memory

so, how'd we do?



for this problem, on the K40m, using shared memory in this way gets us a 30% boost in performance. On m2050, boost is much more modest.

n lennard-jones particles moving in 3-d with periodic boundary conditions



velocity verlet (https://en.wikipedia.org/wiki/Verlet_integration#Velocity_Verlet)

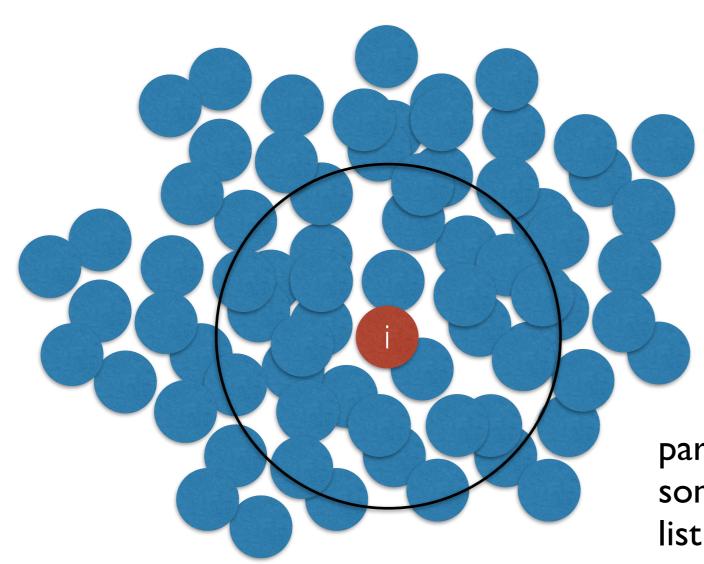
start with some initial positions, velocities

$$v(t+dt) = v(t) + \frac{1}{2}[a(t) + a(t+dt))dt$$

molecular dynamics - extra details

catch - acceleration naively scales as n²

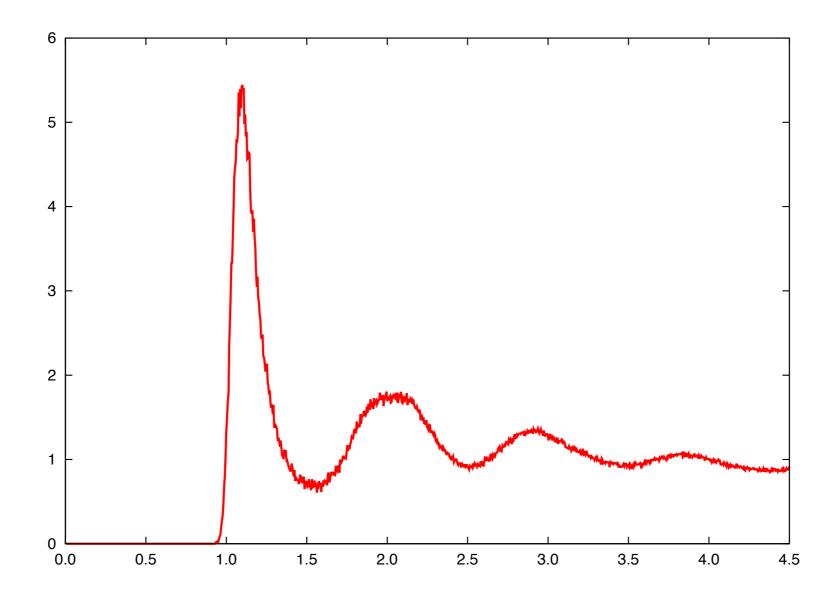
solution - use neighbor lists:



particle i only sees particles j within some radius. computing the neighbor list is expensive, so we only update it every once in a while

molecular dynamics - extra details

analysis: pair correlation function

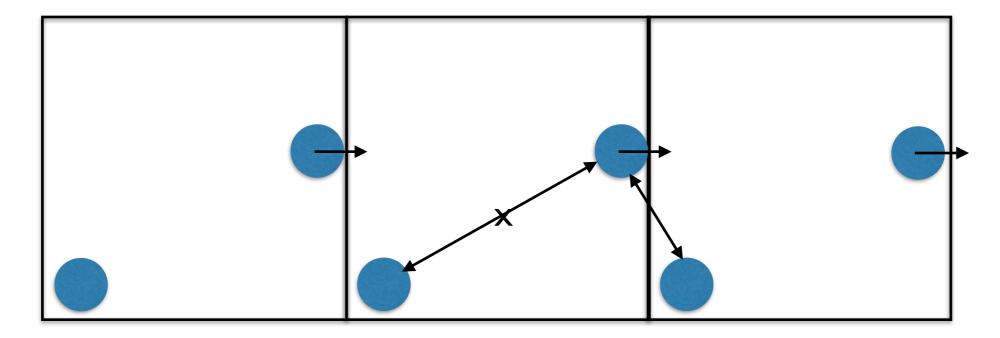


basically, check distances between all pairs of particles and bin them

molecular dynamics - extra details

periodic boundary conditions and minimum image convention

if particle leaves box, put it back in on other side



particles interact with only one "image" of other particles the one that is closest to them

cpu code

S2I2/gpu/md_simulation/cpu/md_cpu.cc

gpu code

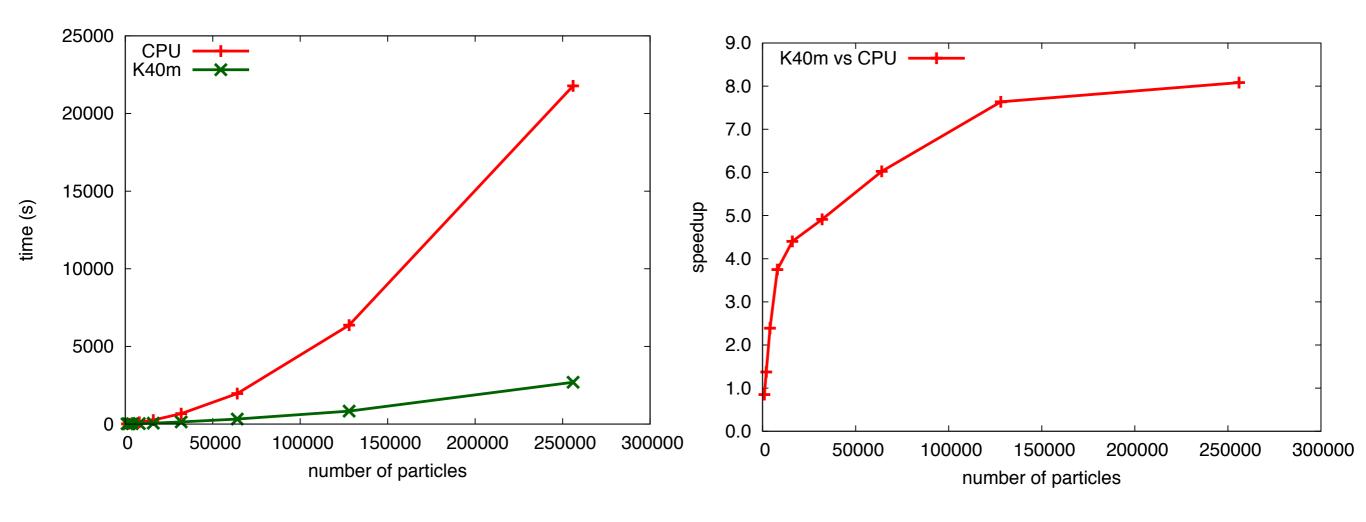
S2I2/gpu/md_simulation/gpu/md_gpu.cc

start modifying the CPU code to run on GPUs ... and ... go!

how did we do?

blue ridge: 2 x 8-core Intel Sandy bridge CPU vs NVIDIA K40m GPU

timings are for 10000 time steps



how did we do?

hokie speed: 2 x 6-core Intel Xeon CPU vs NVIDIA m2050 GPU

timings are for 10000 time steps

