

DATA REGISTER ( IOBASE + 0 )

```

7 6 5 4 3 2 1 0
* . . . . . D7 (pin 9), 1=High, 0=Low
. * . . . . . D6 (pin 8), 1=High, 0=Low
. . * . . . . D5 (pin 7), 1=High, 0=Low
. . . * . . . D4 (pin 6), 1=High, 0=Low
. . . . * . . D3 (pin 5), 1=High, 0=Low
. . . . . * . D2 (pin 4), 1=High, 0=Low
. . . . . * . D1 (pin 3), 1=High, 0=Low

```

STATUS REGISTER ( IOBASE + 1 )

```

7 6 5 4 3 2 1 0
* . . . . . Busy . . (pin 11), high=0, low=1 (inverted)
. * . . . . . Ack . . (pin 10), high=1, low=0 (true)
. . * . . . . No paper (pin 12), high=1, low=0 (true)
. . . * . . . Selected (pin 13), high=1, low=0 (true)
. . . . * . . Error . . (pin 15), high=1, low=0 (true)
. . . . . * * * Undefined

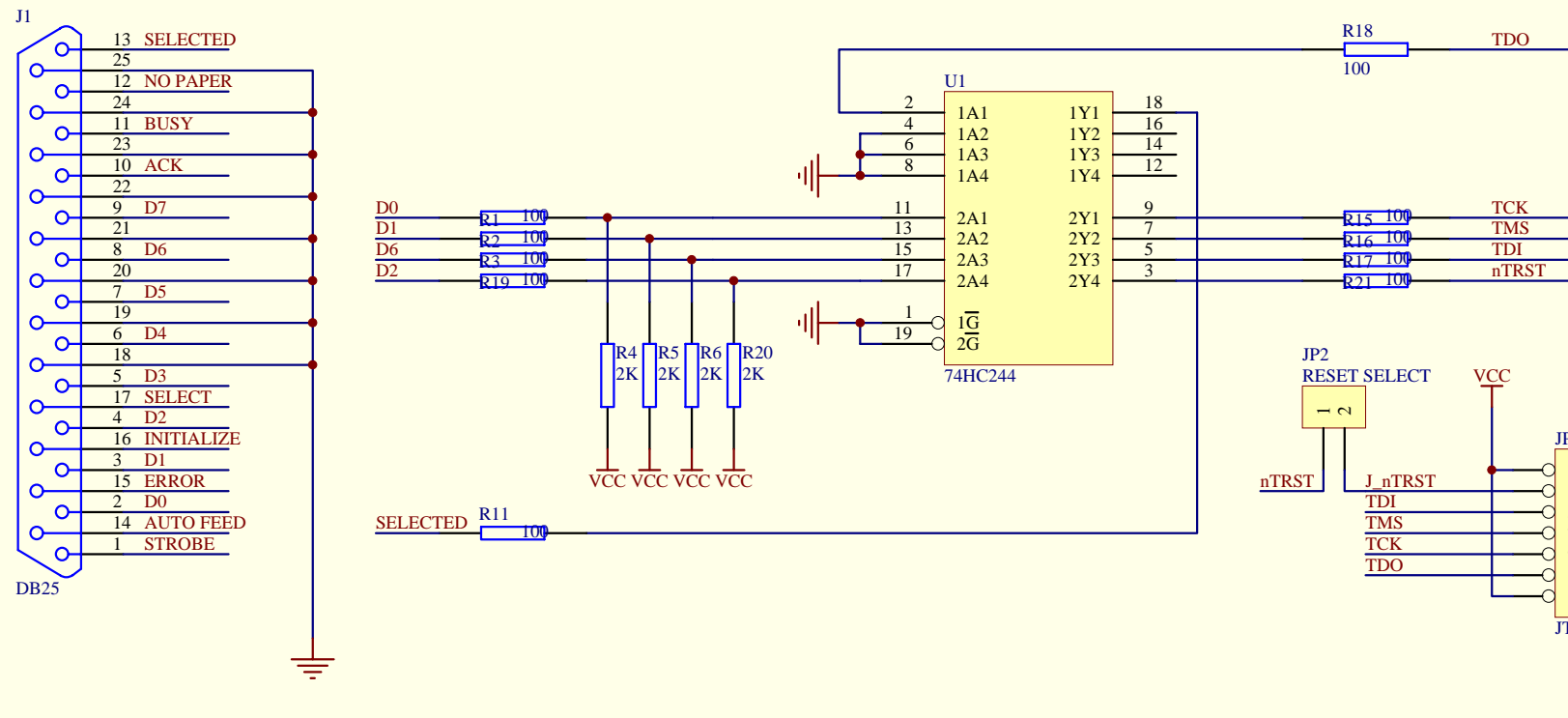
```

CONTROL REGISTER ( IOBASE + 2 )

```

7 6 5 4 3 2 1 0
* * . . . . . Unused (undefined on read, ignored on write)
. . * . . . . Bidirectional enable on PS/2 ports, see below
. . . * . . . Interrupt control, 1=enable, 0=disable
. . . . * . . Select . . (pin 17), 1=low, 0=high (inverted)
. . . . . * . Initialize (pin 16), 1=high, 0=low (true)
. . . . . * . Auto Feed (pin 14), 1=low, 0=high (inverted)
. . . . . * . Strobe . . (pin 1), 1=low, 0=high (inverted)

```



Most important:

When you use parallel lines to connect the JTAG debugger with your demo board, please let the lines as short as possible.

For more information, please visit:

<http://www.cfly.org>
<http://wangyunfei.yeah.net>

Usage of JP2:

To select whether you want to use nTRST:

connect = use it

not connect = not use nTRST but use software to reset JTAG

For Jtag.exe, please don't connect it!!!

Title		
JTAG debugger ( Embedded ICE + PC Parallel )		
Size	Number	Revision
A	1	5.2.0
Date:	8-Oct-2001	Sheet of 1
File:	C:\My Documents\arm\EmbeddedICE\sch\pinning\Jtag.ddb	Cfly