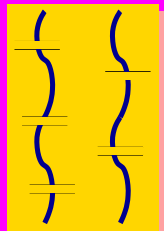
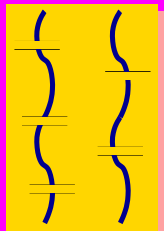


L1

L1

L2



L1

L1

L2

Bus