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	Aditya Balu     Sergio Botelho     Biswajit Khara     Vinay V Rao     Chinmay Hegde     Soumik Sarkar     Santi Adavani		authors	Aditya Balu     Sergio Botelho     Biswajit Khara     Vinay Rao     Chinmay Hegde     Soumik Sarkar     Santi Adavani     Adarsh Krishnamurthy     Baskar Ganapathysubramanian		
		<ul><li>Adarsh Krishnamurthy</li><li>Baskar Ganapathysubramanian</li></ul>	title	Distributed Multigrid Neural Solvers on Megavoxel Domains  2021-04-29 17:53:22+00:00		
		Baskai Ganapaunysuoramaman	source	SupportedSources.ARXIV		
	title	Distributed Multigrid Neural Solvers on	journal	None None		
	Megavoxel Domains  publication date 2021-04-29 00:00:00		volume		DUPLICATE	ES  219
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	journal	arXiv (Cornell University)		<ul> <li>http://arxiv.org/pdf/2104.14538v1</li> <li>http://arxiv.org/abs/2104.14538v1</li> <li>http://arxiv.org/pdf/2104.14538v1</li> </ul>		
	volume		urls			
	doi	None	<u> </u>			
	urls	https://openalex.org/W3157360434	id	id105848817719652782		
	id	id3678391576514845732	abstract	We consider the distributed training of large-scale neural networks that serve as PDE solvers producing full field outputs. We specifically consider neural solvers for the generalized 3D Poisson equation over megavoxel domains. A scalable framework is presented that integrates two distinct advances. First, we accelerate training a large		
	abstract			model via a method analogous to the multigrid technique used in numerical linear algebra. Here, the network is trained using a hierarchy of increasing resolution inputs in sequence, analogous to the 'V', 'W', 'F', and 'Half-V' cycles used in multigrid approaches. In conjunction with the multi-grid approach, we implement a distributed deep learning framework which significantly reduces the time to solve. We show the scalability of this approach on both GPU (Azure VMs on Cloud) and CPU clusters (PSC		
	versions					
				Bridges2). This approach is deployed to train a generalized 3D Poisson solver that scales well to predict output full-field solutions up to the resolution of 512x512x512 for a high dimensional family of inputs.		
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