	doc_1		doc_2		decision	id
cases		authors Aaron R. Voelker		Aaron Voelker Ivana Kajic Chris Eliasmith		
	authors	Ivana Kajic	title	Legendre Memory Units: Continuous-Time Representation in Recurrent Neural Networks		
		Chris Eliasmith	publication_date	2019-01-01 00:00:00		
			source	SupportedSources.INTERNET_ARCHIVE		
	title	Legendre Memory Units: Continuous-Time Representation in Recurrent Neural Networks	journal			
	publication date 2019-09-06 00:00:00		volume			
	source	SupportedSources.OPENALEX	doi			i
	journal	Neural Information Processing Systems	urls	• https://web.archive.org/web/20220308032320/https://proceedings.neurips.cc/paper/2019/file/952285b9b7e7a1be5aa7849f32ffff05-Paper.pdf	DUPLICATES 29	.96
	volume	32	id	id3706357230768942915		
	doi	None	abstract (	We propose a novel memory cell for recurrent neural networks that dynamically maintains information across long windows of time using relatively few resources. The		
	urls	https://openalex.org/W2970783931		Legendre Memory Unit (LMU) is mathematically derived to orthogonalize its continuous-time history -doing so by solving d coupled ordinary differential equations (ODEs), whose phase space linearly maps onto sliding windows of time via the Legendre polynomials up to degree d â ' 1. Backpropagation across LMUs outperforms equivalently-		
	id	id-2087147303269411797		sized LSTMs on a chaotic time-series prediction task, improves memory capacity by two orders of magnitude, and significantly reduces training and inference times. LMUs		
	abstract			can efficiently handle temporal dependencies spanning 100,000 time-steps, converge rapidly, and use few internal state-variables to learn complex functions spanning long windows of time -exceeding state-of-the-art performance among RNNs on permuted sequential MNIST. These results are due to the network's disposition to learn scale-		
	versions			invariant features independently of step size. Backpropagation through the ODE solver allows each layer to adapt its internal time-step, enabling the network to learn task-		
				relevant time-scales. We demonstrate that LMU memory cells can be implemented using m recurrently-connected Poisson spiking neurons, O(m) time and memory, with error scaling as O(d/ â^š m). We discuss implementations of LMUs on analog and digital neuromorphic hardware.		
			versions			