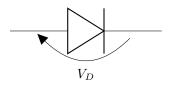
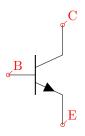
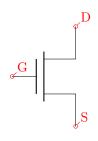
DIODO



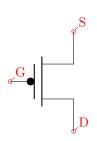
BJT



MOSFET N-MOS



MOSFET P-MOS



$$\begin{cases} I_D = 0 & \text{per } V_D < V_{\gamma} \\ V_D = V_{\gamma} & \text{per } I_D > 0 \end{cases}$$

• OFF

$$I_B = I_C = I_E = 0$$
 $\text{per}V_{BE} < V_{\gamma}$

Trasferisce uno 0 forte: $V_L=0V$

• SATURAZIONE

$$I_D = \frac{\beta}{2} (V_{GS} - V_{Tn})^2 \quad \text{per} \begin{cases} V_{DS} \ge V_{GS} - V_{Tn} \\ V_{GS} \ge V_{Tn} \end{cases}$$

• LINEARITÀ

$$I_D = \beta \left((V_{GS} - V_T)V_{DS} - \frac{V_{DS}^2}{2} \right) \quad \text{per} \begin{cases} V_{DS} < V_{GS} - V_{Tn} \\ V_{GS} \ge V_{Tn} \end{cases}$$

Trasferisce 1 forte $V_H = V_{CC}$

• SATURAZIONE:

$$I_{SD} = \frac{\beta}{2} (V_{SG} - |V_{TP}|)^2$$
 per $V_{SD} \ge V_{SG} - |V_{TP}|$

• LINEARITÀ:

$$I_{SD} = \beta \left((V_{SG} - |V_{TP}|)V_{SD} - \frac{V_{SN}^2}{2} \right) \qquad \text{per} \begin{cases} V_{SD} < V_{SG} - |V_{TP}| \\ V_{SG} \ge |V_T P| \end{cases}$$