

Pinout Table:

D5	1	RxD2/CCP1/ADC0/P1.0	P3.7/INT3/TxD_2/CCP2/CCP2_2CMP+	16	D4
D6	2	TxD2/CCP0/ADC1/P1.1	P3.6/ADC14/INT2/RxD_2/CCP1_2/CMP	15	D3
D7	3	XTALO/MCLKO_2/RxD_3/ADC6/P1.6	P3.5/ADC13/T1/T0CLKO/CCP0_2/SS_4	14	D2
S0	4	XTALI/TxD_3/ADC7/P1.7	P3.4/ADC12/T0/T1CLKO/EC1_2/CMP0/MOSI_4	13	D1
	5	MCLKO/RST/P5.4	P3.3/ADC11/INT1/MISO_4/I2CSDA_4	12	SDA
3V3	6	Vcc/AVcc/ADC_VRef+	P3.2/ADC10/INT0/SCLK_4/I2CSCL_4	11	SCLK
	7	P5.5	P3.1/ADC9/TxD	10	S1
	8	Gnd/A_Gnd	P3.0/ADC8/RxD/INT4	9	S2
					S3
					S4