LOD:gen\_blk1.l LOD:gen\_blk1.l LOD:gen\_blk1.l out 1:0 in[1..0] out[0..0] out~[1..0] out~[2..0] in[7..0] 3:0 in[3..0] out[2..0] out[1..0] WideOr0 vld LOD:gen\_blk1.h vld vld vld out 3:2 in[1..0] out[0..0] WideOr0 vld LOD:gen\_blk1.h LOD:gen\_blk1.l out 1:0 in[1..0] out[0..0] out~[1..0] 7:4 in[3..0] out[1..0] WideOr0 LOD:gen\_blk1.h vld out 3:2 in[1..0] out[0..0] WideOr0