

A

A

B

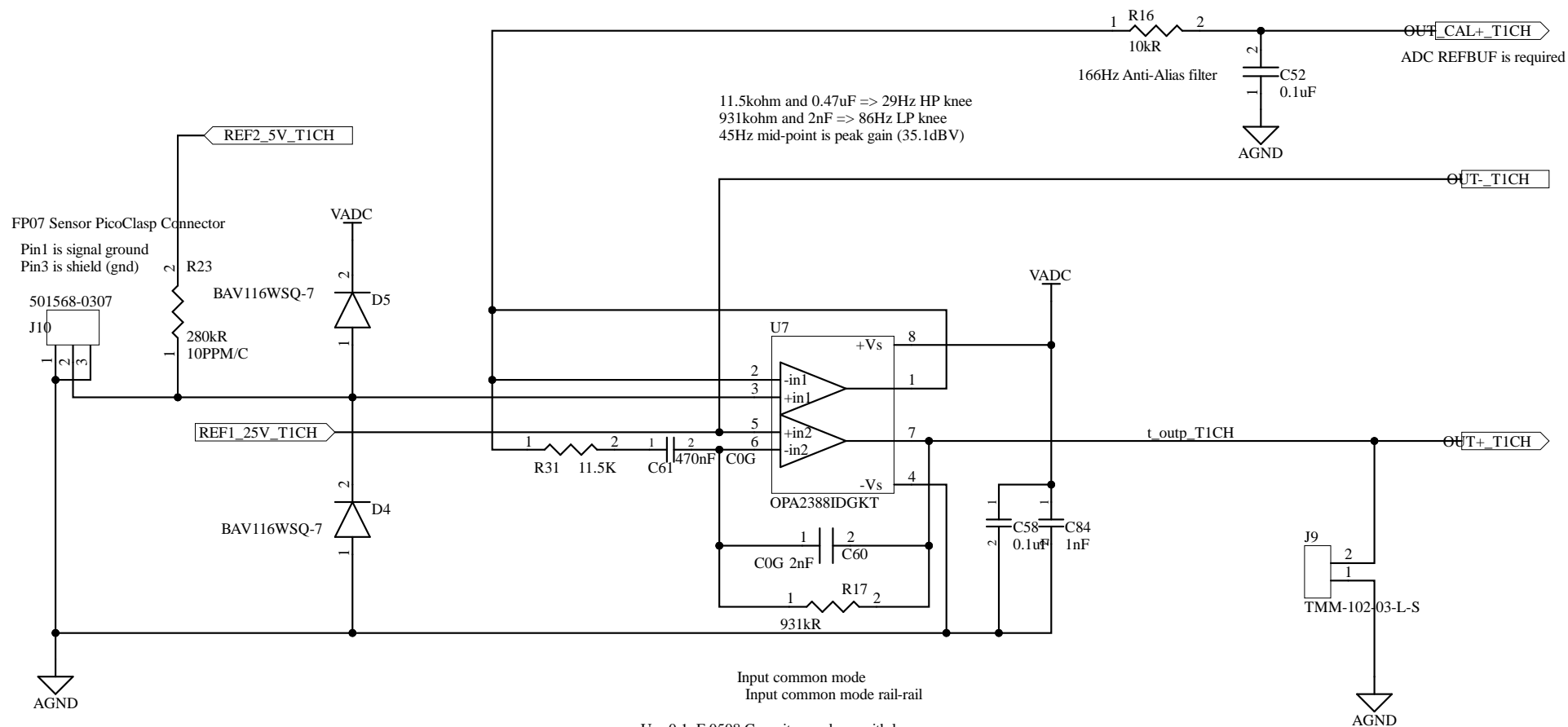
B

C

C

D

D



PicoClasp header 501940 mates with 501939 series plugs
PicoClasp Terminal 5011933000 (gold) is compatible with 501939
Pre-Crimped OTS Cable Assy 797581016 uses 5011933000 & 28AWG cable

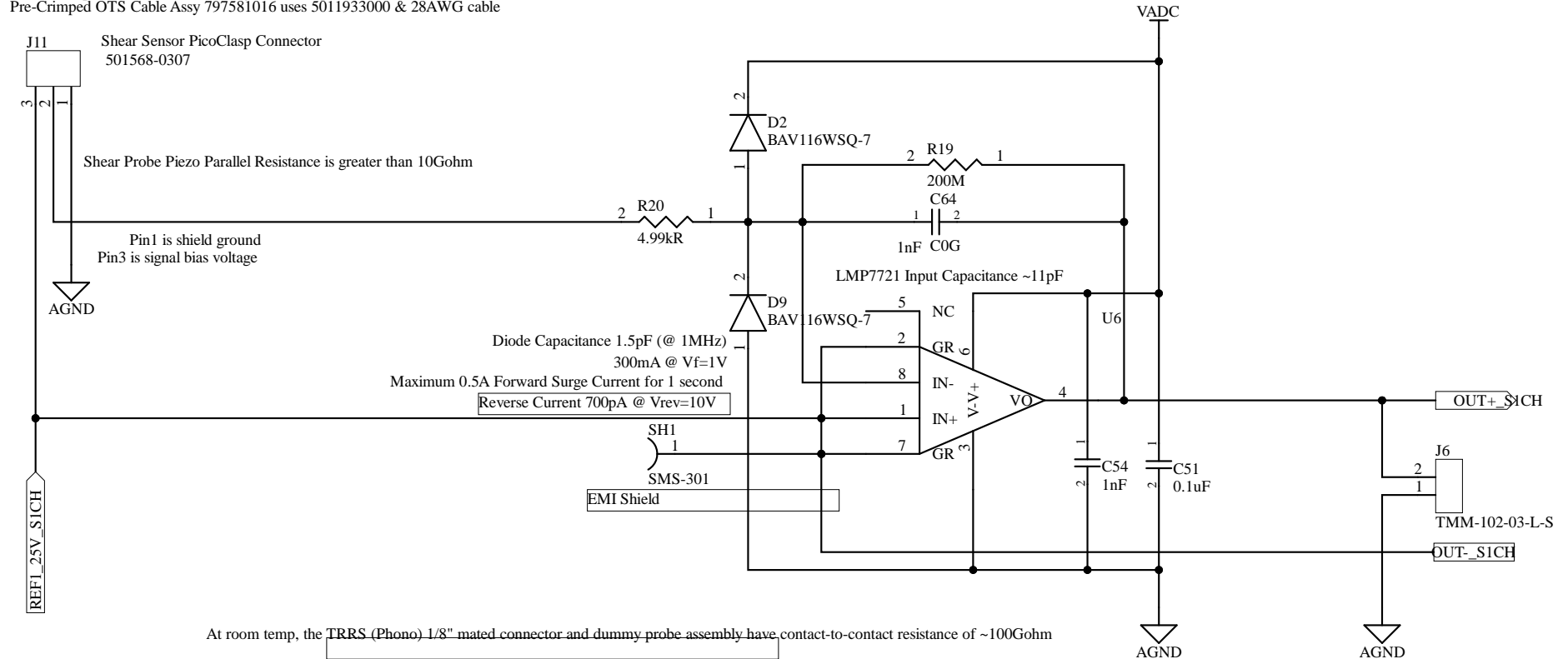
Use 0.1uF 0508 Capacitor package with lower inductance per ada4805 datasheet page 23

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A		
Date:	7/07/2020	Sheet of
File:	Y:\Desktop\..\EFE_temp.SchDoc	Drawn By:

As the piezo produces charge, a voltage is applied to the inverting input line
 The line (and piezo) is held to a virtual ground potential by the charge amp feedback
 At low frequency, the equal reactance of the piezo and feedback cap increase and keep unity gain,
 but the frequency independent resistance in the feedback path cause a low frequency cutoff
 The source resistor causes a similar effect with high frequency cutoff

Feedback Cap and Source Cap form Cs/Cf gain
 Historically, gain has been unity
 Feedback Cap/Resistor set HP Knee
 Source Resistor/Cap form LP Knee

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A

A

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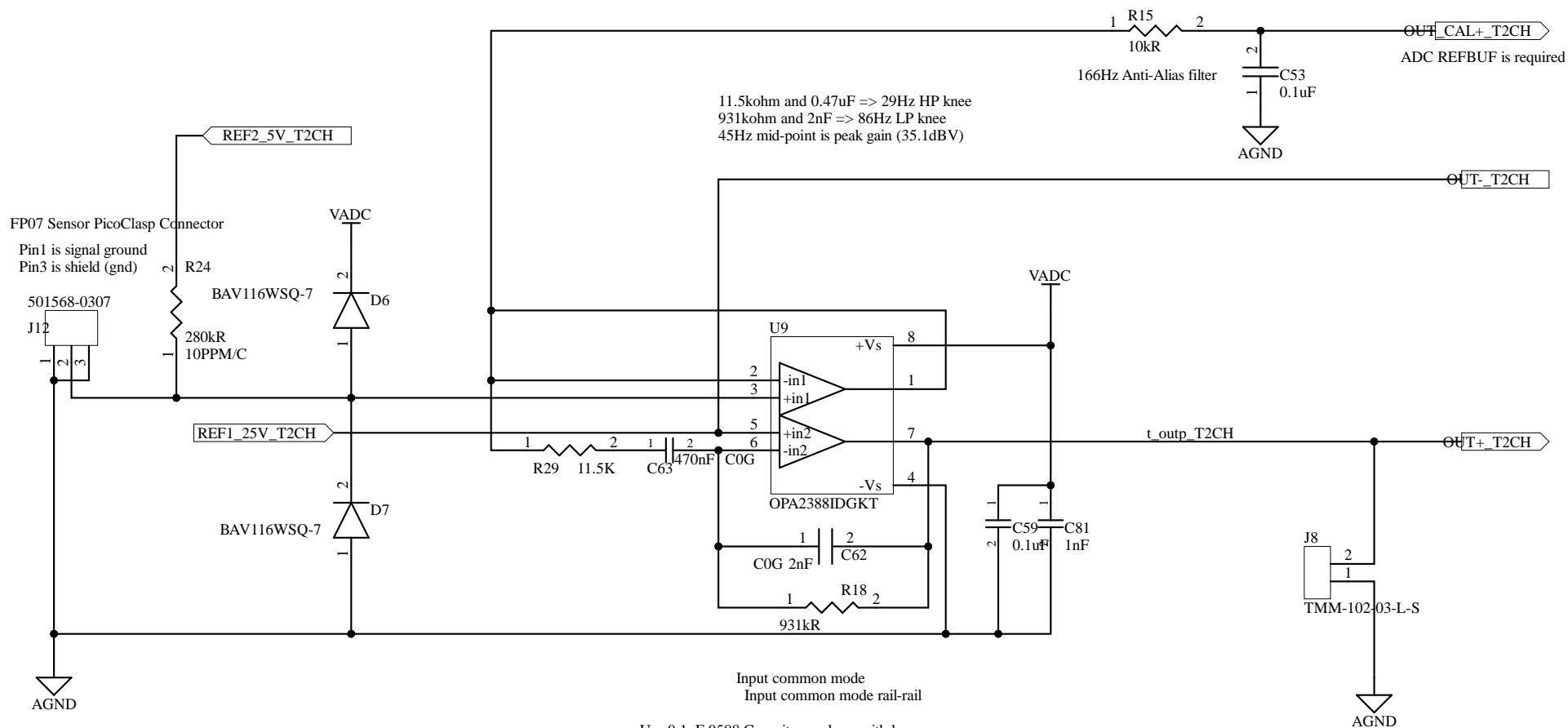
B

C

C

D

D



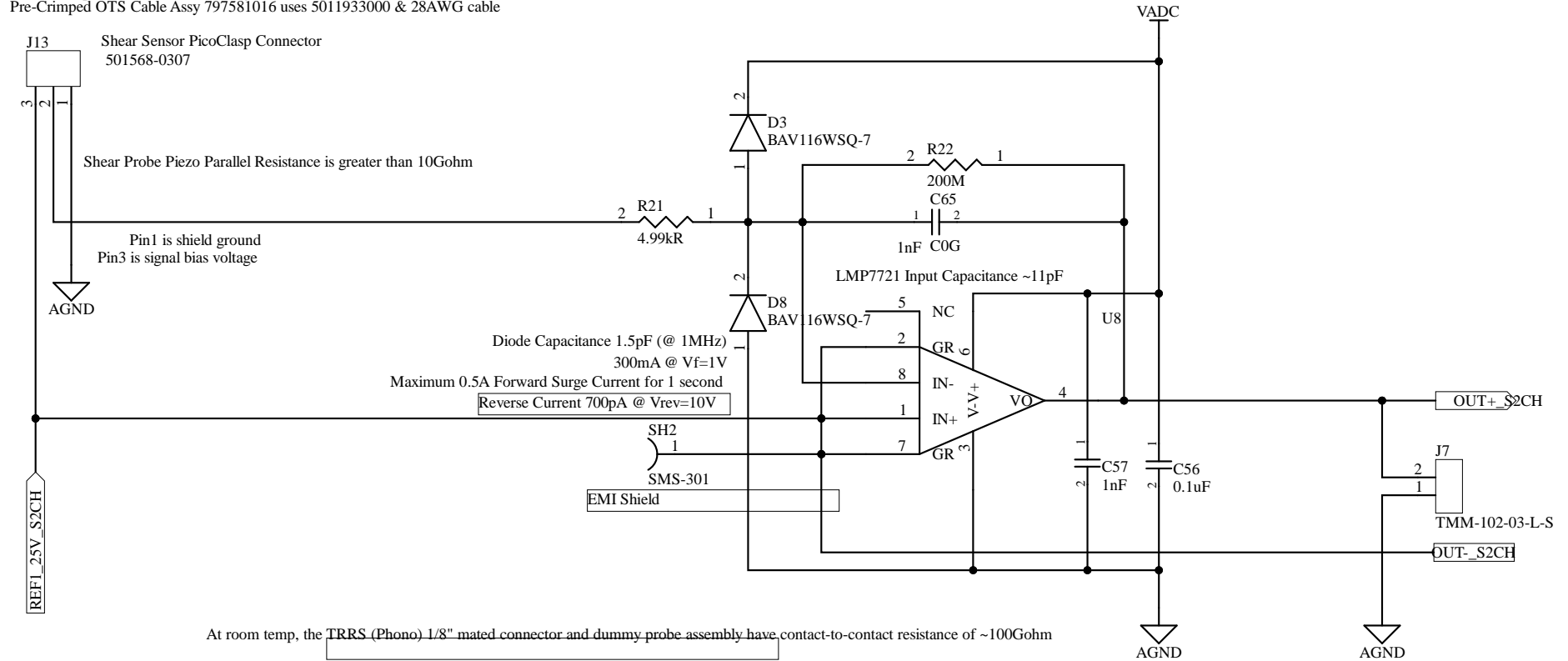
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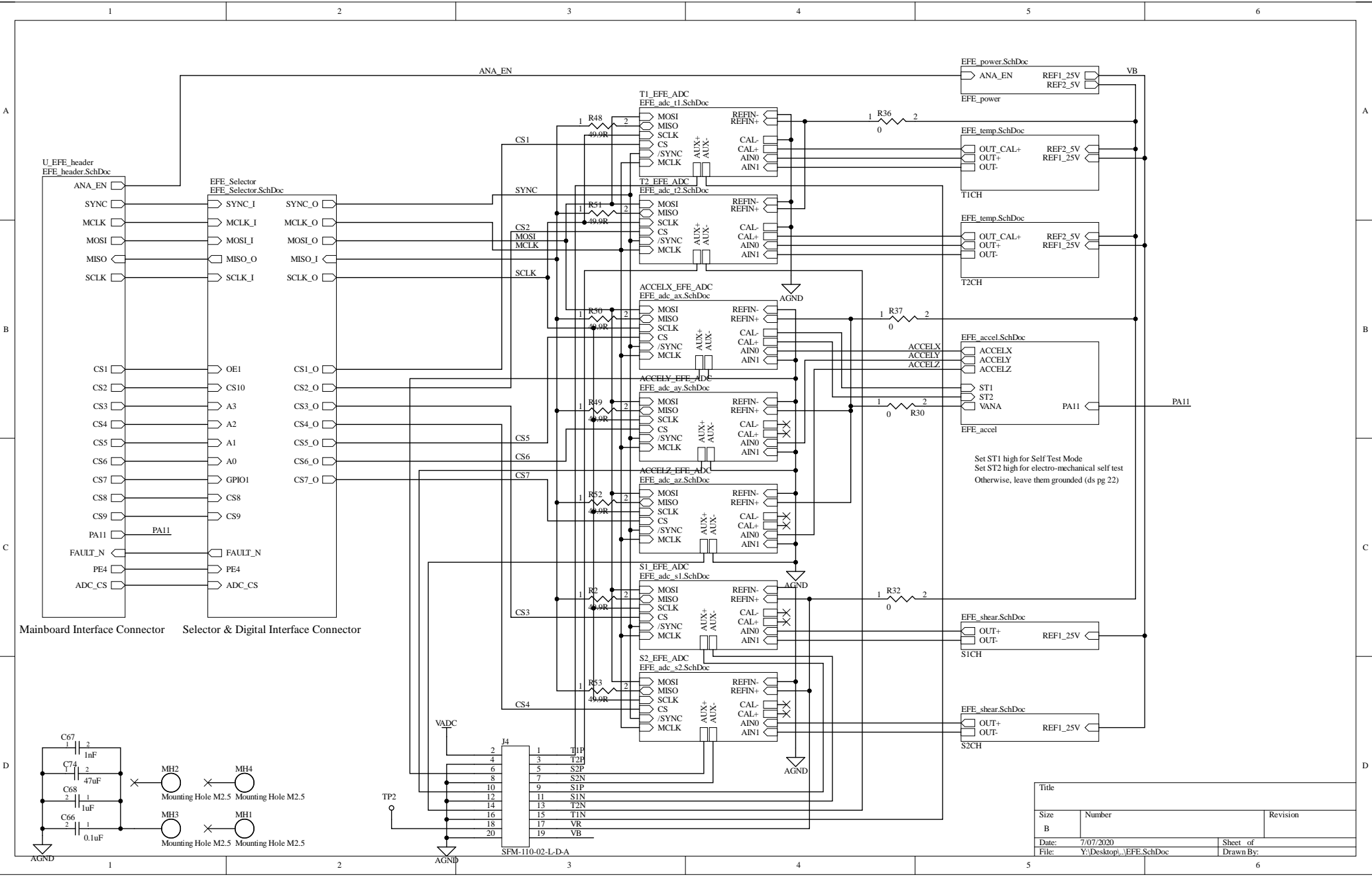
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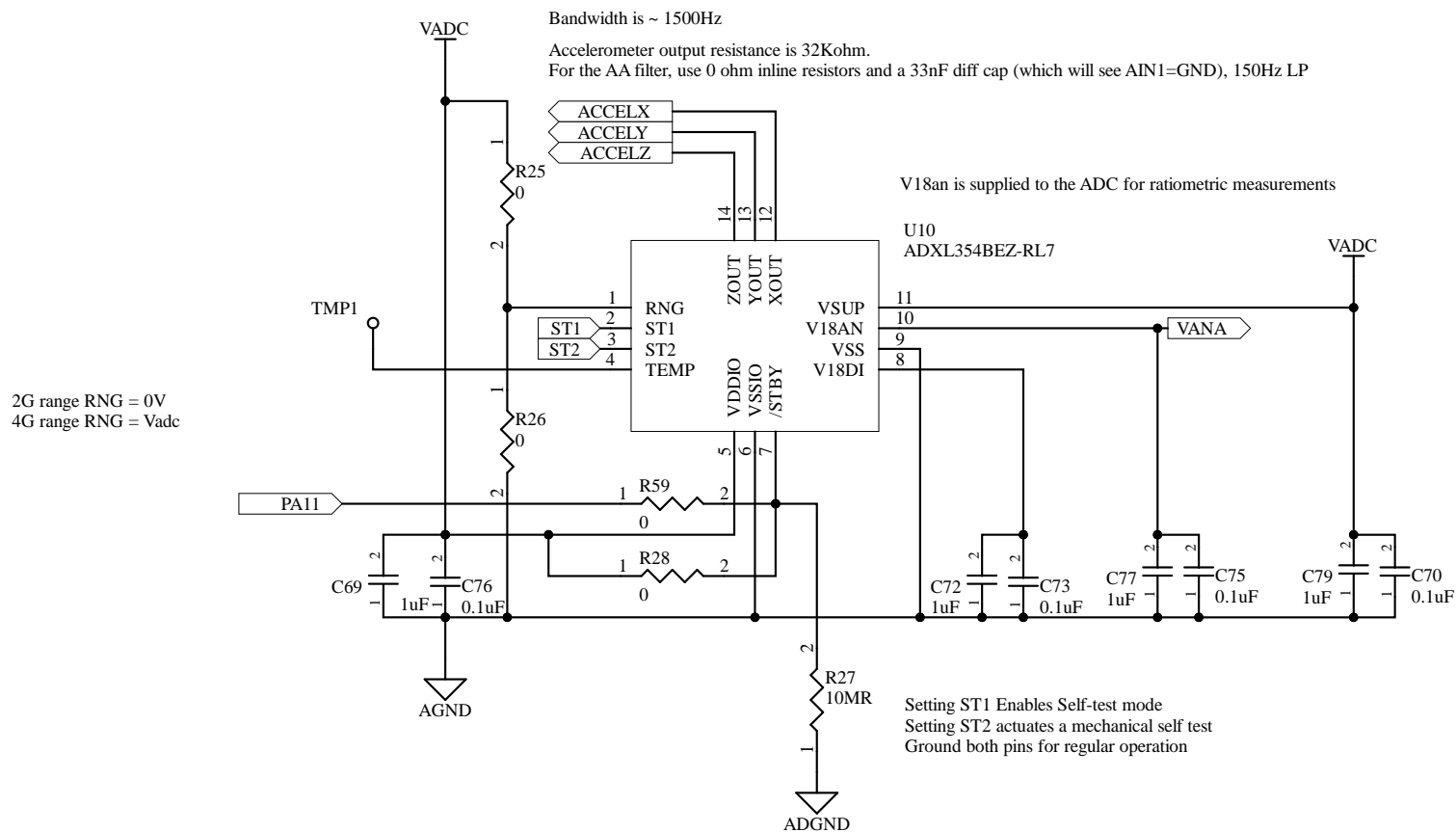
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Title		
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Title		
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A		
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File:	Y:\Desktop\..\EFE_accel.SchDoc	Drawn By:

A

A

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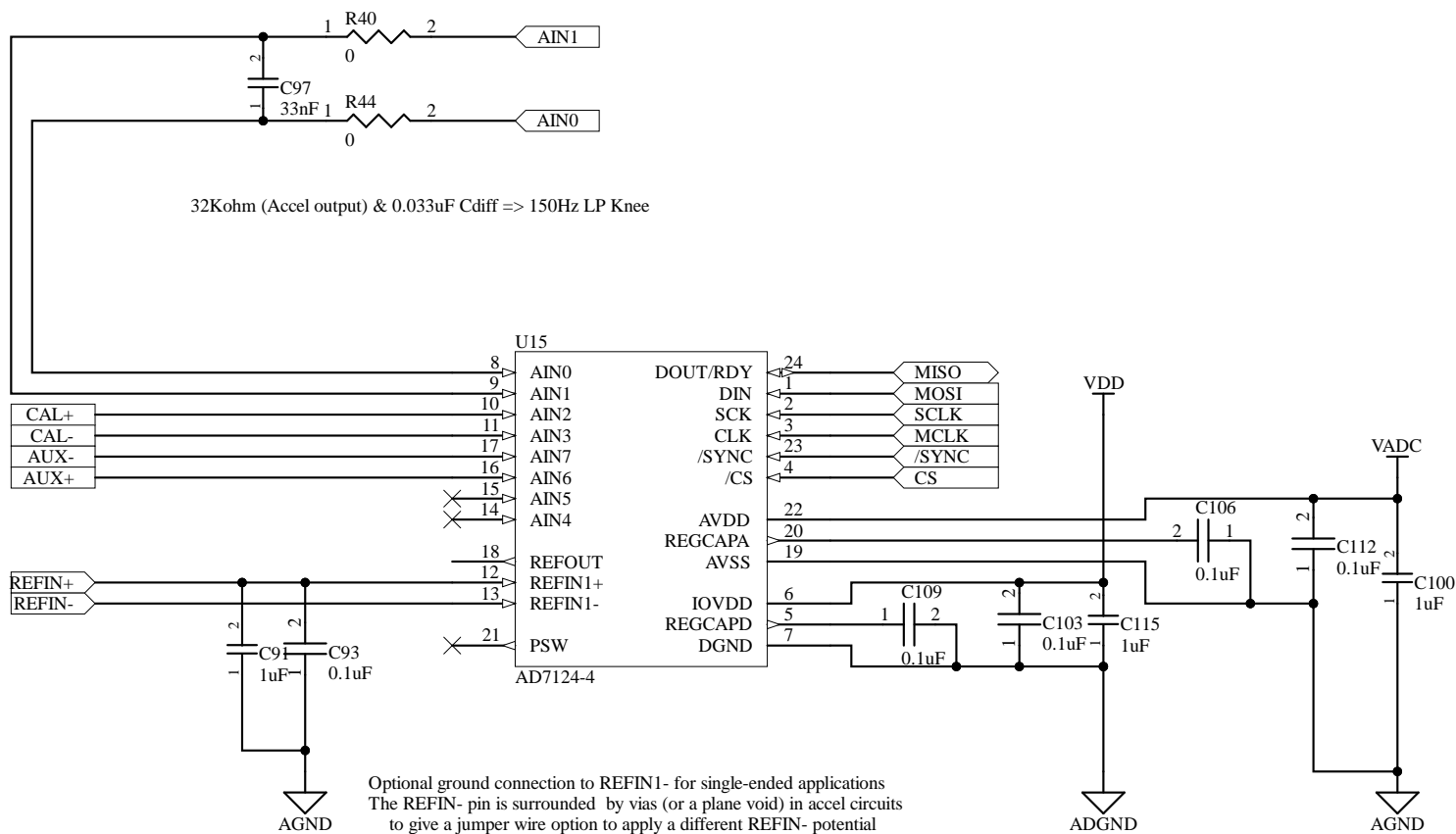
B

C

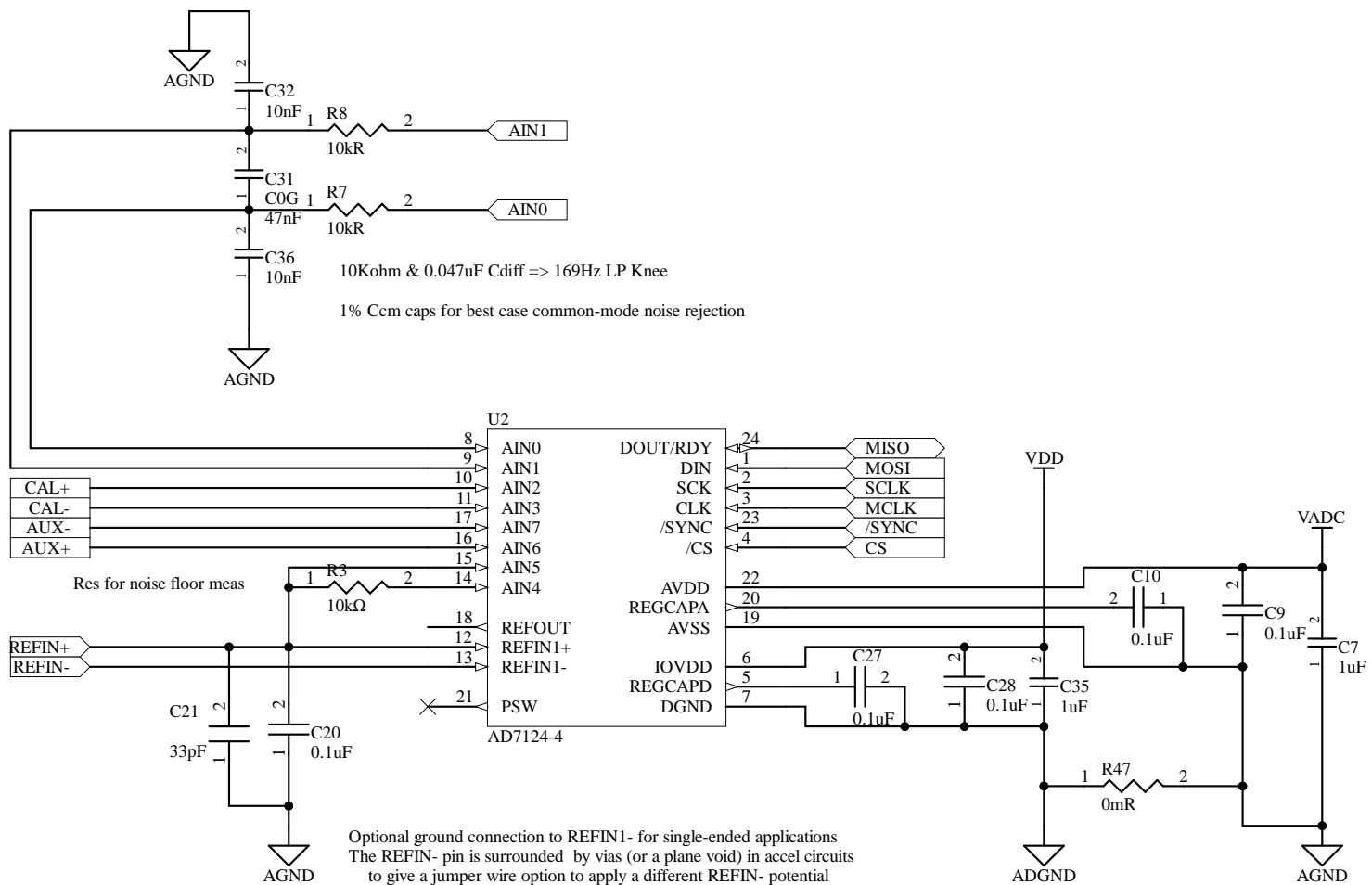
C

D

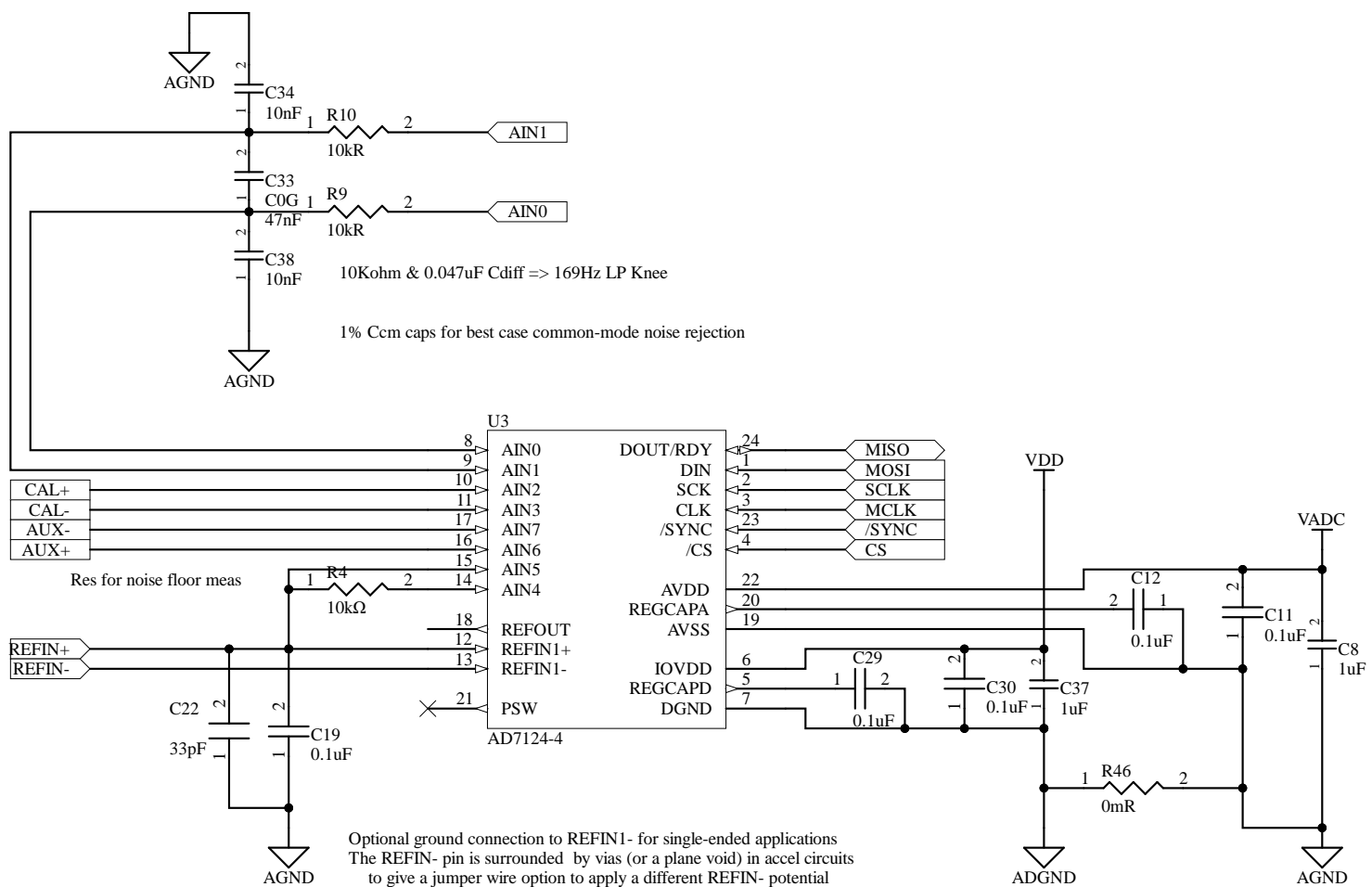
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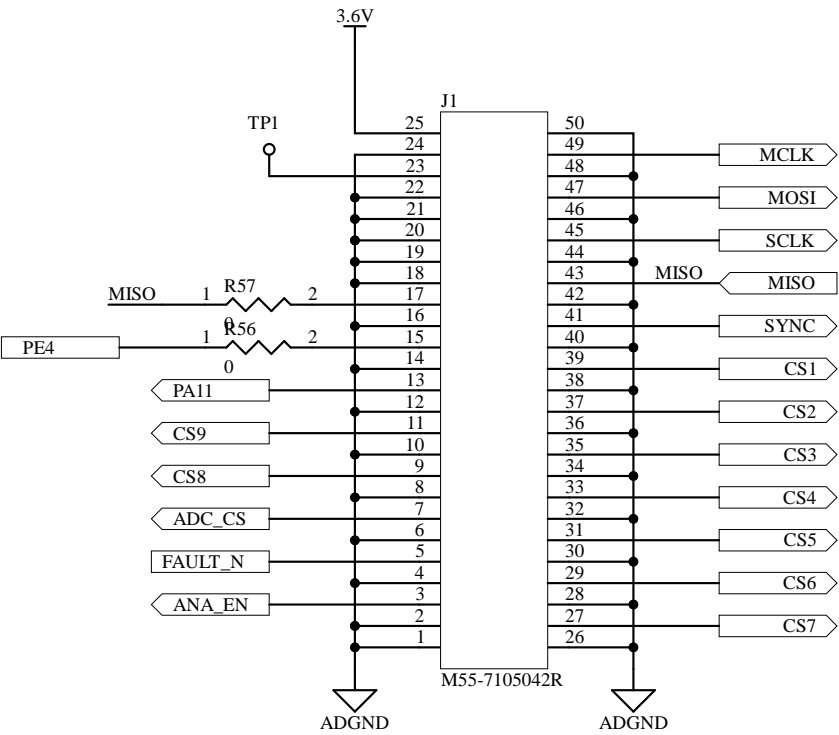
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File:	Y:\Desktop\..\EFE_adc.s1.SchDoc	Drawn By:



use 0ohm jumper to reduce stray capacitance noise between gnd regimes within the IC

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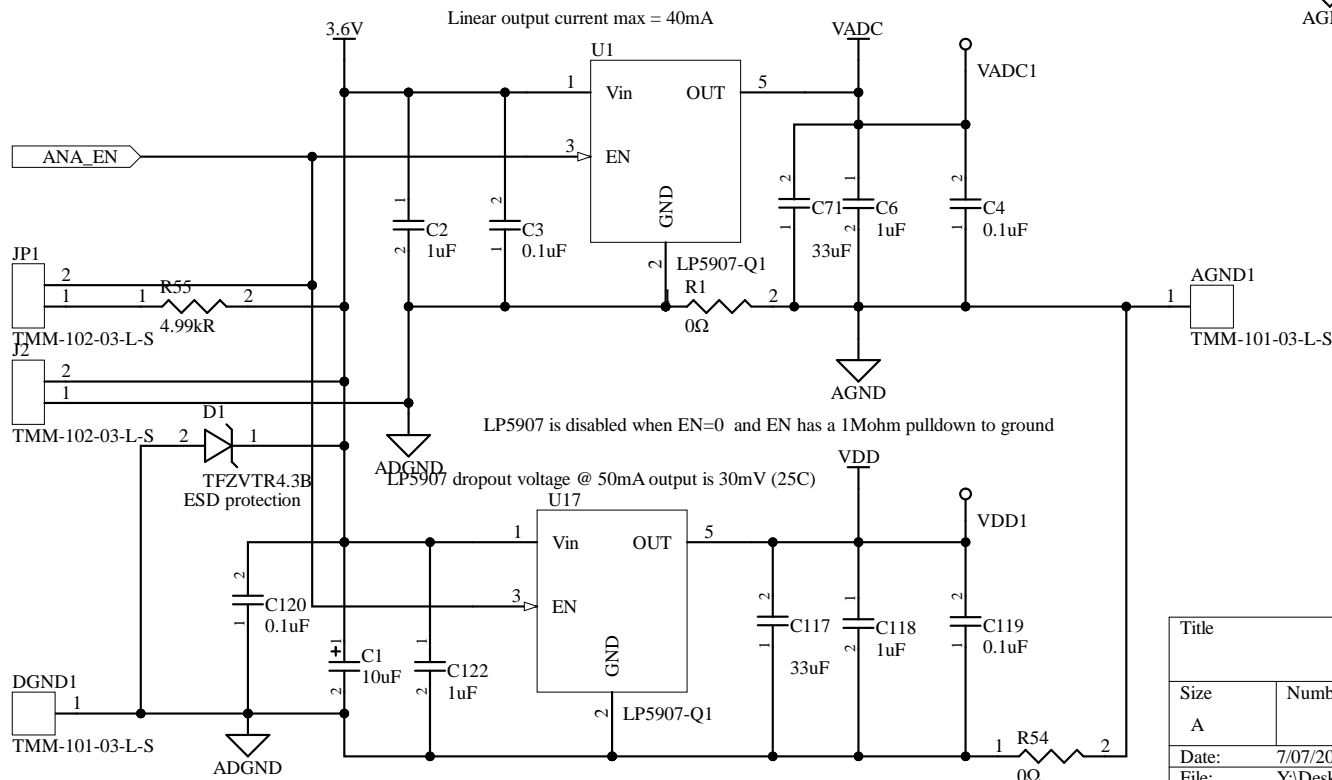
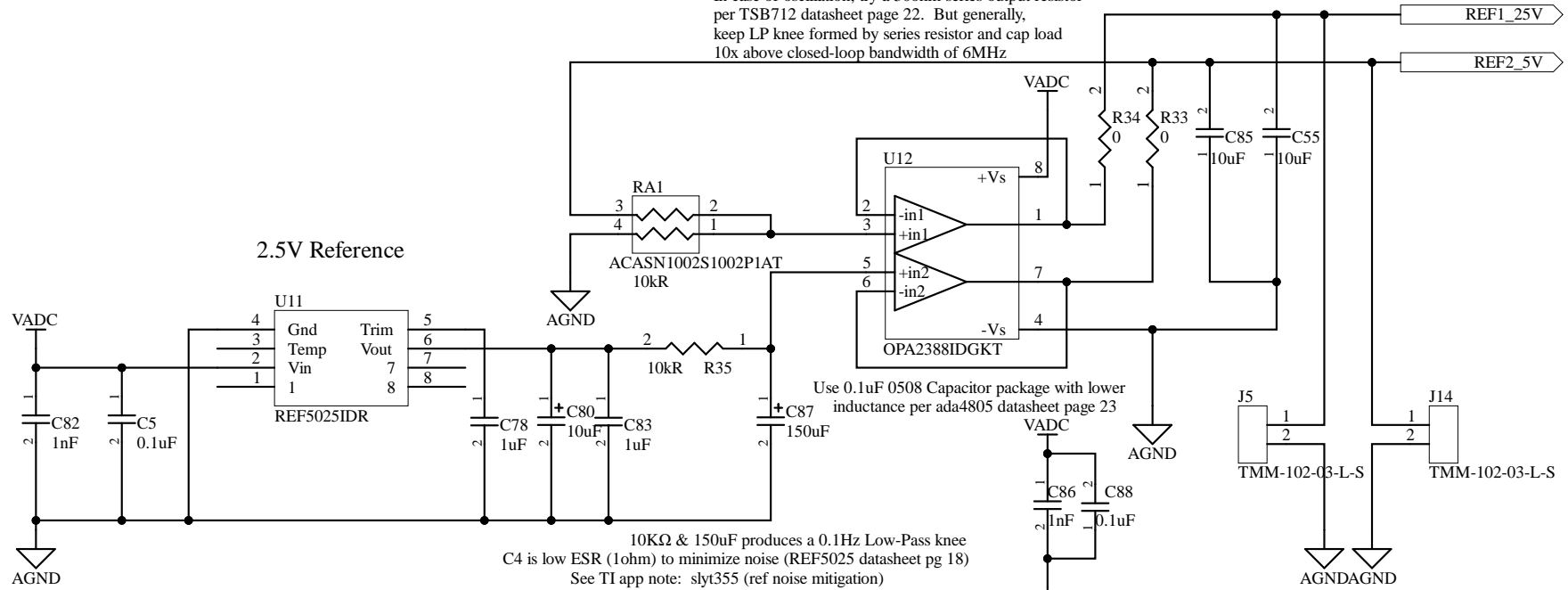
3.6BV is the lowest nominal voltage (when the input switching regulator is bypassed)



Right-Angle edge connector. Connects to SOM.

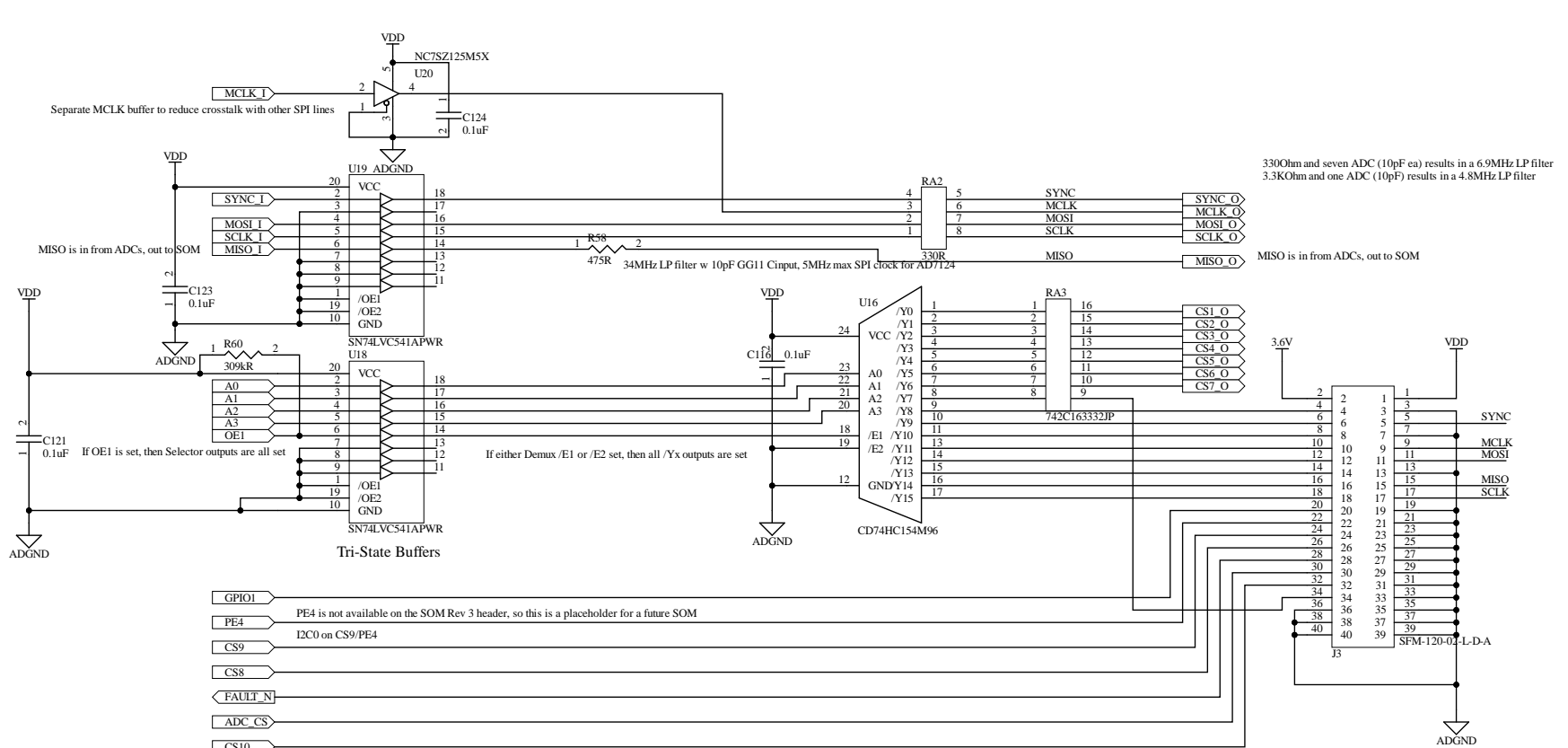
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In case of oscillation, try a 30ohm series output resistor per TSB712 datasheet page 22. But generally, keep LP knee formed by series resistor and cap load 10x above closed-loop bandwidth of 6MHz



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1 2 3 4 5 6



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1 2 3 4 5 6

