

Name

Previous Job Position

Yih sien Lu

Former Process Integration Engineer of tsmc

Education

National ChiaoTung University, Taiwan, 2007-2012

Doctor of Electrophysics, Electrophysics

- Majored in the FinFET like and Nanowire structure on Thin Film transistor
- 2011 Paper presentation, IEEE VLSI Technology and Circuits Symposium

Work Experience tsmc, Taiwan (6 years 5months)

**Principal Engineer for R&D Process Integration &
Principal Engineer for Advance Process Transfer
July 2012 –January 2019**

- Provided the capacitance process for customers to monitor electrical characteristics.
- Optimization of the process to meet the customer's SPEC of devices.
- Provided the SPICE model table for customers to demonstrate their circuit design.
- Establish the implantation tuning table to meet various Vth level of different customer's samples.
- Integrated and developed 28nm Embedded Flash for automotive application.
- Supported the new technology pilot program transferred to 2nd Fab by building key process in-line monitor table.
- Support 20nm advanced R&D platform Idsat Uniformity (IDU) Improvement project.

Extra Curriculum Activity

Sep/2002~Jun/2003 **National Chung Hsing University**

Executive: Department of Physics **Captain:** Basketball team

Benefits

- Establish excellent plan, control, organization and management skills
- Acquire strong leadership and optimize teamworkship.
- Obtain great people management and interpersonal skill
- Develop effective and efficient communication and negotiation capabilities
- Inspire interest in sports management