

# Implementing Improvements for Secure Function Evaluation

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A Thesis  
Presented to  
The Division of Mathematics and Natural Sciences  
Reed College

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# Acknowledgements

I want to thank a few people.



# Preface

This is an example of a thesis setup to use the reed thesis document class.





# List of Abbreviations

You can always change the way your abbreviations are formatted. Play around with it yourself, use tables, or come to CUS if you'd like to change the way it looks. You can also completely remove this chapter if you have no need for a list of abbreviations. Here is an example of what this could look like:

{AL-0: Do I need this?}	<b>ABC</b>	American Broadcasting Company
	<b>CBS</b>	Columbia Broadcasting System
	<b>CDC</b>	Center for Disease Control
	<b>CIA</b>	Central Intelligence Agency
	<b>CLBR</b>	Center for Life Beyond Reed
	<b>CUS</b>	Computer User Services
	<b>FBI</b>	Federal Bureau of Investigation
	<b>NBC</b>	National Broadcasting Corporation



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# Abstract

The preface pretty much says it all.



# Dedication

You can have a dedication here if you wish.



# Introduction

{AL-2: in primitives, should I clarify what I mean by "break an algorithm"?} {AL-3: Needs to be redone to emphasize chaining, and the work that I did. Right now, it just generally motivates SFE}

Secure Function Evaluation (SFE) is the study and creation of protocols for securely computing a function between multiple parties. Secure, in this context, means that the protocol prevents each party from learning anything about the other parties.

The idea is best communicated through an example: suppose Alice and Bob are millionaires and wish to determine who is wealthier. But Alice and Bob are also secretive, and do not want to disclose their exact amount of wealth. Is there some method by which they can determine who has more money?

{AL-4: check if using greater than or less than function} Alice and Bob can solve their problem by specifying a function  $f$ ; in this case  $f$  takes two inputs  $x$  and  $y$  and outputs 1 if  $x < y$ , and 0 otherwise ( $f$  is the less than function). If Alice and Bob can secretly give  $f$  their amount of wealth and then retrieve the answer from  $f$ , then their problem is solved: they have securely computed who is wealthier.

The goal of SFE broadly is to give Alice, Bob and their friends the ability to securely compute an arbitrary function at their will. For the protocol to be secure, it needs to have the following informal properties:

- **Privacy:** Each party's input is kept secret.
- **Correctness:** The correct answer to the computation is computed.

Originally, the goal of the research community was to develop a secure protocol, define security, and prove the protocol is secure. In more recent times, the focus has shifted to making SFE protocols faster.

If SFE could be made fast enough, it could serve a wide range of applications. For example, imagine that two companies who operate in a similar industry want to work together, but they don't want to disclose any company research which the other doesn't know. These companies could run a set intersection function (a function that given two inputs finds their intersection, or overlap), to determine what information they can disclose without giving away important information.

Another example is running algorithms on confidential medical data. Consider the case where various hospitals want to jointly run algorithms on their data for medical research. The hospital needs guarantees, for both legal and ethical reasons, that the data will not be disclosed as the algorithm is being run, as medical data can be very sensitive. The hospitals could use SFE to run computation on all of their data, maintaining the privacy of their patients.

Since research into SFE has focused on creating a method by which an arbitrary function can be computed securely, the application of SFE may be beyond what we can presently conceive of. It's not unlikely that SFE will become a standard in the internet, where when you access the internet, behind the scenes your access is being plugged into an SFE protocol, sent off to another computer to do some processing. For this future to be realized, work needs to be done to make SFE faster and more flexible.

# Chapter 1

## Cryptographic Primitives

Secure Computation (SC) is the study of computing functions in a secure fashion. SC can be split into two cases: cases where there are two parties involved, referred to as two-party computation (2PC), and cases where there are three or more parties, referred to as multiparty computation (MPC). This thesis will focus primarily on 2PC protocols, but many of the methods are applicable to MPC as well.

2PC protocols are complex cryptographic protocols that rely on a number of cryptographic primitives. In order to understand 2PC, it is not crucial to understand how the cryptographic primitives work, but it is important to understand their inputs, outputs and security guarantees. This first chapter will give an overview of the cryptographic primitives used in 2PC protocols.

### 1.1 Computational Indistinguishability

We formally define computational indistinguishability as follows.

**Definition 1** {AL-5: Adam noted that a might be redundant, and the counting parameter might be confusing} Let  $\mathcal{X} = \{X(n, a)\}_{n \in \mathbb{N}, a \in \{0,1\}^*}$  and  $\mathcal{Y} = \{Y(n, a)\}_{n \in \mathbb{N}, a \in \{0,1\}^*}$  be distribution ensembles.  $\mathcal{X}$  and  $\mathcal{Y}$  are computationally indistinguishable, denoted

$\mathcal{X} \approx_C \mathcal{Y}$ , if and only if for all non-uniform polynomial-time distinguisher  $D$ , there exists a function  $\mu(\cdot)$  that is negligible in  $n$ , such that for all  $a \in \{0, 1\}^*$ ,

$$|Pr[D(X(n, a)) = 1] - Pr[D(Y(n, a)) = 1]| < \mu(n) \quad (1.1)$$

Lindell & Pinkas (2009). ◇

**Definition 2** A function  $\mu : \mathbb{N} \rightarrow \mathbb{R}$  is negligible if for all positive polynomial  $p(\cdot)$ , there exists positive integer  $N_p$  such that for all  $x > N_p$ ,

$$|\mu(x)| < \frac{1}{p(x)}. \quad (1.2)$$

Goldreich (1995) ◇

## 1.2 Encryption

{AL-6: add in from google drive}

$$\begin{aligned} \text{Enc}_k(pt) &= ct \\ \text{Dec}_k(pt) &= \text{Enc}_k^{-1}(ct) = pt \end{aligned} \quad (1.3)$$

where  $pt$  is the original message or the *plaintext*,  $ct$  is the encrypted message or the *ciphertext*, and  $k$  is the secret key. For the algorithm to be effective, the secret key  $k$  must be randomly sampled from  $\{0, 1\}^\lambda$ , where  $\lambda$  is the security parameter of our protocol.<sup>1</sup> {AL-7: does footnote go before or after period? ShouldI have footnotes?}

If  $\lambda$  is increased, thereby increasing the size of the key, then the encryption algorithm becomes harder to break. We will not go into details about how encryption algorithms are instantiated; we will merely treat them as algorithms which we can use at our

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<sup>1</sup>The notion of randomness in cryptography is precisely defined, and in cases where  $\lambda$  is large, it is sufficient for  $k$  to be pseudorandom. Pseudorandomness is also precisely defined.



leisure.

**Definition 3** {AL-8: Get crypto book and finish this} We say that an encryption scheme is secure if:<sup>2</sup>

Define the following game: What are the inputs?

1. Adversary picks two messages  $m_0$  and  $m_1$ , and gives  $m_0$  and  $m_1$  to the sender.
2. The sender samples  $k \leftarrow \{0, 1\}^\lambda$  and  $b \leftarrow \{0, 1\}$ . The sender gives  $m_b$  to the adversary.
3. The adversary may spend a polynomial amount of time doing computation. Eventually, the output  $b'$ , their guess as to which message they received.

◇

It is useful for 2PC to define a specific type of symmetric-key encryption algorithm called a *dual-key cipher* (DKC) [Bellare et al. \(2012\)](#). A DKC requires two keys to encrypt and decrypt the message, in contrast to classic encryption which only requires one. It is easy to instantiate a DKC if one has a secure encryption scheme: let  $k_0$  and  $k_1$  be two keys and instantiate the DKC as follows:

$$\begin{aligned} \text{EncDKC}_{k_0, k_1}(pt) &= \text{Enc}_{k_1}(\text{Enc}_{k_0}(pt)) \\ \text{EncDKC}_{k_0, k_1}^{-1}(ct) &= \text{Enc}_{k_0}^{-1}(\text{Enc}_{k_1}^{-1}(ct)) \end{aligned} \tag{1.4}$$

This construction of a DKC is slow, and there are many faster methods for instantiating DKCS. For more information, see [Bellare et al. \(2012\)](#).

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<sup>2</sup>Specifically, this is the definition of a chosen-plaintext-attack secure encryption scheme. There exists weaker and stronger definitions of security, but chosen-plaintext-attack security is frequently used for 2PC

## 1.3 Boolean Circuit

A function in a 2PC protocol is represented as a boolean circuit. A boolean circuit takes as input  $x \in \{0,1\}^n$ , performs a series of small operations on the inputs, and outputs  $y \in \{0,1\}^m$ . You may have encountered circuits and logical operators in another context, where the inputs and outputs were True and False. For our usage, True will correspond to the value 1, and False will correspond to the value 0.

The small operations done inside of a circuit are performed by a *gate*. A gate is composed of three wires: two input wires and one output wire, where a *wire* can have a value either 0 or 1. A gate performs a simpler operation on the two inputs, resulting in a single output bit. Table 1.3 gives the mapping of an XOR gate.

x	y	xor(x,y)
1	1	0
1	0	1
0	1	1
0	0	0

Table 1.1: The mapping of an XOR gate.

A circuit is a combination of gates that are stringed together. It turns out that circuits are quite powerful: in fact, a circuit composed only of AND gates, XOR gates and NOT gates can compute any function or algorithm ?. In other words, if there's some algorithm that can do it, then there is some circuit that can do it as well. Figure 1.3 shows the circuit representation of the less than function,  $f$  as specified in equation ??.

x	y	$x < y$
0	0	0
0	1	1
1	0	0
1	1	0

Table 1.2: The truth table of the less than circuit.

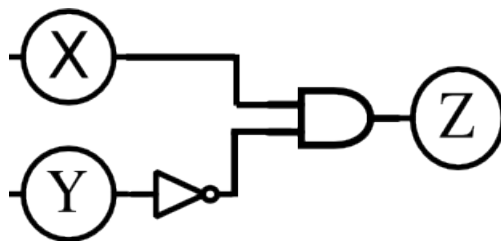


Figure 1.1: A circuit that computes the less or equal to function, equivalent to the less than function for input of two one-bit values. {AL-10: [define symbols and google circuit diagrams latex / tree diagrams](#)}

## 1.4 Oblivious Transfer

{AL-11: [more sources here - get from CompGC paper](#)} {AL-12: [Give a nice metaphor of using OT - what happens. Don't need to give the protocol](#)} Oblivious Transfer (OT) is a two-party protocol where one party (sender) has two inputs,  $m_0$  and  $m_1$ , and the other party (receiver) has a bit  $b \in \{0, 1\}$ . OT enables the receiver to acquire  $m_b$  from the sender without revealing which message is received, i.e., the value of  $b$ . Moreover, the receiver learns nothing about the message that isn't received, i.e. anything about  $m_{1-b}$ .

The protocol relies on the DDH assumption.<sup>3</sup>

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<sup>3</sup>The Decisional Diffie-Hellman (DDH) assumption is a computational hardness assumption involving discrete logarithms in cyclic groups. For more information, see [Boneh \(1998\)](#).



# Chapter 2

## Classic 2PC

[{AL-13: this paragraph is a little weak}](#) Secure Function Evaluation was first proposed in an oral presentation by Andrew Yao <sup>?</sup>. Since Yao's presentation, multiple methods for performing SFE were developed. One method was developed by Yao himself and is Yao's garbled circuits. Another was developed by a group of researchers, Beaver, Micali and Widgerson. The two methods are premised on a similar idea: encrypt a circuit by encrypting its gates, which has since been termed garbled circuit. At this point, it is unclear which method is better, so researchers continue to study both methods. [{AL-14: get source}](#)

This chapter explains the two most prominent methods of SFE for the two party case, referred to as Two-Party Protocol (2PC). The first part of the chapter will motivate and describe desirable properties of a 2PC protocol, culminating in a definition. The second part of the chapter will describe Yao's Garbled Circuits, a method for performing 2PC, and give a proof of security. The third part will provide an overview of GMW's method.

## 2.1 2PC Security Motivation

Think back to Alice and Bob from the introduction. Alice and Bob are millionaires who wish to determine who is wealthier without disclosing how much wealth they have. Formally, we say that Alice has input  $x$  and Bob has input  $y$  ( $x$  and  $y$  are integers corresponding to the wealth of each party), and they want to compute the less than function  $f$ , such that

$$f(x, y) = \begin{cases} 1 & \text{if } x < y; \\ 0 & \text{otherwise.} \end{cases} \quad (2.1)$$

We call the overarching interaction between Alice and Bob protocol  $\Pi$ , and  $\Pi$  consists of all messages exchanged and computations performed. Based on the setup of the problem, we can list a few properties that Alice and Bob wish  $\Pi$  to have.

**Privacy** Parties only learn their output. Any information learned by a single party during the execution of  $\Pi$  must be deduced from the output. For example, in the case of Alice and Bob, if Alice learns that they had more money, then they learn that  $y < x$ , which is some information about  $y$ , but it is deduced from the output so it is reasonable. It would be unreasonable if during the execution of  $\Pi$  Alice learns that  $1,000,000 < y < 2,000,000$ , as that information is not deducable from  $f(x, y)$ .

**Correctness** Each party receives the correct output. In the case of Alice and Bob, this simply means that they learn correctly who has more money.

**Fairness** Each party receives their input if and only if the other party receives their input. In the case of Alice and Bob, this means that Alice receives their input if and only if Bob receives their input. This property prevents the case where Alice sends her input to Bob, Bob computes  $f$  with Alice's input and their own

input, and then Bob refuses to send their input to Alice. Hence Bob has learned to output of  $f$ , but Alice hasn't. We do not want to this scenario to happen, so we require that each party receive their input if and only if the party does. We do not require that all parties always receive their inputs, because a party could abort from the execution in the middle of the protocol.<sup>1</sup>

**Semi-honesty** Each party must obey the protocol. We are assuming that Alice and Bob are semi-honest agents, which means that Alice and Bob are not malicious, trying to steal information from each other. They are simply trying to compute  $f$ , but would use the information that they receive in order to deduce information about the other party. In contrast, we could imagine the scenario where Alice and Bob are untrustworthy: they would send lies, send the wrong input, or deviate in any number of ways to get the upper hand in the protocol. While assuming semi-honesty is not the most realistic scenario, most cryptographic protocols secure with semi-honest agents can be transformed into stronger protocols protecting against malicious agents. Moreover, the semi-honest protocols are more intuitive.

One possible method for constructing a definition of security would be to list a number of properties a secure protocol must have. This approach is unsatisfactory for a number of reasons.

One reason is that an import security property that is only relevant in certain cases may be missed. There are many applications of 2PC, and in some cases, there may be certain properties that critical to security. Ideally, a good definition of 2PC works for all applications, hence capturing all desirable properties. A second reason that the property based definition is unsatisfactory is that the definition should be simple. If the definition is simple, then it should be clear that *all* possible attacks

---

<sup>1</sup>Technically this is incorrect since we assume that each party is semihonest, but the property generalizes more easily when this requirement when considering aborts.

against the protocol are prevented by the definition. A definition based on properties in this respect as it becomes the burden of the prover of security to show that all relevant properties are covered [Lindell & Pinkas \(2009\)](#).

## 2.2 2PC Security Definition

A number of definitions formalize what it means for a 2PC protocol to be secure. Yao, in his 1982 paper on secure function evaluation, says a 2PC protocol is secure if:

If one participant behaves according the the protocol, the probability that the other participant successfully cheats is at most  $\gamma$  for  $\gamma \in \{0, 1\}$ .  
?

TODO mention that this doesn't use the properties.

Since Yao's original definition, many other definitions of security of 2PC protocols have been proposed. Here we give Goldreich's definition, which is given in his textbook *Foundations of Cryptography Volume II*, and is reproduced in various papers, most notably Lindell and Pinkas 2009 survey paper on SFE [Goldreich \(1995\)](#); [Lindell & Pinkas \(2009\)](#).

**Setup:**

- Let  $f = (f_1, f_2)$  be a probabilistic, polynomial time functionality where Alice and Bob compute  $f_1, f_2 : \{0, 1\}^n \rightarrow \{0, 1\}^m$  respectively.
- Let  $\Pi$  be a two party protocol for computing  $f$ .
- Define  $\text{view}_i^\Pi(n, x, y)$  (for  $i \in \{1, 2\}$ ) as the view of the  $i$ th party on input  $(x, y)$  and security parameter  $n$ .  $\text{view}_i^\Pi(n, x, y)$  equals the tuple  $(1^n, x, r^i, m_1^i, \dots, m_t^i)$ , where  $r^i$  is the contents of the  $i$ th party's internal random tape, and  $m_j^i$  is the  $j$ th message that the  $i$ th party received.



- Define  $\text{output}_i^\Pi(n, x, y)$  as the output of the  $i$ th party on input  $(x, y)$  and security parameter  $n$ . Also denote

$$\text{output}^\Pi(n, x, y) = (\text{output}_1^\Pi(n, x, y), \text{output}_2^\Pi(n, x, y)).$$

- Note that  $\text{view}_i^\Pi$  and  $\text{output}_i^\Pi$  are random variables whose probabilities are taken over the random tapes of the two parties. Also note that for two party computation.

**Definition:** We say that  $\Pi$  securely computes  $f$  in the presence of static<sup>2</sup> semi-honest adversaries if there exists probabilistic polynomial time algorithms  $S_1$  and  $S_2$  such that for all  $x, y \in \{0, 1\}^*$ , where  $|x| = |y|$ , the following are true:

$$\{(S_1(x, f_1(x, y), f(x, y)))\}_{x,y} \equiv^C \{(\text{view}_1^\Pi(x, y), \text{output}^\Pi(x, y))\}_{x,y} \quad (2.2)$$

$$\{(S_2(x, f_2(x, y), f(x, y)))\}_{x,y} \equiv^C \{(\text{view}_2^\Pi(x, y), \text{output}^\Pi(x, y))\}_{x,y} \quad (2.3)$$

**Intuition:** We think of  $\text{view}_i^\Pi$  as all of the information that the  $i$ th party has to operate with, such that any conclusion that the  $i$ th party can come to could be determined from  $\text{view}_i^\Pi$ . Moreover,  $\text{output}_i^\Pi$  is simply the output of the  $i$ th party. The value of  $\text{output}_i^\Pi$  is computable from the tuple  $\text{view}_i^\Pi$ .

Equations 2.2 and 2.3 state that a probabilistic, polynomial time algorithm, denoted  $S_1$  and  $S_2$ , which is given access *only* to the party's input and output can compute the view of a party. The definition requires that  $S_1$  on input  $(x, f(x, y))$  must be able to compute  $\text{view}_1^\Pi(x, y)$ , in particular the messages received by party 1, such that the generated view is indistinguishable from the actual view. If there exists an algorithm that can perform the aforementioned task, then  $\Pi$  does not adequately conceal information, so we should not consider

---

<sup>2</sup>{AL-15: TODO} Mention what static means

$\Pi$  to be secure.

Finally, the definition requires that  $|x| = |y|$ ; however, this constraint can be overcome in practice by padding the shorter input.

The definition of security provided here only applies when adversaries are semi-honest (see ??). Definitions of security in settings with malicious adversaries require substantially more complexity. As a result, these definitions are often simulation based definitions. They imagine an ideal world, where the function  $f$  must be computed securely, and by a series of comparisons, show that the real world where  $\Pi$  computes  $f$  is essentially the same as the ideal world. For an easy to understand security definition of 2PC with malicious adversaries, we refer to reader to ?.

## 2.3 Yao's Garbled Circuit

{AL-16: struggling a bit with the organization of this section} We now give an implementation of a generic 2PC scheme created by Yao ?. The scheme is designed for two parties, Alice and Bob, who have inputs  $x$  and  $y$  respectively. We suppose that Alice and Bob wish to compute the function  $f : \{0, 1\}^n \rightarrow \{0, 1\}^m$ .

{AL-17: This should probably be 2-4 sentences, instead of one} At a high level, Yao's scheme has the following steps: (1) Alice builds and encrypts the circuit representing  $f$ . (2) Alice sends the garbled (encrypted) circuit to Bob. (3) Bob receives the input labels via Oblivious Transfer. (4) Bob uses the input labels to decrypt the circuit. (5) Bob sends the output of the circuit to Alice.

### 2.3.1 Step 0: Setup

Alice and Bob wish to compute the function  $f$ , which is represented by a circuit (see section ??). Alice is the garbler, and will create and encrypt the circuit. Bob is the evaluator, and will compute the circuit.

---

**Algorithm 1** Garble Circuit

---

**Require:** Circuit  $f(x, \cdot)$ **Ensure:** Populate garbled tables  $f(x, \cdot).tables$ .  **for** wire  $w_i$  in  $f(x, \cdot).wires$  **do**    Generate two encryption keys, called garbled values,  $W_i^0$  and  $W_i^1$ .    Assign  $(W_i^0, W_i^1)$  to  $w_i$ .  **end for**  **for** gate  $g$  in  $f(x, \cdot).gates$  **do**    Let  $w_i$  be  $g$ 's first input wire.    Let  $w_j$  be  $g$ 's second input wire.    Let  $w_k$  be  $g$ 's output wire.    **for**  $(u, v) \in \{(0, 0), (0, 1), (1, 0), (1, 1)\}$  **do**       $T_g[u, v] = \text{Enc}_{W_i^u}(\text{Enc}_{W_j^v}(W_k^{g(u,v)}))$  {AL-18: use DKC}    **end for**     $f(x, \cdot).tables[g] = T_g$ .  **end for**

---

---

**Algorithm 2** Evaluate Circuit

---

**Require:**  $(input\_wires, tables, gates)$   **for** Input wire  $w_i$  in  $input\_wires$  **do**     $\triangleright$  retrieve garbled values of input wires    Perform  $\text{OT}(w_i, x_i)$      $\triangleright$  retrieve  $W_i^{x_i}$  from Alice    Save the value to  $w_i$ .  **end for**  **for** Gate  $g$  in  $gates$  **do**     $\triangleright$  compute the output of each gate.    Let  $w_i$  be  $g$ 's first input wire.    Let  $w_j$  be  $g$ 's second input wire.    Let  $w_k$  be  $g$ 's output wire.    **Require**  $w_i$  and  $w_j$  have been assigned garbled values.    **Require**  $w_k$  has not been assigned a garbled value.    **for**  $(u, v) \in \{(0, 0), (0, 1), (1, 0), (1, 1)\}$  **do** {AL-19: use DKC}       $temp = \text{Dec}_{w_j}(\text{Dec}_{w_i}(tables[g][u, v]))$       **if**  $temp$  decrypted correctly **then**         $w_k = temp$       **end if**    **end for**  **end for**

---

### 2.3.2 Step 1: Garbling the Circuit

{AL-20: define a wire and a label} Alice garbles each gate, according to algorithm ??, resulting in a table  $T_g$  for each gate  $g$ . The table enables the computation of a single gate  $g$ , if one is given either  $W_i^0$  or  $W_i^1$  and either  $W_j^0$  or  $W_j^1$  where wire  $i$  and  $j$  are the input wires to  $g$ . Figure ?? gives an example of a table for an AND gate. {AL-21: TODO}

### 2.3.3 Step 2: Bob's Input and Computing the Circuit

Alice sends the garbled circuit to Bob. The garbled circuit consists of a garbled table for each gate,  $f(x, \cdot).tables$ , and the rules for connecting the gates together. In order for Bob to compute the circuit, he needs the garbled values of all input wires. Once he has the garbled values of the input wires, he can decrypt the first few gates, and acquire the decryption keys of the other gates until he has the keys to decrypt all of the gates in the circuit, yielding the output. Bob can acquire the garbled values of the input wires from Alice using 1-out-of-2 oblivious transfer on each input wire (see section ??). If necessary, Bob sends the final output of the circuit to Alice. This is necessary only if  $f_1(x, y) \neq f_2(x, y)$ . Bob's protocol is outlined in more detail in Algorithm 2.

### 2.3.4 Explanation of the security of Yao's protocol

To do.

### 2.3.5 Notes about complexity

1 OT per (input?) wire. 4 cts per gate. How much encryption? Not good enough for practice.

## 2.4 GMW

Where Yao's protocol is premised on encrypting gates individually, GMW's protocol for garbling circuits is premised on secret sharing, and performing operations on the shared secrets. Secret sharing, in general, is a class of methods for distributing a secret to a group of participants, where each participants is allocated a *share* of the secret. The secret can only be reconstructed when a sufficient number of the participants combine their shares, but any pool of insufficient shares yields no information about the secret.

GMW begins by having Alice and Bob secret share their inputs, so each party now has a collection of *shares*. Algorithm 3 describes this process in more detail. Then Alice and Bob perform a series of operations on their shares, which are dictated by the gates in the function they wish to compute. As with Yao's protocol, a gate may either compute XOR, AND or NOT. Each operation requires a different series of operations, which are described in Algorithm 4. Finally, Alice and Bob publicize their shares to each other, at which point each party will have sufficient shares to compute the output of the function.

---

### Algorithm 3 GMW Setup

---

Alice does the following on input Circuit  $f(x, \cdot)$  and  $x = x_0x_1 \dots x_n$   
**for** wire  $w_i$  in  $f(x, \cdot).wires$  **do**  
    Assign  $a_{w_i}^1 \leftarrow \{0, 1\}$   $\triangleright$  a uniform random selection of 0 or 1.  
    Assign  $b_{w_i}^1 = x_i \oplus a_{w_i}^1$   
**end for**  
Bob does likewise on input Circuit  $f(x, \cdot)$  and  $y = y_0y_1 \dots y_n$   
Hence Alice has generated shares  $\{a_w^1, b_w^1\}_w$   
and Bob has generated shares  $\{a_w^2, b_w^2\}_w$   
Alice and Bob divide the shares such that Alice has all  $a_w$  and Bob has all  $b_w$ .

---

{AL-22: see mike rosulek first few slides for good images. } {AL-23: illustration about translating bits over to wire labels}

---

**Algorithm 4** GMW Gate Evaluation
 

---

**XOR Gate**

$$\triangleright x_i \oplus y_i = (a_{w_i}^1 \oplus b_{w_i}^1) \oplus (a_{w_i}^2 \oplus a_{w_i}^2)$$

Alice evaluates  $a_{w_i}^1 \oplus a_{w_i}^2$

Bob evaluates  $b_{w_i}^1 \oplus b_{w_i}^2$

**AND Gate**

$$\triangleright x_i \wedge y_i = (a_{w_i}^1 \oplus b_{w_i}^1) \wedge (a_{w_i}^2 \oplus a_{w_i}^2)$$

Alice samples  $\sigma \leftarrow \{0, 1\}$

$\triangleright$  a uniform random selection of 0 or 1

Alice constructs table  $T$ :

**for**  $(u, v) \in \{(0, 0), (0, 1), (1, 0), (1, 1)\}$  **do**

$$T[u, v] = (a_{w_i}^1 \oplus u) \wedge (a_{w_i}^2 \oplus v)$$

$$s[u, v] = \sigma \oplus T[u, v].$$

**end for**

Do 1-4OT. Alice sends  $(s[0, 0], s[0, 1], s[1, 0], s[1, 1])$

Bob selects result based on  $(u, v) = (b_{w_i}^1, b_{w_i}^2)$ .

**NOT Gate**

$$\triangleright w_i = (\neg a_{w_i}) \oplus (\neg b_{w_i})$$

$\triangleright$  Evaluate the negative of a particular wire  $w_i$ .

$$w_i = a_{w_i} \oplus b_{w_i}$$

$$\text{Let } a'_{w_i} = 1 \oplus a_{w_i}$$

$$\triangleright \text{i.e. } a'_{w_i} = \neg a_{w_i}$$

$$\text{Let } b'_{w_i} = 1 \oplus b_{w_i}$$

$$\triangleright \text{i.e. } b'_{w_i} = \neg b_{w_i}$$


---

# Chapter 3

## Improving MPC

A number of improvements have been made to Yao's garbled circuit since its inception in 1986. The first scheme for a garbled circuit, the one described in section ?? has the following requirements: **include chart here**. Let's look into how much work is required in classical garbled circuits. **mention that we look gate-wise** The first metric that we look at is the size of the garbled table. **check this definition:**The garbled table, if you call recall from earlier, is the set of ciphertexts that is sent from the garbler to the evaluator, like in figure ??. Hence the size refers to the number of ciphertexts that an evaluator needs to compute a single gate. The XOR column gives the size of the garbled table for an xor gate, and naturally the AND columns gives the of the garbled table for an AND gate. The cost associated with the size of a garbled table manifests in effecting the amount of bandwidth that needs to be transmitted between the garbler and evaluator. In order to evaluate the gate, the evaluator needs the entire garbled table (**research idea: what if they don't? what if they can know when to request more information, to reduce the average amount of bandwidth? maybe this could be used to reduce calls to OT?**), so reducing the size of the garbled table reduces bandwidth. It turns out that bandwidth contributes a large hit to the total time required to perform mpc with

garbled circuits, and as a result, many of the improvements to garbled circuits have specifically targeted reducing the size of the garbled table down from 4 ciphertexts.

**maybe mention lower bound?**

The second metric we use to analyze garbled circuits is eval cost. Eval cost is the amount of computation that the evaluator must perform in order to process a single gate. In the classical setting, the evaluator goes down the garbled table decrypting each ciphertext until one of the decrypts correctly. In the worst case, this requires 8 decryptions, because each wire is encrypted twice, once with the key from the first input wire and then again with the key from second input wire. If a dual-key cipher (an encryption scheme that takes two keys, see section ??), then the evaluator only needs to perform four decryptions in the worst case. The metric that we consider is the garble cost of a gate. The garble cost of a gate is amount of computation that the garbler needs to perform. The improvements to MPC have generally increased the garble cost; having the garbler do more work, can reduce the size of the garble table which reduces bandwidth, and can reduce the amount of computation the evaluator needs to perform **fact check this**. Before the research of ? and the research presented in this thesis, there were not substantial benefits to garbler preprocessing, that is having the garbler do work *before* the actual computation time. We call this having the garbler do offline work, because they can do the work at their leisure, like at night when computers are less used. Then the online work, when the actual computation is performed, is reduced.

As we walk through the improvements to Yao's garbled circuit, we are going to think about the improvements affect the three metrics described above. It should also be noted before we begin that these are improvements to the the computation of a single gate. Speedups on the gate level propagately widely through the computation of the entire garbled circuit. For example, the computation of AES now requires approximatley 40,000 gates, so reducing the number of ciphertexts being transmitted



per gate by 1, reduces the total amount of ciphertexts that need to be sent by 10,000 - a substantial speedup indeed.

## 3.1 Point and Permute

Beaver, Micali and Rogaway contributed the first major improvement to Yao's garbled circuit in 1990. A detail that I may or may not (**check this**) have left out in the description of Yao's garbled circuit is that the order of the garbled table needs to be permuted. If the first key sent is always the key corresponding to the input wires's 0 keys, then the evaluator can learn what the value of the input wires is - a violation of security. The solution to this problem is easy: permute the order of the garbled table. Now the evaluator trial decrypts each of the 4 ciphertexts until there is a valid decryption . <sup>1</sup>

The *point and permute* technique speeds up the evaluator's computation of the garbled table by removing the need to trial decrypt the ciphertexts; instead, the garbler subtly communicates which single ciphertext to decrypt. Using point and permute, the garbler randomly assigns a select bit 0 or 1 to each wire label in a wire **Is it clear what a wire label is, as opposed to a wire? Should be by now.** The assignment of the select bit is independent of the truth value of the wire, so two things can happen. First, the garbler can permute the garbled table based on the select bits, and second, the select bits can be sent to the evaluator. Upon receiving the garbled table, the evaluator knows exactly which ciphertext to decrypt based on the select bits of the input wires.

Point and permute slightly increases the amount of garbler-side computation to substantially decrease the amount of evaluator computation. The garbler needs to

---

<sup>1</sup>There exists encryption/decryption schemes that in addition to decrypting a ciphertext will also output a bit indicating if the decryption occurred correctly. Using schemes like this, the evaluator can decrypt all 4 ciphertexts until one of them decrypts correctly, as indicated by the extra bit output by the decryption algorithm.

sample  $n + q^2$  additional random bits. Without point and permute, the evaluator needs to decrypt 2.5 ciphertexts on average, hence we estimate that there are roughly  $1.5(n + q)$  fewer decryptions required. The overall bandwidth is increased by 2 bits per wire, from 256 bits to 258 bits: a small constant increase. <sup>3</sup>

Select Bit	Wire Label	Select Bits	Encryption
0	$A_0$	(0,0)	$\text{Enc}_{A_0, B_1}(C_1)$
1	$A_1$	(0,1)	$\text{Enc}_{A_0, B_0}(C_0)$
1	$B_0$	(1,0)	$\text{Enc}_{A_1, B_1}(C_0)$
0	$B_1$	(1,1)	$\text{Enc}_{A_1, B_0}(C_0)$

$C_0 \leftarrow \{0, 1\}^n$   
 $C_1 \leftarrow \{0, 1\}^n$

Table 3.1: Garbled Gate for Point and Permute

Table 3.2: Example garbled gate using point and permute. The gate being computed is given in figure **Make it 23:30 in mike's talk**

**add chart if you want**

## 3.2 Garbled Row Reduction 3

Garbled Row Reduction 3 (GRR3) is technique proposed by **who** which decreases the number of ciphertexts that need be communicated between the garbler and evaluator. To use GRR3, the garbler must also use the point and permute method. Using Garbled Row Reduction, the garbler sets the ciphertext in the top row of the garbled table equal to a value that decrypts to  $0^n$ . <sup>4</sup> Upon evaluating the garbled gate, if the evaluator sees that the select bits of the input wires indicate to decrypt the first row, the evaluator simply assumes the ciphertext be of value  $0^n$ .

Discuss security.

GRR3 does not have a significant effect on garbler side computation. The difference being that for constructing some wire labels, the garbler may need to compute  $\text{Enc}^{-1}$  instead of generating random bits. The evaluator needs to perform slightly

<sup>2</sup>Recall from **todo some previous section** that  $n + q = \text{number of wires}$ .

<sup>3</sup>The value is constant in the sense that it is independent of the security parameter.

<sup>4</sup>In previous constructions the value for the ciphertext was chosen randomly.

Select Bit	Wire Label	Select Bits	Encryption
0	$A_0$	(0,1)	$\text{Enc}_{A_0,B_0}(C_0)$
1	$A_1$	(1,0)	$\text{Enc}_{A_1,B_1}(C_0)$
1	$B_0$	(1,1)	$\text{Enc}_{A_1,B_0}(C_0)$
0	$B_1$		

$$C_0 \leftarrow \{0, 1\}^n$$

$$C_1 \leftarrow \text{Enc}_{A_0,B_1}^{-1}(0^n)$$

Table 3.3: Example garbled gate using point and permute and garbled row reduction 3. The gate being computed is given in figure **Make it 23:30 in mike's talk**

less computation: in the event that the first row needs to be decrypted, the evaluator doesn't need to perform the decryption algorithm. This events occur with probability  $\frac{1}{4}$ , so we can extrapolate that the evaluator needs to perform  $\frac{1}{4}$  fewer decryptions than would be necessary if only PP were being used. GRR3 reduces the size of the garbled table from 4 ciphertexts to 3 ciphertexts, a 25% reduction.

### 3.3 Free XOR

The Free XOR technique was developed by **who** in **when**. The technique, as the name suggests, makes the computation of XOR gates essentially free. Say the garbler is determining the labels for wire  $A$ . The label associated with truth value 0,  $A_0$ , is randomly sampled from  $\{0, 1\}^n$ . Then the label associated with the truth value 1 is set such that  $A_1 \leftarrow A_0 \oplus \Delta$ , where  $\Delta$  is global, randomly sampled value from  $\{0, 1\}^n$ . If the garbler is garbling an XOR gate, then they set  $C_0$  to  $A \oplus B$ , and like wire  $A$ , the garbler sets  $C_1 = C_0 \oplus \Delta$ . With these wire labels, it turns out that the garbler doesn't need to send a garbled table: the evaluator can compute  $C_0$  and  $C_1$  by XORing the

inputted wire labels. The math for this operation is shown below:

$$A \oplus B = C$$

$$(A \oplus \Delta) \oplus B = (A \oplus B) \oplus \Delta = C \oplus \Delta$$

$$A \oplus (B \oplus \Delta) = (A \oplus B) \oplus \Delta = C \oplus \Delta$$

$$(A \oplus \Delta) \oplus (B \oplus \Delta) = (A \oplus B) \oplus (\Delta \oplus \Delta) = C$$

Wire Label	Value
$A_0$	$A$
$A_1$	$A \oplus \Delta$
$B_0$	$B$
$B_1$	$B \oplus \Delta$
$C_0$	$A \oplus B$
$C_1$	$C_0 \oplus \Delta$

Table 3.4: Example XOR garbled gate wires using PP, GRR3 and Free XOR.

The Free XOR technique is also compatible with GRR3, but since XOR doesn't require a garbled table, GRR3 is only used on AND gates.

Using the Free XOR technique, the size of the garbled table is zero for all XOR gates and of size 3 for AND gates. This fact incentivizes the construction of circuits that optimize the number of XOR gates, minimize the number AND gates (while minimizing the size of the entire circuit of course). One interesting implication of using the Free XOR technique is that an added assumption must be made to our encryption algorithm. Since  $\Delta$  is part of the key and part of the the payload<sup>5</sup> of the encryption algorithm, the encryption algorithm must be secure under the circularity assumption. Fortunately, modern encryption uses AES-128, which is presumed to be secure under the circularity assumption.

---

<sup>5</sup>The payload is the value that is being encrypted

## 3.4 Garbled Row Reduction 2

Garbled row reduction 2 (GRR2) reduces the size of the garbled table for AND gates to 2 ciphertexts, but it is not compatible with Free XOR, so XOR gates also require two ciphertexts. The approach of GRR2 is quite different from GRR1. At a high level, it creates two third degree polynomials, one based on the input wires which should output  $C_0$ , and a second polynomial which should output  $C_1$ . Since the polynomials are of three degrees, they are each uniquely generated by 3 points. To construct the polynomials, the garbler uses  $x, y, z$  for the first polynomial and  $u, v, w$  for the second polynomial. The garbler sends  $x, y, u, v$  to the evaluator, who can interpolate the polynomial and plug in values  $a, b, c$ .

GRR2 is incompatible with Free XOR because the ciphertext values cannot be set to the xor of the input wires (e.g.  $C \leftarrow A \oplus B$ ), since the value of  $C$  is always determined by the polynomial.

**Add to this, but no need to spend too much time on it since it's not used**

## 3.5 FleXOR

Before the creation of FlexXOR, MPC was at an awkward point where some circuits with a large amount of xors could be computed faster with Free XOR and circuits with fewer XORS could be computed faster with GRR2. FleXOR serves as a method to reconcile GRR2 with Free XOR, solving the problem where the ciphertext values of AND gates are independent of  $\Delta$ .

FleXOR's construction is straightforward: use GRR2 on AND gates, and use Free XOR on XOR gates, correcting the delta value where necessary. To correct the delta value, FleXOR adds in a unary gate that maps  $A, A \oplus \Delta_1 \rightarrow A', A' \oplus \Delta_2$ .

Suppose we have the following situation: we computing an XOR gate with input

wires  $A, B$  and output wire  $C$ . Input wires  $A$  and  $B$  have each come from a different AND gate, so their labels are the result of the polynomial interpolation of GRR2. To perform Free XOR,  $A, B$  and  $C$  need to be using the same delta value. The garbler adds an extra gate between  $A$  and  $B$  and the XOR gate that adjusts their XOR value to the correct value. The unary<sup>6</sup> gate maps  $A, A \oplus \Delta_1 \rightarrow A', A' \oplus \Delta_2$ , where  $\Delta_2$  is the correct delta value for the XOR gate.

A garbled AND table costs 2 ciphertexts, and a garbled XOR table costs 0, 1 or 2 ciphertexts depending on how many ciphertexts need to be corrected. This turns the creation of the circuit into a combinatorial optimization problem, where the offset of each wire needs to be determined to minimize the total cost of each XOR gate and be subject to the compatibility of GRR2 for AND gates. Without doing too much (if any) optimization of the circuit, FleXOR usually requires 0 or 1 ciphertext in practice.

## 3.6 Half Gates

Half Gates is the most recent improvement Yao's garbled circuit. The goal of Half Gates is to make AND gates cost two ciphertexts, while preserving properties necessary for Free XOR. I'm going to introduce Half Gates in a different way from the original paper.

We consider the case of an AND gate  $c = a \wedge b$ , where the generator knows the value of  $a$ . If  $a = 0$ , then the generator will create a unary gate that always outputs false,  $c$ . Otherwise if  $a = 1$ , then the generator create a unary identity gate that always outputs  $b$ . Table 3.6 shows the garbled gates for different values of  $a$ .

Since the evaluator has the wire label corresponding to  $b$  (either  $B$  or  $B \oplus \Delta$ ), the evaluator can compute the label of the output wire of the AND gate by xoring the rows in the garbled table by  $H(B)$  or  $H(B \oplus \Delta)$  to get  $C$  or  $C \oplus \Delta$  respectively.

---

<sup>6</sup>Unary means has one input and one output.

Garbled Table for $a = 0$	Garbled Table for $a = 1$	→	Garbled Table for any $a$
$H(B) \oplus C$	$H(B) \oplus C$		$H(B) \oplus C$
$H(B) \oplus C$	$H(B) \oplus C \oplus \Delta$		$H(B) \oplus C \oplus a\Delta$

Table 3.5: Generator’s Garbled Half Gate for  $a = 0$ ,  $a = 1$ , and written more succinctly with  $a\Delta$  for  $a \in \{0, 1\}$ . If  $a = 0$ , then  $a\Delta = 0$ . Otherwise if  $a = 1$ , then  $a\Delta = \Delta$ .

A further improvement can be garnered by using the garbled row-reduction trick. We choose  $C$  such that the first of the top row of the garbled table is the all zeros ciphertext. The top row may not necessarily be  $H(B) \oplus C$ , since for security the rows are permuted. Because the top row is all 0s, it does not need to be sent to the evaluator. If the evaluator should decrypt the cipher text on the top row (as directed by point and permute), then the evaluator assumes the cipher text to be all 0s. Overall, computing  $a \wedge b = c$  requires two having operations by the generator, a single hash operation by the evaluator, and the communication of one ciphertext.

We now consider computing  $a \wedge b = c$ , where the evaluator somehow already knows the value of  $a$ . If  $a = 0$ , then the evaluator should acquire  $C$ . Otherwise if  $a = 1$ , then the evaluator should acquire  $C \oplus b\Delta$ , in which case it is sufficient for the evaluator to obtain  $\Omega = C \oplus B$  (then xor  $\Omega$  with the wire label corresponding to  $b$ ). Table 3.6 shows cipher texts which the garbler gives to the evaluator.

This table is different from other garbled tables. First, the table does not need to be permuted. Secondly, evaluation is different. If  $a = 0$ , then the evaluator uses wire label  $A$  to decrypt the top row of the table and acquire  $C$ . If  $a = 1$ , then the evaluator uses  $A \oplus \Delta$  to decrypt the second row, yielding  $C \oplus B$ . The evaluator then xors wire label  $B + b\Delta$  by  $C \oplus B$  to obtain  $C \oplus b\Delta$ . As before, the ciphertext on the top row does not need to be communicated by using the garbled row-reduction trick. The generator sets  $C$  such that  $H(A) \oplus C = 0$  (i.e.  $C = H(A)$ ). The total cost of this half gate is the same as before: two hashes by the generator, one hash by the evaluator, and once cipher text.

Garbled Table for any $A$
$H(A) \oplus C$
$H(A \oplus \Delta) \oplus C \oplus B$

Table 3.6: Evaluator’s half gate garbled table.

We now put the two half gates together to form an AND gate. Consider the following where  $r$  is a random bit generated by the generator:

$$a \wedge b = a \wedge (r \oplus r \oplus b) = (a \wedge r) \oplus (a \wedge (r \oplus b)). \quad (3.1)$$

The first AND gate,  $a \wedge r$ , can be computed with a generator-half-gate - the generator “knows”  $r$ . Furthermore, if we can let the evaluator know the value of  $r \oplus b$ , then the second AND gate,  $(a \wedge (r \oplus b))$ , can be computed with an evaluator-half-gate - the evaluator “knows”  $r \oplus b$ . And the final XOR can be computed with free xor at the cost of no ciphertexts.

It is secure for the garbler to give  $r \oplus b$  to the evaluator, since  $r$  is random and blinds the value of  $b$ . The value of  $r \oplus b$ , while only a single bit, can be communicated to the evaluator for free: use the select bit (from the point and permute technique) of the false wire label on wire  $b$  (so  $r$  is the select bit on the true wire label of wire  $b$ ).

The overall cost of using Half Gates for AND gates is four calls to  $H$  for the generator, two calls to  $H$  for the evaluator, and the communication of 2 ciphertexts. Half Gates guarantees only two ciphertexts are needed per AND gate, but the tradeoff is the additional computation for both parties (i.e. computing  $H$ ). With FleXOR, the number of ciphertexts that need to be communicated may vary, but there is less computation required.

When MPC becomes used in real operations, the circuit will likely not be optimized for XOR gates (reducing the number of AND gates). In this case, where an arbitrary function is being computed, it is hypothesized that Half Gates will outperform FleXOR. But in many of the cases being examined now - most notably



computing AES - the circuit has been optimized heavily to have very few AND gates, hence the benefits of Half Gates are less noticeable.<sup>7</sup>

Garbled Circuit Improvement	Size ( $x\lambda$ )		Eval Cost		Garble Cost		Assumption
	XOR	AND	XOR	AND	XOR	AND	
Classical	1365	1260	1024	$\mu$ s	a	b	a
Point and Permute	TBD	TBD	TBD	$\mu$ s	a	b	a
GRR3	TBD	TBD	TBD	$\mu$ s	a	b	a
Free XOR	TBD	TBD	TBD	$\mu$	a	bs	a
GRR2 XOR	TBD	TBD	TBD	$\mu$	a	bs	a
FleXOR	TBD	TBD	TBD	$\mu$	a	bs	a
Half Gates	2	0	2	0	4	0	a

Table 3.7: Summary of Garbled Circuit Improvements. GRR3 stands for garbled row reduction 3 and GRR2 stands for garbled row reduction 2

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<sup>7</sup>At present, the AES circuit is 80% XOR gates



# Chapter 4

## Chaining Garbled Circuits

the “theory”

talk about our idea about chaining here.

### 4.1 The Random Oracle Model and Random Permutation Model

### 4.2 Chaining

### 4.3 Security of Chaining

### 4.4 Single Communication Multiple Connections

### 4.5 Security of SCMC



# Chapter 5

## Implementation

talk about my implementation here. be clear about what I did.

### 5.1 JustGarble

### 5.2 Our Implementation: CompGC

### 5.3 Adding SCMC



# Chapter 6

## Experiments and Results

talk about experiments and results here

### 6.1 Experimental Setup

talk about what computer was used, bandwidth, network simulation

### 6.2 Experiments

AES, CBC, Levenshtein

### 6.3 Results





# Chapter 7

## Future and Related Work

talk about future and related work here



# Conclusion

Here's a conclusion, demonstrating the use of all that manual incrementing and table of contents adding that has to happen if you use the starred form of the chapter command. The deal is, the chapter command in  $\text{\LaTeX}$  does a lot of things: it increments the chapter counter, it resets the section counter to zero, it puts the name of the chapter into the table of contents and the running headers, and probably some other stuff.

So, if you remove all that stuff because you don't like it to say "Chapter 4: Conclusion", then you have to manually add all the things  $\text{\LaTeX}$  would normally do for you. Maybe someday we'll write a new chapter macro that doesn't add "Chapter X" to the beginning of every chapter title.



# Appendix A

## The First Appendix



## Appendix B

The Second Appendix, for Fun





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