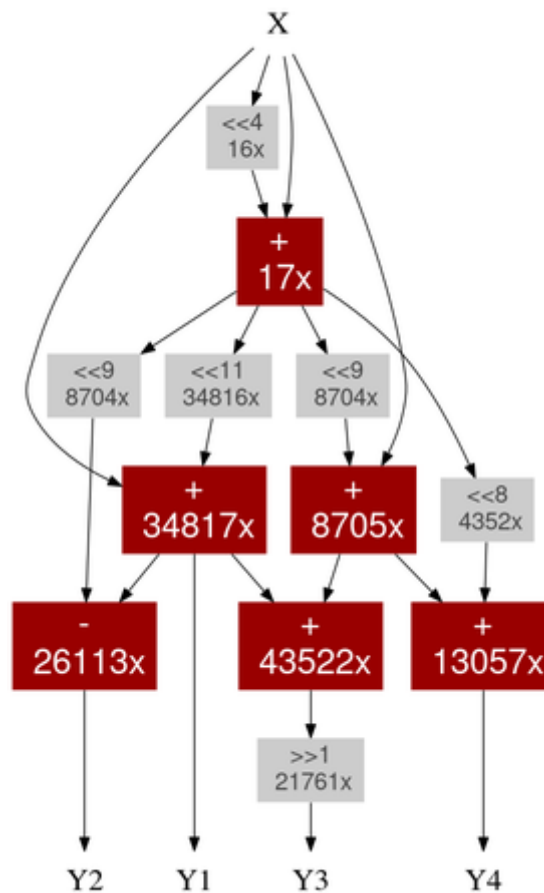


# EEL7123: Laboratório 3

**Aluno:** Alexandre Hoffmann Genthner

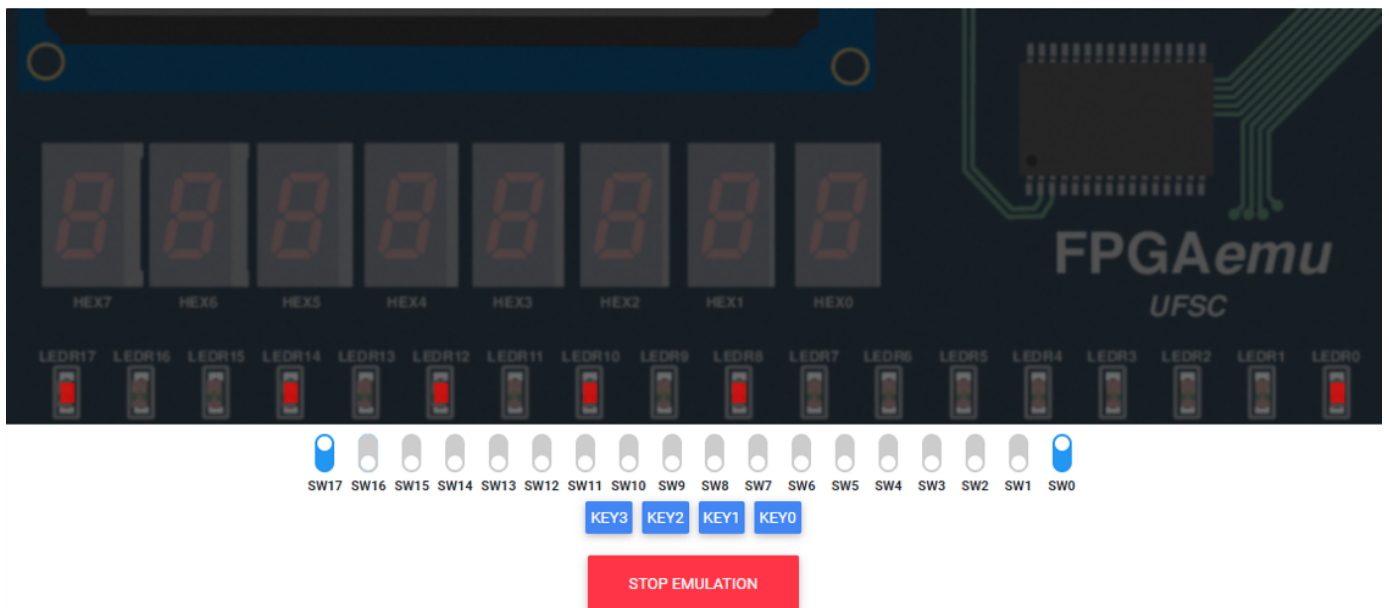
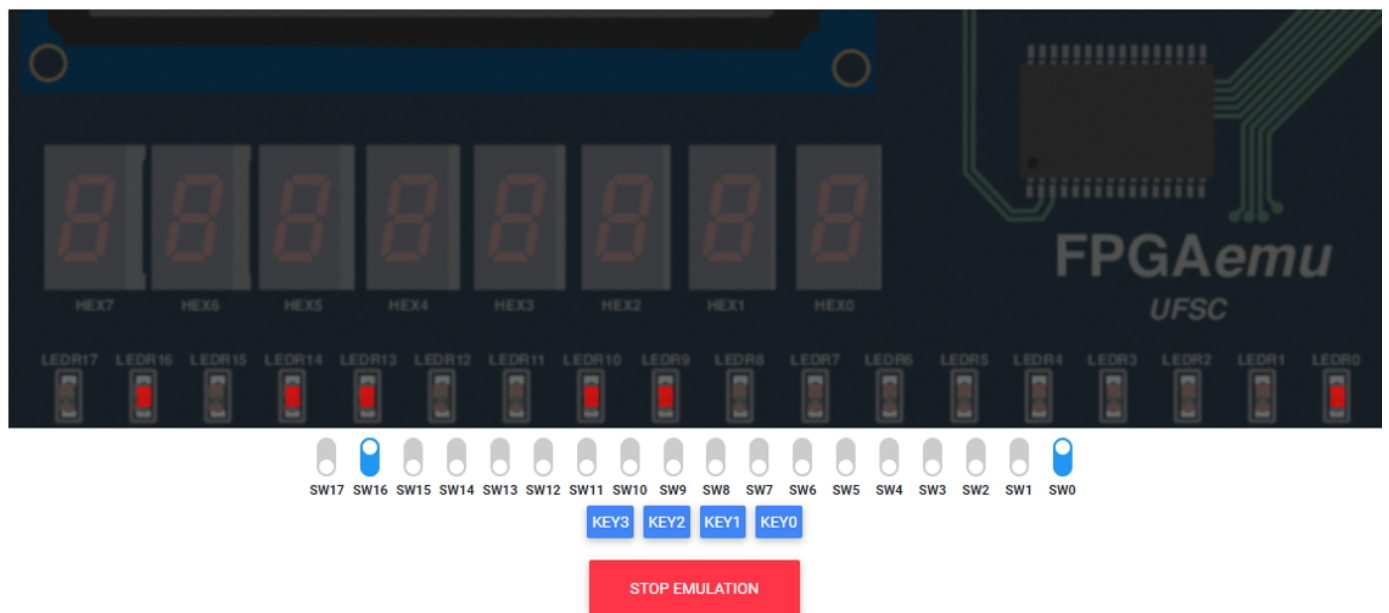
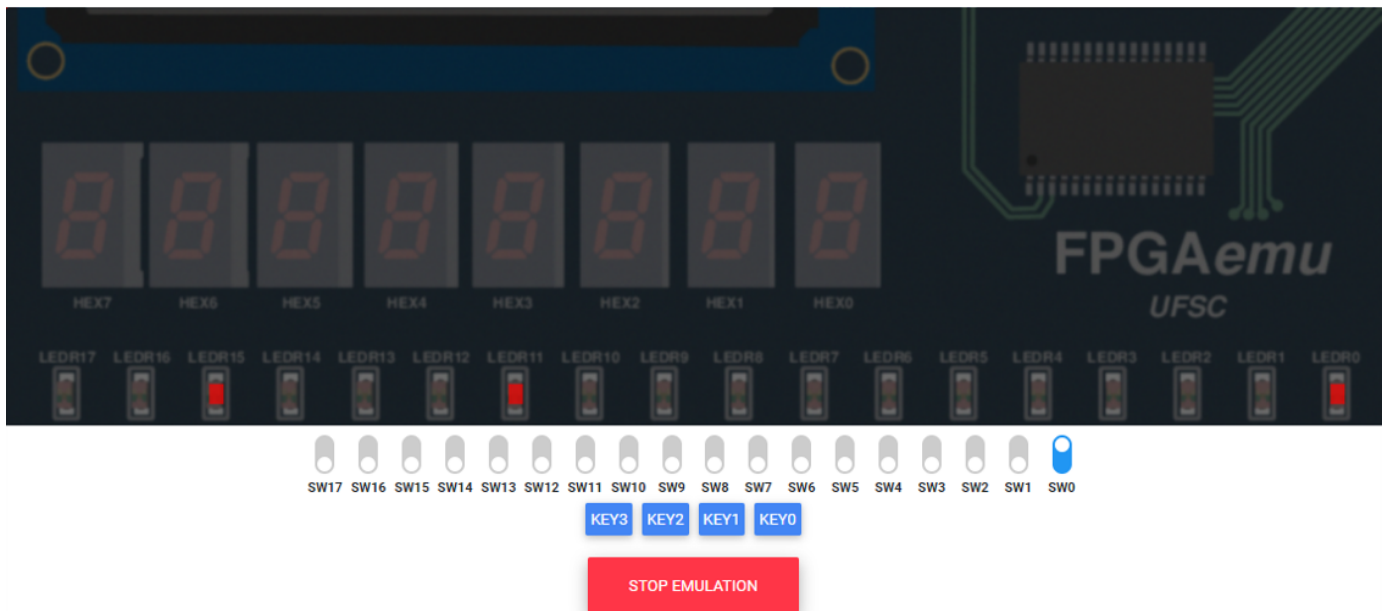
**Matrícula:** 16103372

## Diagrama



Sim, houve ganho uma vez que não há a necessidade de se utilizar diversas etapas de soma a fim de que realize a multiplicação proposta pelas constantes.

## ***Printscreens da emulação na placa***



88888888

HEX7HEX6HEX5HEX4HEX3HEX2HEX1HEX0

LED17LED16LED15LED14LED13LED12LED11LED10LED9LED8LED7LED6LED5LED4LED3LED2LED1LED0

SW17SW16SW15SW14SW13SW12SW11SW10SW9SW8SW7SW6SW5SW4SW3SW2SW1SW0

KEY3KEY2KEY1KEY0

STOP EMULATION

FPGAemu

UFSC