



pin number	STM32 pin	library usage	GPIO capabilities
1	GND	-	-
2	PA15	UART1 TX [uart.c/h]	in/out
3	PB7	UART1 RX [uart.c/h]	in/out
4	3V3	-	-
5	3V3	-	-
6	PA0	ADC_0 [ADC.c/h]	in
7	PA1	ADC_1 [ADC.c/h]	in
8	PC0	ADC_2 [ADC.c/h]	in
9	PC1	ADC_3 [ADC.c/h]	in
10	PC2	ADC_4 [ADC.c/h]	in
11	PC3	ADC_5 [ADC.c/h]	in
12	GND	-	-
13	3V3	-	-
14	-	-	op-amp_1 +
15	-	-	op-amp_1 out
16	-	-	op-amp_1 -
17	-	-	op-amp_2 out
18	-	-	op-amp_2 +
19	-	-	op-amp_2 -
20	GND	-	-
21	GND	-	-
22	PA7	-	in
23	PA6	-	in
24	3V3	-	-
25	GND	-	-
26	PC12	-	in
27	PC5	-	in
28	3V3	-	-
29	GND	-	-
30	PC4	-	in
31	PD2	-	in

pin number	STM32 pin	library usage	GPIO capabilities
32	3V3	-	-
33	GND	-	-
34	PB12	-	out
35	PB13	-	out
36	PB15	-	out
37	PB14	-	in
38	3V3	-	-
39	GND	-	-
40	PB5	ENC_B [QEI.h]	in
41	PB4	ENC_A [QEI.h]	in
42	3V3	-	-
43	GND	-	-
44	PA2	-	in/out
45	PA3	-	in/out
46	3V3	-	-
47	3V3	-	-
48	PA8	PWM_0 [pwm.c/h]	out
49	PA9	PWM_1 [pwm.c/h]	out
50	PA10	PWM_2 [pwm.c/h]	out
51	PA11	PWM_3 [pwm.c/h]	out
52	PB6	PWM_4 [pwm.c/h]	out
53	PB8	PWM_5 [pwm.c/h]	out
54	GND	-	-
55	3V3	-	-
56	PB10	SCL [I2C.c/h]	-
57	PB9	SDA [I2C.c/h]	in
58	GND	-	-
59	GND	-	-
60	PC6	UART6 TX [uart.c/h]	in/out
61	PC7	UART6 RX [uart.c/h]	in/out
62	3V3	-	-

