Б. С.	CC[h]	CC[b]			
bruncnd	1	0	0	0	1

С.У.С.	CC[h]	CC[b]			
notPSWSTandFETCH	2	0	0	1	0
brbezadr1	3	0	0	1	1
brjmp2	4	0	1	0	0
bradr2	5	0	1	0	1
brjmp3	6	0	1	1	0
brnotfcbus	7	0	1	1	1
bradr4	8	1	0	0	0

step00 br (if notPSWSTandFETCH then step00)
step01 ldMAR, incPC
step02 rdMEM, ldMDR, br (if notFCBUS then step02)
step03 ldIR31..24
step04 br (if bezadr1 then step13)
step05 ldMAR, incPC
step06 rdMEM, ldMDR, br (if notFCBUS then step06)
step07 ldIR23..16
step08 br (if jmp2 then step13)
step09 br (if adr2 then step12)

step0A ldMAR, incPC
step0B rdMEM, ldMDR, br (if notFCBUS then step0B)
step0C ldIR15..8
step0D br (if jmp3 then step13)
step0E ldMAR, incPC
step0F rdMEM, ldMDR, br (if notFCBUS then step0F)
step10 ldIR7..0
step11 br (if adr4 then step12)
step12 clFETCH, stADDR, br step00
step13 clFETCH, stEXEC, br step00

Адреса	Садржај [h]	ba[h]	cc[h]	Коментар	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
					ba								**cc**				**/**	**stEXEC**	**stADDR**	**clFETCH**	**ldIR7..0**	**ldIR15..8**	**ldIR23..16**	**ldIR31..24**	**ldMDR**	**rdMEM**	**incPC**	**ldMAR**
0h	002000	0	2												1	0												
1h	000003	0	0																								1	1
2h	02700C	2	7								1	0		1	1	1									1	1		
3h	000010	0	0																					1				
4h	133000	13	3					1	0	0	1	1			1	1												
5h	000003	0	0																								1	1
6h	06700C	6	7							1	1	0		1	1	1									1	1		
7h	000020	0	0																				1					
8h	134000	13	4					1	0	0	1	1		1	0	0												
9h	125000	12	5					1	0	0	1	0		1	0	1												
Ah	000003	0	0																								1	1
Bh	0B700C	B	7						1	0	1	1		1	1	1									1	1		
Ch	000040	0	0																			1						
Dh	136000	13	6					1	0	0	1	1		1	1	0												
Eh	000003	0	0																								1	1
Fh	0F700C	F	7						1	1	1	1		1	1	1									1	1		
10h	000080	0	0																		1							
11h	128000	12	8					1	0	0	1	0	1	0	0	0												
12h	001300	0	1													1			1	1								
13h	001500	0	1													1		1		1								