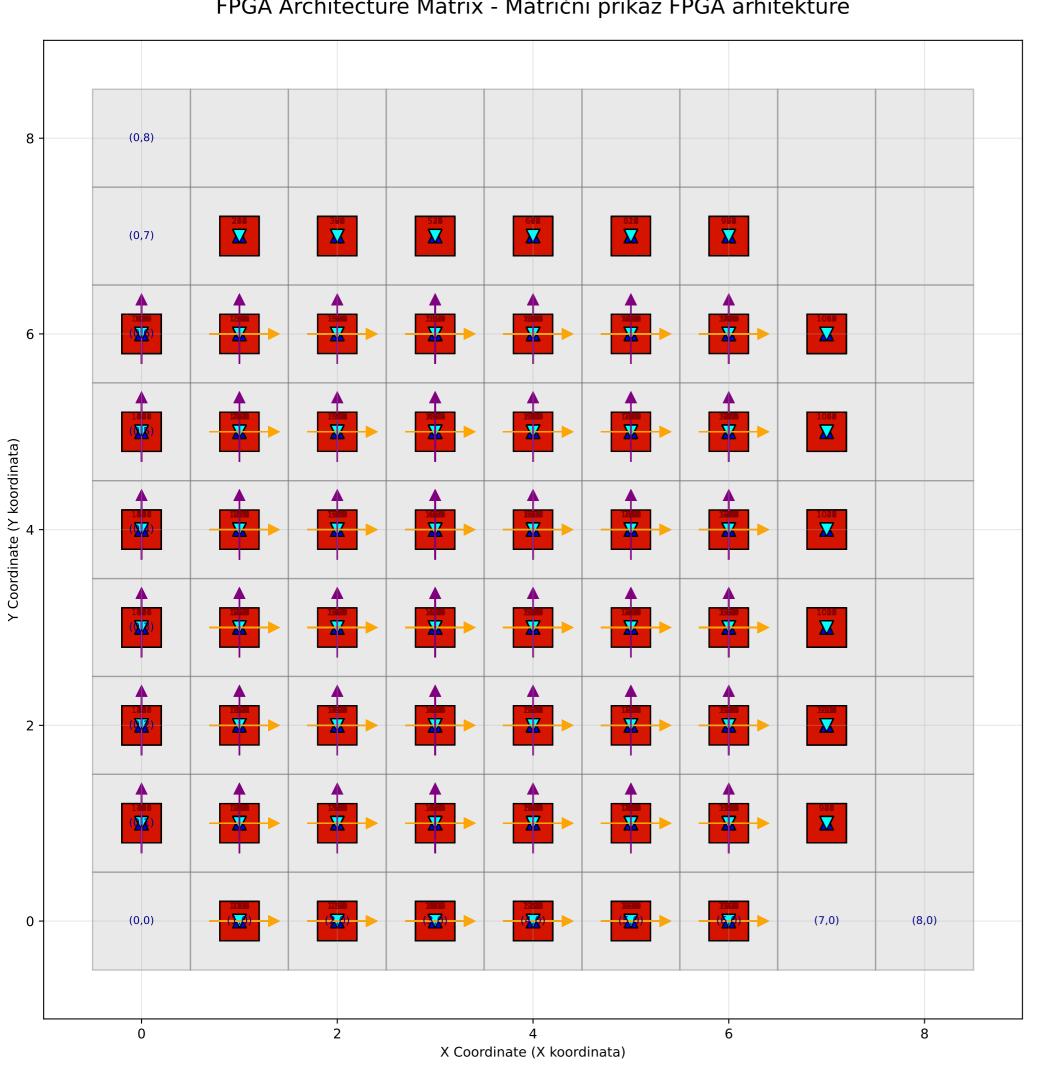
FPGA Architecture Matrix - Matrični prikaz FPGA arhitekture



LEGENDA - Objašnjenje simbola LEGEND - Symbol explanations

SOURCE: Output port (izlaz logičkog bloka)

SINK: Input port (ulaz logičkog bloka)

OPIN: Output pin (izlazni pin)

IPIN: Input pin (ulazni pin)

CHANX: Horizontal channel (horizontalni kanal)

CHANY: Vertical channel (vertikalni kanal)

Routed path (ruta signala) Routing node (čvor rute)