



ALLOY*: General-Purpose Higher-Order Relational Constraint Solver

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ICSE 2015
Florence, Italy

What is ALLOY*

ALLOY*: a more powerful version of the alloy analyzer



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alloy: general-purpose relational specification language

alloy analyzer: automated bounded solver for alloy

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typical uses of the alloy analyzer

- bounded software verification → but no software synthesis
- analyze safety properties of event traces → but no liveness properties
- find a safe full configuration → but not a safe partial conf
- find an instance satisfying a property → but no min/max instance

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higher-order

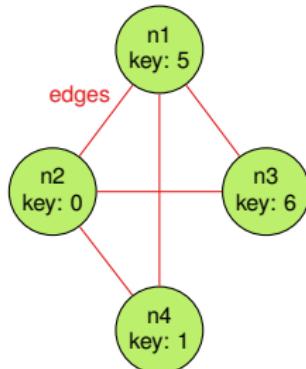
ALLOY*

- capable of automatically solving arbitrary higher-order formulas

First-Order Vs. Higher-Order: clique

first-order: finding a graph and a clique in it

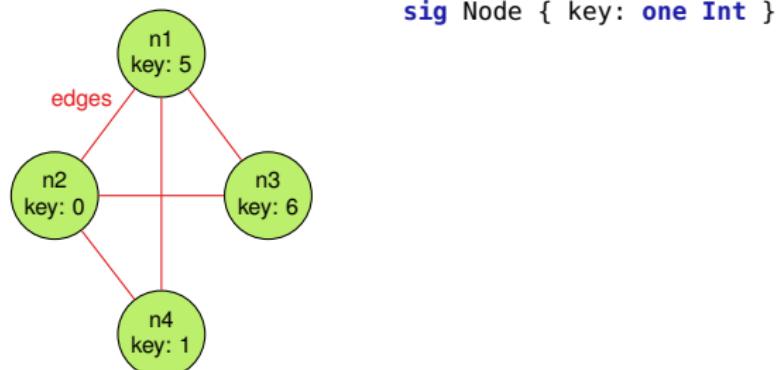
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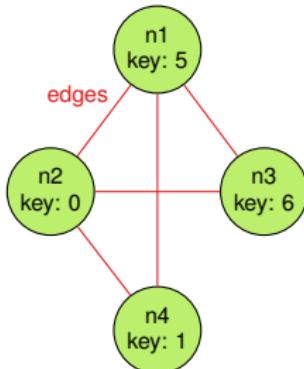
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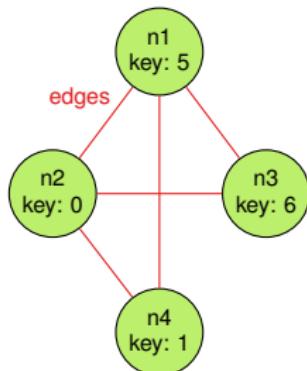
```
sig Node { key: one Int }

run {
    some edges: Node -> Node |
    some clqNodes: set Node |
    clique[edges, clqNodes]
}
```

First-Order Vs. Higher-Order: clique

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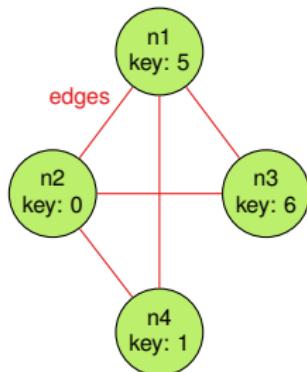
run {
    some edges: Node -> Node |
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    clique[edges, clqNodes]
}

pred clique[edges: Node->Node, clqNodes: set Node] {
    all disj n1, n2: clqNodes | n1->n2 in edges
}
```

First-Order Vs. Higher-Order: clique

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- Alloy Analyzer:** automatic, bounded, relational constraint solver

First-Order Vs. Higher-Order: clique

first-order: finding a graph and a clique in it

- every two nodes in a clique must be connected

The screenshot shows the Alloy Analyzer 4.2 interface. On the left, there is a code editor window containing an Alloy specification. The code defines a node type with a lone integer value, specifies a run constraint involving edges and a clique, and defines a predicate for a clique. On the right, there is a terminal-like window showing the execution logs. The logs show the command "Run run\$1" being executed, followed by several informational messages from the solver (Sig, Solver=minisat(jni), Generating facts, Generating CNF, Generating the solution, Instance found). The logs also mention 833 vars, 105 primary vars, 1303 clauses, and 68 instances found.

```
File Edit Execute Options Window Help
New Open Reload Save Execute Show
sig Node {
    val: lone Int
}

run {
    some edges: Node -> Node |
    some clq: set Node |
        clique[edges, clq]
}

pred clique[edges: Node->Node, clq: set Node] {
    all disj n1, n2: clq | n1->n2 in edges
}

Alloy Analyzer 4.2_2015-02-22 (build date: 2015-02-22)
Executing "Run run$1"
Sig this/Node scope <= 3
Sig this/Edge scope <= 3
Sig this/Graph scope <= 3
Sig this/Node in [[Node$0], [Node$1], [Node$2]]
Sig this/Edge in [[Edge$0], [Edge$1], [Edge$2]]
Sig this/Graph in [[Graph$0], [Graph$1], [Graph$2]]
Generating facts...
Simplifying the bounds...
Solver=minisat(jni) Bitwidth=4 MaxSeq=4 Skolemization=0
Generating CNF...
Generating the solution...
833 vars. 105 primary vars. 1303 clauses. 108 instances found.
Instance found. Predicate is consistent. 68 instances found.
```

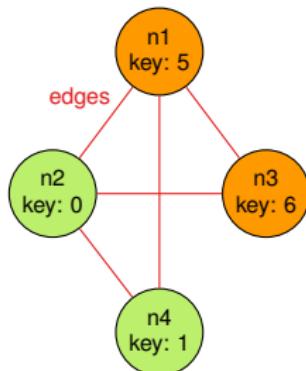
Line 1, Column 1

- Alloy Analyzer:** automatic, bounded, relational constraint solver
- a **solution** (automatically found by Alloy): **clqNodes = { n_1, n_3 }**

First-Order Vs. Higher-Order: **clique**

first-order: finding a graph and a clique in it

- every two nodes in a clique must be connected



```
sig Node { key: one Int }

run {
    some edges: Node -> Node |
    some clqNodes: set Node |
    clique[edges, clqNodes]
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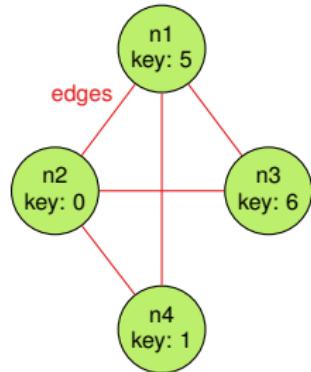
pred clique[edges: Node->Node, clqNodes: set Node] {
    all disj n1, n2: clqNodes | n1->n2 in edges
}
```

- Alloy Analyzer**: automatic, bounded, relational constraint solver
- a **solution** (automatically found by Alloy): **clqNodes** = { n_1, n_3 }

First-Order Vs. **Higher**-Order: `maxClique`

higher-order: finding a graph and a **maximal clique** in it

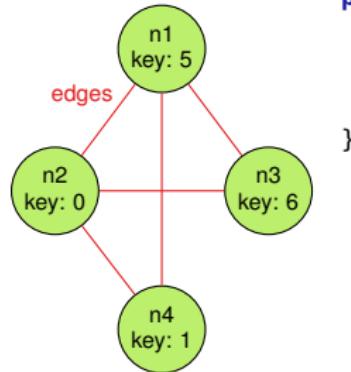
- there is no other clique with more nodes



First-Order Vs. Higher-Order: **maxClique**

higher-order: finding a graph and a **maximal clique** in it

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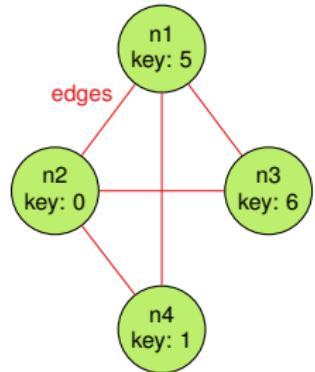


```
pred maxClique[edges: Node->Node, clqNodes: set Node] {  
    clique[edges, clqNodes]  
    all ns: set Node |  
        not (clique[edges, ns] and #ns > #clqNodes)  
}
```

First-Order Vs. Higher-Order: `maxClique`

higher-order: finding a graph and a **maximal clique** in it

- there is no other clique with more nodes



```
pred maxClique[edges: Node->Node, clqNodes: set Node] {
    clique[edges, clqNodes]
    all ns: set Node |
        not (clique[edges, ns] and #ns > #clqNodes)
    }
run {
    some edges: Node -> Node |
    some clqNodes: set Node |
        maxClique[edges, clqNodes]
}
```

First-Order Vs. Higher-Order: **maxClique**

higher-order: finding a graph and a **maximal clique** in it

- there is no other clique with more nodes

expressible but not solvable in Alloy!

The screenshot shows the Alloy Analyzer interface. On the left, the code editor contains an Alloy specification. On the right, the execution log window displays the progress of the analysis and the error message.

Alloy Analyzer 4.2_2015-02-22 (build date: 2015-02-22)

Executing "Run run\$1"

Sig this/Node scope <= 3
Sig this/Node in [[Node\$0], [Node\$1], [Node\$2]]
Generating facts...
Simplifying the bounds...
Solver=minisatprover(jni) Bitwidth=4 MaxSeq=4 Sko
Generating CNF...
Generating the solution...
A type error has occurred: (see the stacktrace)
Analysis cannot be performed since it requires highe
quantification that could not be skolemized.

```
File Edit Execute Options Window Help
New Open Reload Save Execute Show
sig Node { key: Int }

pred clique[edges: Node->Node, clq: set Node] {
    all disj n1, n2: clq | n1->n2 in edges
}

pred maxClique[edges: Node->Node, clq: set Node] {
    clique[edges, clq]
    all ns: set Node |
        not (clique[edges, ns] and #ns > #clq)
}

run { // find a maximal clique in a given graph
let edges = Node -> Node |
some clq: set Node | maxClique[edges, clq]
}
Line 10, Column 7
```

First-Order Vs. Higher-Order: **maxClique**

higher-order: finding a graph and a **maximal clique** in it

- there is no other clique with more nodes

expressible but not solvable in Alloy!

The screenshot shows the Alloy Analyzer interface. On the left, the code editor displays an Alloy model:

```
File Edit Execute Options Window Help
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sig Node { key: Int }

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}

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        not (clique[edges, ns] and #ns > #clq)
}

run { // find a maximal clique in a given graph
let edges = Node -> Node |
some clq: set Node | maxClique[edges, clq]
```

The code defines two predicates: `clique` and `maxClique`. The `clique` predicate takes an edge set and a clique set, stating that all nodes in the clique are connected. The `maxClique` predicate takes an edge set and a clique set, stating that the clique is maximal (no larger clique exists). A `run` block attempts to find a maximal clique in a graph defined by `edges`.

On the right, the execution log window shows the progress of the analysis:

```
Alloy Analyzer 4.2_2015-02-22 (build date: 2015-02-22)

Executing "Run run$1"
Sig this/Node scope <= 3
Sig this/Node in [[Node$0], [Node$1], [Node$2]]
Generating facts...
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Solver=minisatprover(jni) Bitwidth=4 MaxSeq=4 Sko
Generating CNF...
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A type error has occurred: (see the stacktrace)
Analysis cannot be performed since it requires highe
quantification that could not be skolemized.
```

The log indicates that the analysis is executing, generating facts, simplifying bounds, and generating CNF. It then encounters a **type error** due to high quantification that cannot be skolemized, preventing the analysis from proceeding.

- definition** of higher-order (as in Alloy):
 - quantification over all sets of atoms

Solving **maxClique** Vs. Program **Synthesis**

program synthesis	maxClique
find <u>some</u> program AST s.t., for <u>all</u> possible values of its inputs its specification holds	find <u>some</u> set of nodes s.t., it is a clique and for <u>all</u> possible other sets of nodes not one is a larger clique
<code>some</code> program: ASTNode <code>all</code> env: Var -> Val spec[program, env]	<code>some</code> clq: <code>set</code> Node clique[clq] <code>and</code> <code>all</code> ns: <code>set</code> Node <code>not</code> (clique[ns] <code>and</code> #ns > #clq)

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similarities:

- the same **some/all** ($\exists\forall$) pattern
- the **all** quantifier is higher-order

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how do existing program synthesizers work?

CEGIS: A Common Approach for Program Synthesis

original synthesis formulation

```
run { some prog: ASTNode | all env: Var -> Val | spec[prog, env] }
```

Counter-Example Guided Inductive Synthesis

[Solar-Lezama, ASPLOS'06]

CEGIS: A Common Approach for Program Synthesis

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Counter-Example Guided Inductive Synthesis [Solar-Lezama, ASPLOS'06]

1. search: find *some* program and *some* environment s.t. the spec holds, i.e.,

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to get a concrete *candidate* program \$prog

2. verification: check if \$prog holds for *all* possible environments:

```
check { all env: Var -> Val | spec[$prog, env] }
```

Done if verified; else, a concrete *counterexample* \$env is returned as witness.

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2. verification: check if \$prog holds for *all* possible environments:

```
check { all env: Var -> Val | spec[$prog, env] }
```

Done if verified; else, a concrete *counterexample* \$env is returned as witness.
3. induction: *incrementally* find a new program that *additionally* satisfies \$env:

```
run { some prog: ASTNode |
      some env: Var -> Val | spec[prog, env] and spec[prog, $env]}
```

If UNSAT, return no solution; else, go to 2.

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ALLOY* key insight

CEGIS can be applied to solve **arbitrary higher-order** formulas

ALLOY*

generality

- solve **arbitrary** higher-order formulas
- no **domain-specific** knowledge needed

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implementability

- key solver features for **efficient** implementation:
 - *partial instances*
 - *incremental solving*

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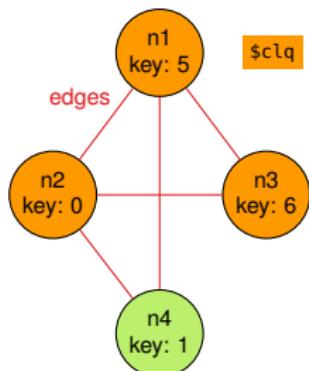
wide applicability (in contrast to specialized synthesizers)

- program synthesis: SyGuS benchmarks
- security policy synthesis: Margrave
- solving graph problems: max-cut, max-clique, min-vertex-cover
- bounded verification: Turán's theorem

Generality: Nested Higher-Order Quantifiers

```
fun keysum[nodes: set Node]: Int {  
    sum n: nodes | n.key  
}
```

```
pred maxMaxClique[edges: Node->Node, clq: set Node] {  
    maxClique[edges, clq]  
    all ns: set Node |  
        not (maxClique[edges, clq2] and  
              keysum[ns] > keysum[clq])  
}  
  
run maxMaxClique for 5
```



```
Executing "Run maxMaxClique for 5"  
Solver=minisat(jni) Bitwidth=5 MaxSeq=5 SkolemDepth=3 Symmetry=20  
13302 vars. 831 primary vars. 47221 clauses. 66ms.  
Solving...  
[Some4All] started (formula, bounds)  
[Some4All] candidate found (candidate)  
[Some4All] verifying candidate (condition, pi) counterexample  
|- [OR] solving splits (formula)  
|- [OR] trying choice (formula, bounds) unsat  
|- [OR] trying choice (formula, bounds) instance  
|- [Some4All] started (formula, bounds)  
|- [Some4All] candidate found (candidate)  
|- [Some4All] verifying candidate (condition, pi) success (#cand = 1)  
searching for next candidate (increment)  
[Some4All] candidate found (candidate)  
[Some4All] verifying candidate (condition, pi) counterexample  
|- [OR] solving splits (formula)  
|- [OR] trying choice (formula, bounds) unsat  
|- [OR] trying choice (formula, bounds) instance  
|- [Some4All] started (formula, bounds)  
|- [Some4All] candidate found (candidate)  
|- [Some4All] verifying candidate (condition, pi) success (#cand = 1)  
searching for next candidate (increment)  
[Some4All] candidate found (candidate)  
[Some4All] verifying candidate (condition, pi) success (#cand = 3)  
|- [OR] solving splits (formula)  
|- [OR] trying choice (formula, bounds) unsat  
|- [OR] trying choice (formula, bounds) unsat  
|- [Some4All] started (formula, bounds)  
Instance found. Predicate is consistent. 490ms.
```

Generality: Checking Higher-Order Properties

Semantics: General Idea

- CEGIS: defined only for a **single** idiom (the $\exists \forall$ formula pattern)

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 - FOL : first-order formula
 - OR : disjunction
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Semantics: General Idea

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- ALLOY*: generalized to **arbitrary** formulas
 1. perform standard transformation: NNF and skolemization
 2. **decompose** arbitrary formula into **known idioms**
 - FOL : first-order formula
 - OR : disjunction
 - $\exists E$: higher-order top-level \forall quantifier (not skolemizable)
 3. **solve** using the following decision procedure
 - FOL : solve directly with Kodkod (first-order relational solver)
 - OR : solve each disjunct separately
 - $\exists E$: apply CEGIS

ALLOY* Implementation **Caveats**

```
some prog: Node |  
acyclic[prog]  
all eval: Node -> (Int+Bool) |  
semantics[eval] implies spec[prog, eval] →  $\exists \forall (conj: \$prog \text{ in } \text{Node} \text{ and } \text{acyclic}[\$prog],$   
 $eQuant: \text{some eval} \dots,$   
 $aQuant: \text{all eval} \dots)$ 
```

ALLOY* Implementation **Caveats**

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some prog: Node |  
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semantics[eval] implies spec[prog, eval]
```

→

$$\exists \forall (\text{conj}: \$\text{prog} \text{ in } \text{Node} \text{ and } \text{acyclic}[\$\text{prog}],
e\text{Quant}: \text{some eval} \dots,
a\text{Quant}: \text{all eval} \dots)$$

1. candidate search

- solve *conj* \wedge *eQuant*
→ *candidate instance* \$cand: values of all relations except *eQuant.var*

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 $eQuant: \text{some eval} \dots,$   
 $aQuant: \text{all eval} \dots)$ 
```

1. candidate search

- solve *conj* \wedge *eQuant*
→ *candidate instance* \$cand: values of all relations except *eQuant.var*

2. verification

- solve $\neg aQuant$ against the \$cand *partial instance*
→ *counterexample* \$cex: value of the *eQuant.var* relation

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```
some prog: Node |  
acyclic[prog]  
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$$\exists \forall (\text{conj}: \$\text{prog} \text{ in } \text{Node} \text{ and } \text{acyclic}[\$\text{prog}],
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partial instance

- partial solution known upfront
- enforced using *bounds*

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 $eQuant: \text{some eval ...},$   
 $aQuant: \text{all eval ...})$ 
```

1. candidate search

- solve *conj* $\wedge eQuant$
→ *candidate instance* \$cand: values of all relations except *eQuant.var*

2. verification

- solve $\neg aQuant$ against the \$cand *partial instance*
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partial instance

- partial solution known upfront
- enforced using *bounds*

3. induction

- use *incremental solving* to add
 replace *eQuant.var* with \$cex in *eQuant.body*
 to previous search condition

ALLOY* Implementation **Caveats**

```
some prog: Node |  
acyclic[prog]  
all eval: Node -> (Int+Bool) |  
semantics[eval] implies spec[prog, eval]
```

→

$$\exists \forall (\text{conj}: \text{$\$prog$ in Node and acyclic[$\$prog$]},
eQuant: \text{some eval ...},
aQuant: \text{all eval ...})$$

1. candidate search

- solve *conj* \wedge *eQuant*
→ *candidate instance* \$cand: values of all relations except *eQuant.var*

2. verification

- solve $\neg aQuant$ against the \$cand *partial instance*
→ *counterexample* \$cex: value of the *eQuant.var* relation

partial instance

- partial solution known upfront
- enforced using *bounds*

3. induction

- use *incremental solving* to add
replace *eQuant.var* with \$cex in *eQuant.body*
to previous search condition

incremental solving

- continue from prev solver instance
- the solver reuses learned clauses

ALLOY* Implementation **Caveats**

```
some prog: Node |  
acyclic[prog]  
all eval: Node -> (Int+Bool) |  
semantics[eval] implies spec[prog, eval]
```

→

```
Ǝ∀(conj: $prog in Node and acyclic[$prog],  
eQuant: some eval ...,  
aQuant: all eval ...)
```

1. candidate search

- solve *conj* \wedge *eQuant*
→ *candidate instance* \$cand: values of all relations except *eQuant.var*

2. verification

- solve \neg *aQuant* against the \$cand *partial instance*
→ *counterexample* \$cex: value of the *eQuant.var* relation

partial instance

- partial solution known upfront
- enforced using *bounds*

3. induction

- use *incremental solving* to add
replace *eQuant.var* with \$cex in *eQuant.body*
to previous search condition

incremental solving

- continue from prev solver instance
- the solver reuses learned clauses

- ? what if the increment formula is not first-order
– optimization 1: use its weaker “first-order version”

ALLOY* Optimization

2. domain constraints

*"for all possible eval,
if the semantics hold then the spec
must hold"*

vs.

*"for all eval that satisfy the semantics,
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pred synth[prog: Node] {  
    all eval: Node -> (Int+Bool) |  
        semantics[eval] implies spec[prog, eval]  
}
```

↓
candidate search

```
some prog: Node |  
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↓
a valid candidate doesn't have to
satisfy the semantics predicate!



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ALLOY* Evaluation

evaluation goals

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1. scalability on classical higher-order graph problems
 - ? does ALLOY* scale beyond “toy-sized” graphs

ALLOY* Evaluation

evaluation goals

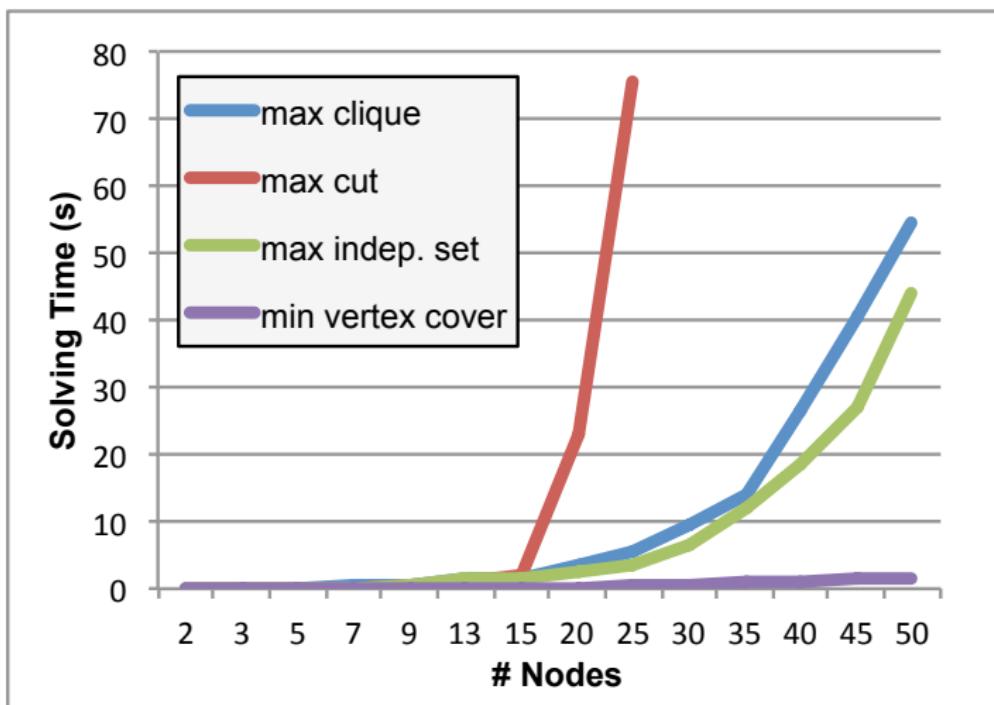
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 - ? does ALLOY* scale beyond “toy-sized” graphs
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 - ? expressiveness: how many SyGuS benchmarks can be written in ALLOY*
 - ? power: how many SyGuS benchmarks can be solved with ALLOY*
 - ? scalability: how does ALLOY* compare to other synthesizers

ALLOY* Evaluation

evaluation goals

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 - ? expressiveness: how many SyGuS benchmarks can be written in ALLOY*
 - ? power: how many SyGuS benchmarks can be solved with ALLOY*
 - ? scalability: how does ALLOY* compare to other synthesizers
3. benefits of the two optimizations
 - ? do ALLOY* optimizations improve overall solving times

Evaluation: **Graph** Algorithms



Evaluation: Program **Synthesis**

expressiveness

- we extended Alloy to support bit vectors
- we encoded **123/173** benchmarks, i.e., all except “ICFP problems”
 - **reason** for skipping ICFP: 64-bit bit vectors (not supported by Kodkod)
 - (aside) not one of them was solved by any of the competition solvers

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- ALLOY* was able to solve **all** different **categories** of benchmarks
 - integer benchmarks, bit vector benchmarks, let constructs, synthesizing multiple functions at once, multiple applications of the synthesized function

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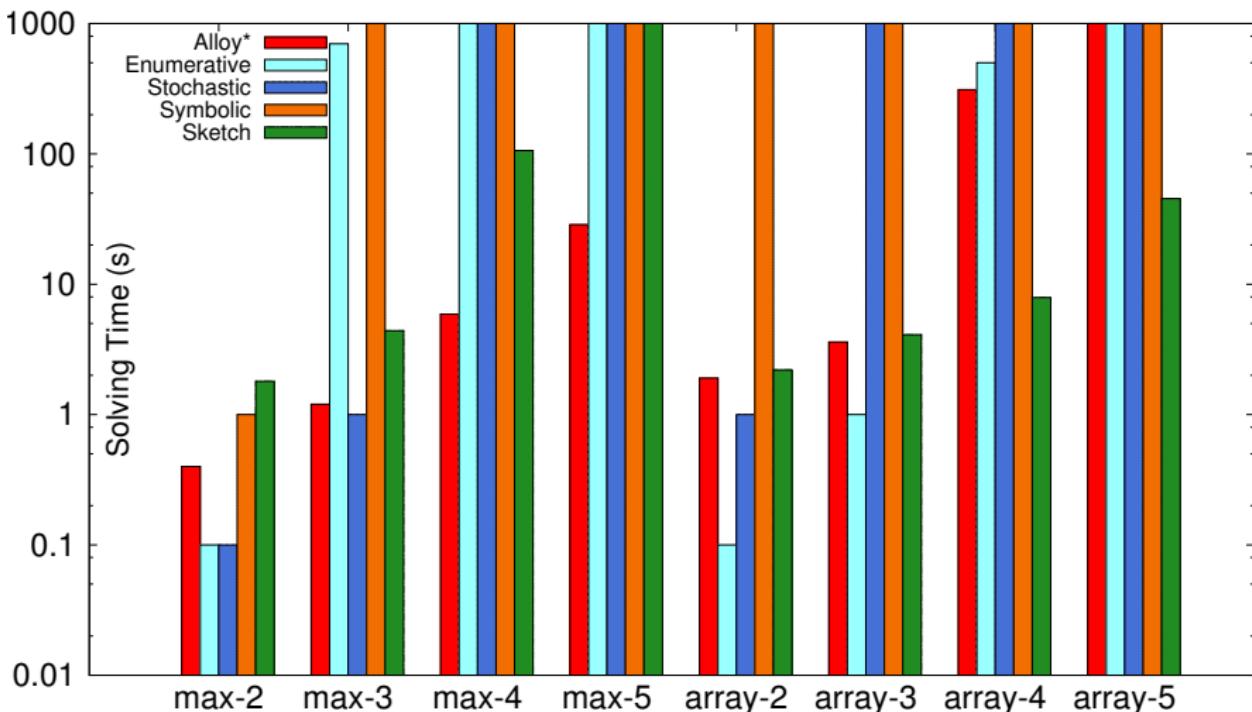
- ALLOY* was able to solve **all** different **categories** of benchmarks
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scalability

- many of the 123 benchmarks are either too easy or too difficult
 - not suitable for scalability comparison
- we primarily used the integer benchmarks
- we also picked a few bit vector benchmarks that were too hard for all solvers

Evaluation: Program **Synthesis**

scalability comparison (integer benchmarks)



Evaluation: Program **Synthesis**

scalability comparison (select bit vector benchmarks)

- benchmarks
 - parity-AIG-d1: full parity circuit using AND and NOT gates
 - parity-NAND-d1: full parity circuit using AND always followed by NOT

Evaluation: Program **Synthesis**

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parity-AIG-d1	parity-NAND-d1
<pre>sig AIG extends BoolNode { left, right: one BoolNode invLhs, invRhs, invOut: one Bool } pred aig_semantics[eval: Node->(Int+Bool)] { all n: AIG eval[n] = ((eval[n.left] ^ n.invLhs) && (eval[n.right] ^ n.invRhs)) ^ n.invOut} run synth for 0 but -1..0 Int, exactly 15 AIG</pre>	<pre>sig NAND extends BoolNode { left, right: one BoolNode } pred nand_semantics[eval: Node->(Int+Bool)] { all n: NAND eval[n] = !(eval[n.left] && eval[n.right]) } run synth for 0 but -1..0 Int, exactly 23 NAND</pre>

Evaluation: Program **Synthesis**

scalability comparison (select bit vector benchmarks)

- benchmarks
 - parity-AIG-d1: full parity circuit using AND and NOT gates
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Evaluation: Benefits of ALLOY* Optimizations

	base	w/ optimizations
max2	0.4s	0.3s
max3	7.6s	0.9s
max4	t/o	1.5s
max5	t/o	4.2s
max6	t/o	16.3s
max7	t/o	163.6s
max8	t/o	987.3s
array-search2	140.0s	1.6s
array-search3	t/o	4.0s
array-search4	t/o	16.1s
array-search5	t/o	485.6s

	base	w/ optimizations
turan5	3.5s	0.5s
turan6	12.8s	2.1s
turan7	235.0s	3.8s
turan8	t/o	15.0s
turan9	t/o	45.0s
turan10	t/o	168.0s

ALLOY* Conclusion

ALLOY* is

- general purpose constraint solver
- capable of efficiently solving arbitrary higher-order formulas
- sound & complete within given bounds



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higher-order and alloy historically

- bit-blasting higher-order quantifiers: attempted, deemed intractable
- previously many ad hoc mods to alloy
 - aluminum, razor, staged execution, ...

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why is this important?

- accessible to wider audience, encourages new applications
- potential impact
 - abundance of tools that build on Alloy/Kodkod, for testing, program analysis, security, bounded verification, executable specifications, ...

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Thank You!

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<http://alloy.mit.edu/alloy/hola>

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First-Order Vs. Higher-Order: clique

first-order: finding a clique in a graph

First-Order Vs. Higher-Order: clique

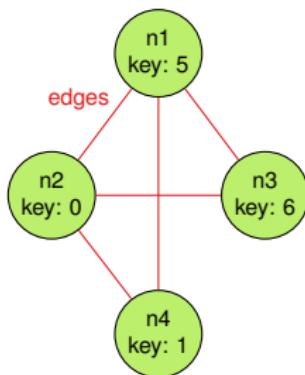
first-order: finding a clique in a graph

```
pred clique[edges: Node->Node, clq: set Node] {
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First-Order Vs. Higher-Order: clique

first-order: finding a **clique** in a graph

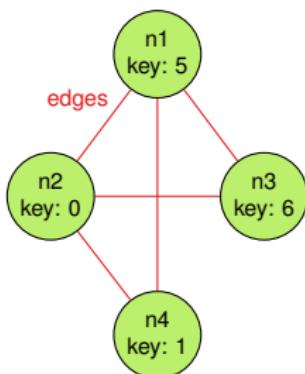
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run { // find a clique in a given graph
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Alloy encoding:

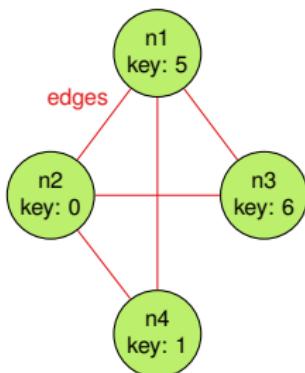
N1: {n1} | N2: {n2} | N3: {n3} | N4: {n4}

atoms

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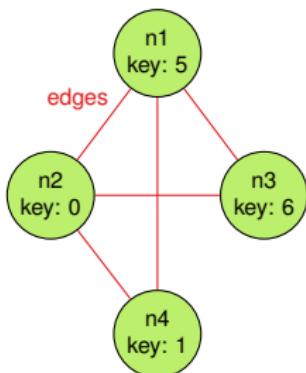
Node: {n1,n2,n3,n4}
key: {(n1 → 5),(n2 → 0),(n3 → 6),(n4 → 1)}
edges: {(n1 → n2),(n1 → n3),(n1 → n4),(n2 → n3),(n2 → n4),
(n2 → n1),(n3 → n1),(n4 → n1),(n3 → n2),(n4 → n2)}

fixed relations

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Alloy encoding:

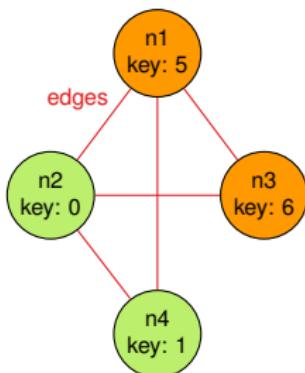
N1: {n1} N2: {n2} N3: {n3} N4: {n4}	atoms
Node: {n1,n2,n3,n4} key: {(n1 → 5), (n2 → 0), (n3 → 6), (n4 → 1)} edges: {(n1 → n2), (n1 → n3), (n1 → n4), (n2 → n3), (n2 → n4), (n2 → n1), (n3 → n1), (n4 → n1), (n3 → n2), (n4 → n2)}	fixed relations
clq: {}, {n1,n2,n3,n4}	relations to be solved

lower bound upper bound → set of nodes: efficiently translated to SAT
(one bit for each node)

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Alloy encoding:

N1: {n1}		N2: {n2}		N3: {n3}		N4: {n4}	→ atoms
Node:	{n1,n2,n3,n4}						
key:	{(n1 → 5),(n2 → 0),(n3 → 6),(n4 → 1)}						→ fixed relations
edges:	{(n1 → n2),(n1 → n3),(n1 → n4),(n2 → n3),(n2 → n4),(n3 → n1),(n3 → n1),(n4 → n1),(n3 → n2),(n4 → n2)}						
clq:	{},		{n1,n2,n3,n4}				relations to be solved
	lower bound		upper bound		→	set of nodes: efficiently translated to SAT (one bit for each node)	

- a **solution** (automatically found by Alloy): **clq** = {n₁, n₃}

First-Order Vs. **Higher**-Order: maxClique

higher-order: finding a maximal clique in a graph

First-Order Vs. **Higher**-Order: maxClique

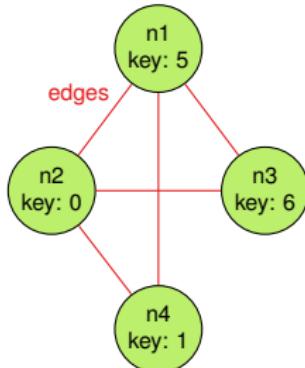
higher-order: finding a maximal clique in a graph

```
pred maxClique[edges: Node->Node,  clq: set Node] {  
    clique[edges, clq]  
    all ns: set Node |  
        not (clique[edges, ns] and #ns > #clq)  
}
```

First-Order Vs. **Higher**-Order: maxClique

higher-order: finding a **maximal clique** in a graph

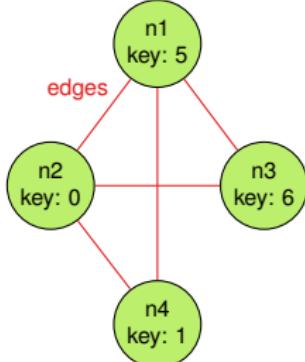
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}
```



expressible but not solvable in Alloy!

```
File Edit Execute Options Window Help
New Open Reload Save Execute Show
sig Node { key: Int }

pred clique[edges: Node->Node, clq: set Node] {
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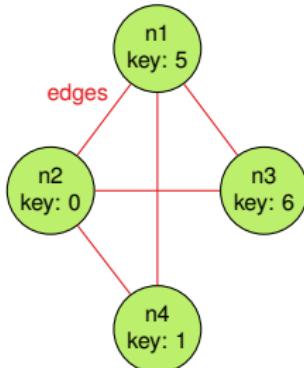
run { // find a maximal clique in a given graph
    let edges = Node -> Node !
    some clq: set Node | maxClique[edges, clq]
}
Line 10, Column 7
```

Alloy Analyzer 4.2_2015-02-22 (build date: 2015-02-2
Executing "Run run\$1"
Sig this/Node scope <= 3
Sig this/Node in ([Node\$0], [Node\$1], [Node\$2])
Generating facts...
Simplifying the bounds...
Solver=minisatprover(jni) Bitwidth=4 MaxSeq=4 Sko
Generating CNF...
Generating the solution...
A type error has occurred: (see the stacktrace)
Analysis cannot be performed since it requires higher
quantification that could not be skolemized.

First-Order Vs. Higher-Order: maxClique

higher-order: finding a maximal clique in a graph

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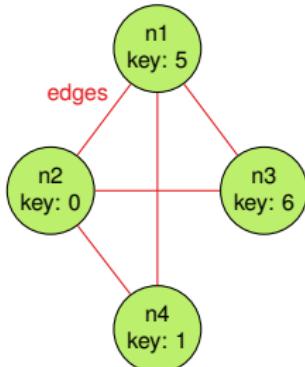


- **definition** of higher-order (as in Alloy):
 - quantification over **all sets** of atoms

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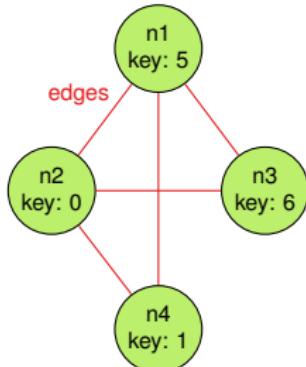


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First-Order Vs. Higher-Order: maxClique

higher-order: finding a maximal clique in a graph

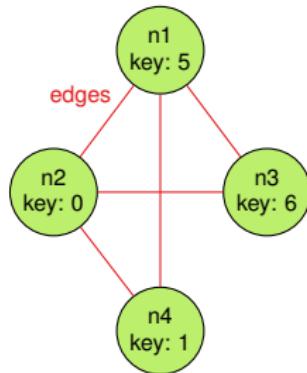
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- **definition** of higher-order (as in Alloy):
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- maxClique: check all possible sets of nodes and ensure not one is a clique larger than clq
- ✗ number of bits required for direct encoding to SAT: $2^{\#Node}$

Solving maxClique: Idea

```
run {
    some clq: set Node |
        clique[edges, clq] and
        all ns: set Node |
            not (clique[edges, ns] and #ns > #clq)
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```

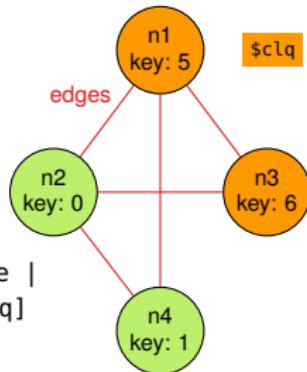


intuitive iterative algorithm

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```
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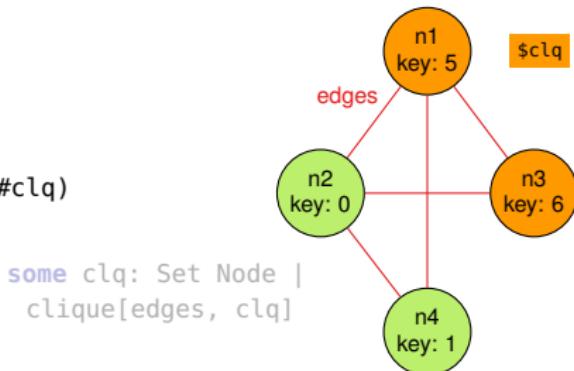


intuitive iterative algorithm

1. find some clique \$clq

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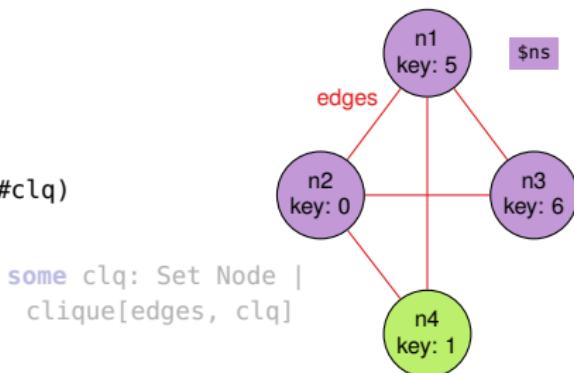
intuitive iterative algorithm

1. find some clique \$clq
2. check if \$clq is maximal
 \Leftrightarrow find some clique \$ns > \$clq from step 1
 - if not found: return \$clq

some ns: Set Node |
clique[edges, ns] and #ns > 2

Solving maxClique: Idea

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    all ns: set Node |
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```



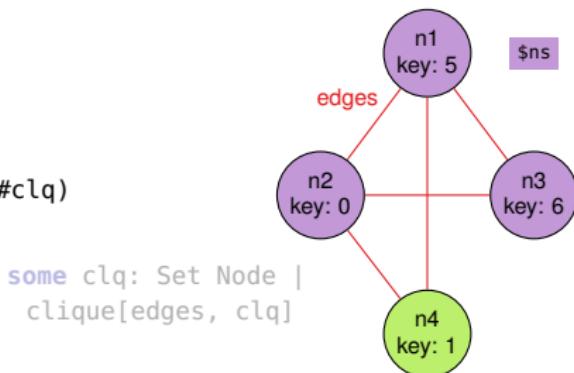
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```
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    some clq: set Node |
        clique[edges, clq] and
    all ns: set Node |
        not (clique[edges, ns] and #ns > #clq)
}
```

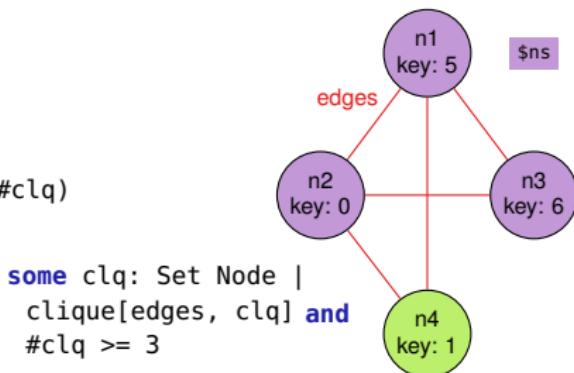


intuitive iterative algorithm

1. **find some clique \$clq**
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Solving maxClique: Idea

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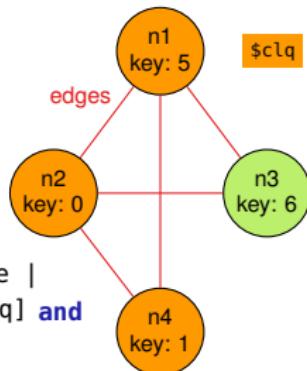
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Solving maxClique: Idea

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run {
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        all ns: set Node |
            not (clique[edges, ns] and #ns > #clq)
}
```

```
some clq: Set Node |
    clique[edges, clq] and
    #clq >= 3
```

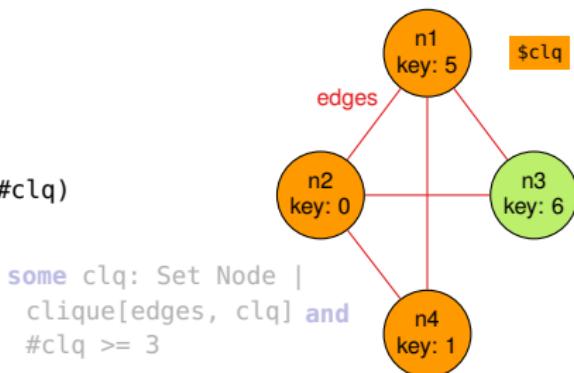


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Solving maxClique: Idea

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intuitive iterative algorithm

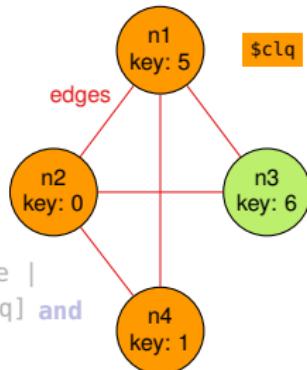
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some ns: Set Node |
clique[edges, ns] and #ns > 3

Solving maxClique: Idea

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run {
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```



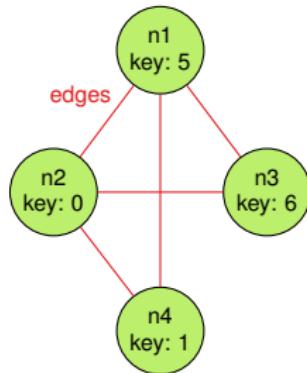
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```
some ns: Set Node |
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UNSAT → return $clq
```

Solving maxClique: Idea

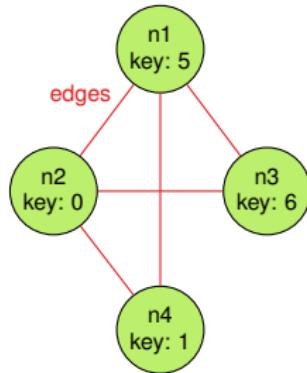
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intuitive iterative algorithm

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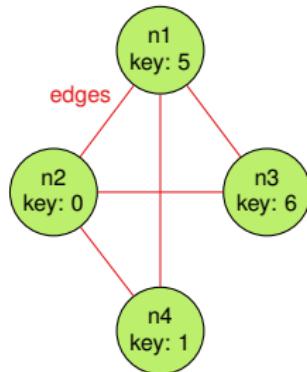


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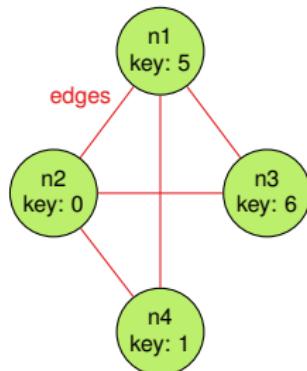


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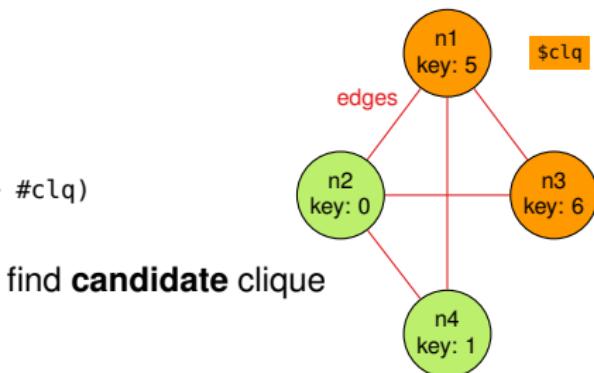


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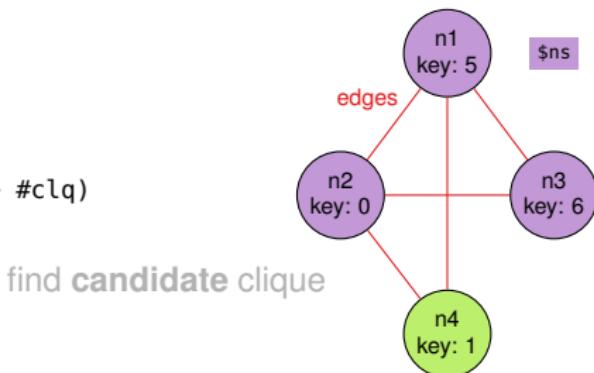


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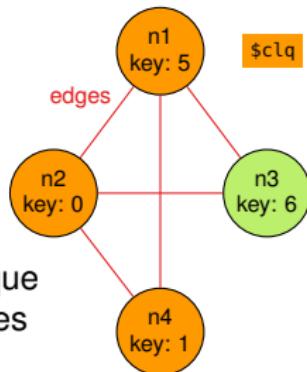
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→ counterexample: \$ns

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find **candidate** clique
with at least 3 nodes



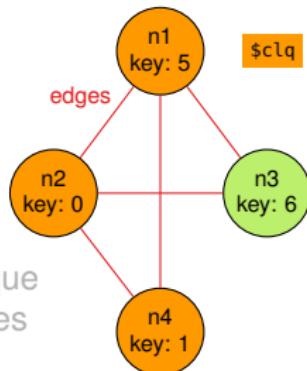
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intuitive iterative algorithm

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UNSAT \longrightarrow return $\$clq$

CEGIS: A Common Approach for Program Synthesis

original synthesis formulation

```
run { some prog: ASTNode | all env: Var -> Val | spec[prog, env] }
```

Counter-Example Guided Inductive Synthesis

[Solar-Lezama, ASPLOS'06]

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1. search: find *some* program and *some* environment s.t. the spec holds, i.e.,

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Done if verified; else, a concrete *counterexample* \$env is returned as witness.

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Done if verified; else, a concrete *counterexample* \$env is returned as witness.

3. induction: *incrementally* find a new program that *additionally* satisfies \$env:

```
run { some prog: ASTNode |
      some env: Var -> Val | spec[prog, env] and spec[prog, $env]}
```

If UNSAT, return no solution; else, go to 2.

Program **Synthesis** with ALLOY*

Program **Synthesis** with ALLOY*

AST nodes

```
abstract sig Node {}  
abstract sig IntNode, BoolNode extends Node {}  
abstract sig Var extends IntNode {}  
  
sig ITE extends IntNode {  
    cond: one BoolNode,  
    then: one IntNode,  
    elsen: one IntNode  
}  
  
sig GTE extends BoolNode {  
    left: one IntNode,  
    right: one IntNode  
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    left: one IntNode,  
    right: one IntNode  
}
```

program semantics

```
fact acyclic {  
    all x: Node | x !in x.^{cond+then+elsen+left+right}  
}  
  
pred semantics[eval: Node -> (Int+Bool)] {  
    all n: IntNode | one eval[n] and eval[n] in Int  
    all n: BoolNode | one eval[n] and eval[n] in Bool  
    all n: ITE |  
        eval[n.cond] = True implies  
        eval[n.then] = eval[n] else eval[n.elsen] = eval[n]  
    all n: GTE |  
        eval[n.left] >= eval[n.right] implies  
        eval[n] = True else eval[n] = False  
}
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    all n: GTE |  
        eval[n.left] >= eval[n.right] implies  
        eval[n] = True else eval[n] = False  
}
```

generic synthesis predicate

```
// for all 'eval' relations for which the  
// semantics hold, the spec must hold as well  
pred synth[root: Node] {  
    all env: Var -> one Int |  
        some eval: Node -> (Int+Bool) |  
            env in eval and  
            semantics[eval] and  
            spec[root, eval]  
}
```

Program **Synthesis** with ALLOY*

AST nodes

```
abstract sig Node {}  
abstract sig IntNode, BoolNode extends Node {}  
abstract sig Var extends IntNode {}  
  
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}
```

spec for max2 (the only benchmark-specific part)

```
one sig X, Y extends Var {}  
  
// the result is equal to either X or Y and  
// is greater or equal than both  
pred spec[root: Node, eval: Node -> (Int+Bool)] {  
    (eval[root] = eval[X] or eval[root] = eval[Y]) and  
    (eval[root] >= eval[X] and eval[root] >= eval[Y])  
}
```

ALLOY* Execution: Example

1. candidate search

```
facts[] and
some prog: Node |
all env: Var -> one Int |
some eval: Node -> (Int+Bool) |
env in eval and
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```

```
// converted to Proc
existsAll(conj: facts[] and $prog in Node,
          // used for search
          eQuant: some env | some eval ...,
          // used for verification
          aQuant: all env | some eval ...)
```

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2. verification

```
not(all env: Var -> one Int |
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env in eval and
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implemented as
"partial instance"

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```

```
// NNF + skolemized
$env in Node -> Int
all eval: Node -> (Int+Bool) |
!($env in eval) or
!semantics[eval] or
!spec[$prog, eval]
```

implemented as
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ALLOY* Execution: Example

1. candidate search

```
facts[] and
some prog: Node |
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```
// converted to Proc
existsAll(conj): facts[] and $prog in Node,
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eQuant: some env | some eval ...,
// used for verification
aQuant: all env | some eval ...)
```

2. verification

```
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```

```
// converted to Proc
existsAll((conj: $env in Node -> Int,
           // used for search
           eQuant: some eval ...,
           // used for verification
           aQuant: all eval ...))
```

3. induction

```
facts[] and
some prog: Node |
some env: Var -> one Int |
(some eval: Node -> (Int+Bool) |
env in eval && semantics[eval] && spec[prog, eval]) and
(some eval: Node -> (Int+Bool) |
$env_cex in eval && semantics[eval] && spec[prog, eval])
```

- body of *aQuant* from step 1 with env replaced by the concrete value (\$env_cex) from step 2
- implemented using "incremental solving"

Semantics: General Idea

1. convert formula to Negation Normal Form (NNF)

- boolean connectives left: \wedge , \vee , \neg
- negation pushed to leaf nodes
- no negated quantifiers

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3. decompose formula into a tree of FOL, OR, and $\exists\forall$ nodes
 - FOL : first-order formula
 - OR : disjunction
 - $\exists\forall$: higher-order top-level \forall quantifier (not skolemizable)

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3. decompose formula into a tree of **FOL**, **OR**, and **$\exists\forall$** nodes
 - **FOL** : first-order formula
 - **OR** : disjunction
 - **$\exists\forall$** : higher-order top-level \forall quantifier (not skolemizable)
4. solve using the following decision procedure
 - **FOL** : solve directly with Kodkod (first-order relational solver)
 - **OR** : solve each disjunct separately
 - **$\exists\forall$** : apply CEGIS

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)    // first-order formula
| OR(disjs: Proc list)           // list of disjuncts (at least some should be higher-order)
|  $\exists\forall$ (conj: FOL,
  allForm: Formula,             // original  $\forall x, f$  formula
  existsProc: Proc)            // translation of the dual  $\exists$  formula ( $T(\exists x, f)$ )
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      |  $\exists\forall$ (conj: FOL,
                    allForm: Formula,           // original  $\forall x.f$  formula
                    existsProc: Proc)    // translation of the dual  $\exists$  formula ( $\mathcal{T}(\exists x.f)$ )
```

\mathcal{T} : Formula \rightarrow Proc // translates arbitrary formula to a tree of Procs

```
let  $\mathcal{T}$  =  $\lambda(f).$ 
```

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)           // first-order formula
      | OR(disjs: Proc list)            // list of disjuncts (at least some should be higher-order)
      |  $\exists\forall$ (conj: FOL,
                    allForm: Formula,          // original  $\forall x, f$  formula
                    existsProc: Proc)        // translation of the dual  $\exists$  formula ( $\mathcal{T}(\exists x, f)$ )
```

$\mathcal{T} : \text{Formula} \rightarrow \text{Proc}$ // translates arbitrary formula to a tree of Procs

```
let  $\mathcal{T} = \lambda(f) .$ 
    let  $f_{nnf} = \text{skolemize}(nnf(f))$ 
```

- convert to NNF and skolemize

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)           // first-order formula
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```

\mathcal{T} : Formula \rightarrow Proc // translates arbitrary formula to a tree of Procs

```
let  $\mathcal{T}$  =  $\lambda(f).$ 
  let  $f_{nnf}$  = skolemize(nnf( $f$ ))
  match  $f_{nnf}$  with
  |  $\neg f_s$      $\rightarrow$  FOL( $f_{nnf}$ )
```

translating negation

- negation can be only in leaves
 \Rightarrow must be first-order

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)           // first-order formula
      | OR(disjs: Proc list)            // list of disjuncts (at least some should be higher-order)
      |  $\exists A$ (conj: FOL,
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```

\mathcal{T} : Formula \rightarrow Proc // translates arbitrary formula to a tree of Procs

```
let  $\mathcal{T}$  =  $\lambda(f).$ 
  let  $f_{nnf}$  = skolemize( $nnf(f)$ )
  match  $f_{nnf}$  with
  |  $\neg f_s$        $\rightarrow$  FOL( $f_{nnf}$ )
  |  $\exists x \cdot f_s$   $\rightarrow$  fail "can't happen"
```

translating the \exists quantifier

- there can't be top-level \exists quantifiers after skolemization

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)           // first-order formula
      | OR(disjs: Proc list)             // list of disjuncts (at least some should be higher-order)
      | ∃A(conj: FOL,
            allForm: Formula,          // original  $\forall x.f$  formula
            existsProc: Proc)          // translation of the dual  $\exists$  formula ( $\mathcal{T}(\exists x.f)$ )
```

$\mathcal{T}: \text{Formula} \rightarrow \text{Proc}$ // translates arbitrary formula to a tree of Procs

```
let T = λ(f) ·
  let fnnf = skolemize(nnf(f))
  match fnnf with
  | ¬fs → FOL(fnnf)
  | ∃x · fs → fail "can't happen"
  | ∀x · fs → let p = T(∃x · fs)
    if (x.mult = SET) || ¬(p is FOL)
      ∃A(FOL(true), fnnf, p)
    else
      FOL(fnnf)
```

translating the \forall quantifier

- translate the dual \exists formula first (where the \exists quantifier will be skolemizable)
- if multiplicity of this \forall quantifier is SET or the dual is not first-order
 - then: f_{nnf} is higher-order
→ create $\exists A$ node
 - else: f_{nnf} is first-order
→ create FOL node

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)           // first-order formula
      | OR(disjs: Proc list)             // list of disjuncts (at least some should be higher-order)
      | ∃A(conj: FOL,
            allForm: Formula,           // original  $\forall x.f$  formula
            existsProc: Proc)          // translation of the dual  $\exists$  formula ( $\mathcal{T}(\exists x.f)$ )
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let T = λ(f) ·
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  match fnnf with
  | ¬fs → FOL(fnnf)
  | ∃x · fs → fail "can't happen"
  | ∀x · fs → let p = T(∃x · fs)
                  if (x.mult = SET) || ¬(p is FOL)
                  ∃A(FOL(true), fnnf, p)
                  else
                      FOL(fnnf)
  | f1 ∨ f2 → OR([T(f1), T(f2)])
```

translating disjunction

- translate both disjuncts
- skolemization through disjunction is not sound → must create OR node (and later solve each side separately)
- optimization: only if $f_1 \vee f_2$ is first-order as a whole, then it is safe to return $\text{FOL}(f_1 \vee f_2)$

Semantics: Formula Decomposition

```
type Proc = FOL(form: Formula)           // first-order formula
      | OR(disjs: Proc list)            // list of disjuncts (at least some should be higher-order)
      |  $\exists A$ (conj: FOL,
                  allForm: Formula,          // original  $\forall x.f$  formula
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```

\mathcal{T} : Formula \rightarrow Proc // translates arbitrary formula to a tree of Procs

```
let  $\mathcal{T}$  =  $\lambda(f).$ 
  let  $f_{nnf}$  = skolemize(nnf(f))
  match  $f_{nnf}$  with
  |  $\neg f_s \rightarrow$  FOL( $f_{nnf}$ )
  |  $\exists x \cdot f_s \rightarrow$  fail "can't happen"
  |  $\forall x \cdot f_s \rightarrow$  let  $p = \mathcal{T}(\exists x \cdot f_s)$ 
    if (x.mult = SET) ||  $\neg(p \text{ is FOL})$ 
       $\exists A$ (FOL(true),  $f_{nnf}$ , p)
    else
      FOL( $f_{nnf}$ )
  |  $f_1 \vee f_2 \rightarrow$  OR([ $\mathcal{T}(f_1)$ ,  $\mathcal{T}(f_2)$ ])
  |  $f_1 \wedge f_2 \rightarrow$   $\mathcal{T}(f_1) \wedge \mathcal{T}(f_2)$ 
```

translating conjunction

- translate both conjuncts
- compose the two resulting Procs
 - FOL \wedge FOL \rightarrow FOL
 - FOL \wedge OR \rightarrow OR
 - FOL \wedge AE \rightarrow AE
 - OR \wedge OR \rightarrow OR
 - OR \wedge AE \rightarrow OR
 - AE \wedge AE \rightarrow AE

Semantics: Formula **Evaluation**

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance } \mathbf{option}$

let $\mathcal{S} = \lambda(p) .$

Semantics: Formula Evaluation

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance } \texttt{option}$

```
let S = λ(p) .  
  match p with  
  | FOL → solve p.form
```

Semantics: Formula **Evaluation**

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance } \texttt{option}$

```
let S = λ(p) .  
  match p with  
  | FOL → solve p.form  
  | OR   → ... // apply S to each Proc in p.disj; return the first solution found
```

Semantics: Formula Evaluation

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance option}$

```
let S = λ(p) .  
  match p with  
  | FOL → solve p.form  
  | OR → ... // apply S to each Proc in p.disj; return the first solution found  
  | ∃E → let pcand = p.conj ∧ p.existsProc  
    match S(pcand) with  
    | None → None // no candidate solution found ⇒ return UNSAT  
    | Some(cand) → // candidate solution found ⇒ proceed to verify the candidate  
      match S(T(¬p.allForm)) with // try to falsify cand ⇒ must run S against the cand instance  
      | None → Some(cand) // no counterexample found ⇒ cand is the solution  
      | Some(cex) → let q = p.allForm  
        // encode the counterexample as a formula: use only the body of the ∀ quant.  
        // in which the quant. variable is replaced with its concrete value in cex  
        let fcex = replace(q.body, q.var, eval(cex, q.var))  
        // add the counterexample encoding to the candidate search condition  
        S(pcand ∧ T(fcex))
```

Semantics: Formula Evaluation

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance option}$

```
let  $\mathcal{S} = \lambda(p) .$ 
  match  $p$  with
  | FOL → solve  $p.form$ 
  | OR → ... // apply  $\mathcal{S}$  to each Proc in  $p.disj$ ; return the first solution found
  |  $\exists$  → let  $p_{cand} = p.conj \wedge p.existsProc$ 
    match  $\mathcal{S}(p_{cand})$  with
    | None → None // no candidate solution found ⇒ return UNSAT
    | Some( $cand$ ) → // candidate solution found ⇒ proceed to verify the candidate
      → match  $\mathcal{S}(\mathcal{T}(\neg p.allForm))$  with // try to falsify  $cand$  ⇒ must run  $\mathcal{S}$  against the  $cand$  instance
        | None → Some( $cand$ ) // no counterexample found ⇒  $cand$  is the solution
        | Some( $cex$ ) → let  $q = p.allForm$ 
          // encode the counterexample as a formula: use only the body of the  $\forall$  quant.
          // in which the quant. variable is replaced with its concrete value in  $cex$ 
          let  $f_{cex} = replace(q.body, q.var, eval(cex, q.var))$ 
          // add the counterexample encoding to the candidate search condition
           $\mathcal{S}(p_{cand} \wedge \mathcal{T}(f_{cex}))$ 
```

partial instance

encode $cand$ as partial instance

Semantics: Formula Evaluation

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance option}$

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let  $\mathcal{S} = \lambda(p) .$ 
  match  $p$  with
  | FOL → solve  $p.form$ 
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  |  $\exists$  → let  $p_{cand} = p.conj \wedge p.existsProc$ 
    match  $\mathcal{S}(p_{cand})$  with
    | None → None // no candidate solution found ⇒ return UNSAT
    | Some( $cand$ ) → // candidate solution found ⇒ proceed to verify the candidate
      → match  $\mathcal{S}(\mathcal{T}(\neg p.allForm))$  with // try to falsify  $cand$  ⇒ must run  $\mathcal{S}$  against the  $cand$  instance
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          // encode the counterexample as a formula: use only the body of the  $\forall$  quant.
          // in which the quant. variable is replaced with its concrete value in  $cex$ 
          → let  $f_{cex} = \text{replace}(q.body, q.var, \text{eval}(cex, q.var))$ 
            // add the counterexample encoding to the candidate search condition
             $\mathcal{S}(p_{cand} \wedge \mathcal{T}(f_{cex}))$ 
```

partial instance
encode $cand$ as partial instance

counterexample encoding
no domain-specific knowledge necessary

Semantics: Formula Evaluation

$\mathcal{S} : \text{Proc} \rightarrow \text{Instance option}$

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let  $\mathcal{S} = \lambda(p) .$ 
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```

// encode the counterexample as a formula: use only the body of the \forall quant.
// in which the quant. variable is replaced with its concrete value in cex

```
let  $f_{cex} = \text{replace}(q.body, q.var, \text{eval}(cex, q.var))$ 
```

// add the counterexample encoding to the candidate search condition

```
 $\mathcal{S}(p_{cand} \wedge \mathcal{T}(f_{cex}))$ 
```

partial instance
encode $cand$ as partial instance

counterexample encoding
no domain-specific knowledge necessary

incremental solving
add $\mathcal{T}(f_{cex})$ to the existing $\mathcal{S}(p_{cand})$ solver

Optimization 1: Domain Constraints

problem: domain for eval too unconstrained

```
pred synth[root: Node] {  
    all eval: Node -> (Int+Bool) |  
        semantics[eval] implies spec[root, eval]  
}
```

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→ candidate search condition:

```
some root: Node |  
    some eval: Node -> (Int+Bool) |  
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```

- a valid candidate **doesn't** have to **satisfy** the **semantics** predicate!

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- a valid candidate **doesn't** have to **satisfy** the **semantics** predicate!
- although logically correct, takes too many steps to converge

*"for all possible eval,
if the semantics hold then the spec must hold"* vs. *"for all eval that satisfy the semantics,
the spec must hold"*

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- although logically correct, takes too many steps to converge

*“for all possible eval,
if the semantics hold then the spec must hold”* vs. *“for all eval that satisfy the semantics,
the spec must hold”*

solution: add new syntax for domain constraints

```
pred synth[root: Node] {  
    all eval: Node -> (Int+Bool)  
    when semantics[eval] |  
        spec[root, eval]  
}
```

Domain Constraints Semantics

first-order logic semantics

`all x: X when dom[x] | body[x]` \iff `all x: X | dom[x] implies body[x]`

`some x: X when dom[x] | body[x]` \iff `some x: X | dom[x] and body[x]`

Domain Constraints Semantics

first-order logic semantics

`all x: X when dom[x] | body[x]` \iff `all x: X | dom[x] implies body[x]`

`some x: X when dom[x] | body[x]` \iff `some x: X | dom[x] and body[x]`

De Morgan's Laws (consistent with classical logic)

`not (all x: X when dom[x] | body[x])` \iff `some x: X when dom[x] | not body[x]`

`not (some x: X when dom[x] | body[x])` \iff `all x: X when dom[x] | not body[x]`

Domain Constraints Semantics

first-order logic semantics

$$\begin{aligned}\text{all } x: X \text{ when } \text{dom}[x] \mid \text{body}[x] &\iff \text{all } x: X \mid \text{dom}[x] \text{ implies } \text{body}[x] \\ \text{some } x: X \text{ when } \text{dom}[x] \mid \text{body}[x] &\iff \text{some } x: X \mid \text{dom}[x] \text{ and } \text{body}[x]\end{aligned}$$

De Morgan's Laws (consistent with classical logic)

$$\begin{aligned}\text{not } (\text{all } x: X \text{ when } \text{dom}[x] \mid \text{body}[x]) &\iff \text{some } x: X \text{ when } \text{dom}[x] \mid \text{not } \text{body}[x] \\ \text{not } (\text{some } x: X \text{ when } \text{dom}[x] \mid \text{body}[x]) &\iff \text{all } x: X \text{ when } \text{dom}[x] \mid \text{not } \text{body}[x]\end{aligned}$$

changes to the ALLOY* semantics

- converting higher-order \forall to \exists : $\forall x \cdot f \rightarrow \exists x \cdot f$ (domain constraints stay with x)
- encoding a counterexample as a formula: in

```
let fcex = replace(q.body, q.var, eval(cex, q.var))
```

$q.\text{body}$ is expanded according to the first-order semantics above

Optimization 2: First-Order Increments

problem: search space too big, counterexamples not focused

```
pred synth[root: Node] {  
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→

- quantifies over evaluations of Nodes instead of only Vars
- counterexamples encode entire eval relation,
instead of only values of variables

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→

- quantifies over evaluations of Nodes instead of only Vars
- counterexamples encode entire eval relation, instead of only values of variables

idea: rewrite the synth predicate to separate env from eval

```
pred synth[root: Node] {  
    all env: Var -> one Int |  
    some eval: Node -> (Int+Bool)  
    when env in eval && semantics[eval] |  
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}
```

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not (all env: Var -> one Int |
 some eval: Node -> (Int+Bool)
 when env in eval && semantics[eval] |
 spec[\$root, eval])
 \Leftrightarrow
some env: Var -> one Int |
all eval: Node -> (Int+Bool)
when env in eval && semantics[eval] |
not spec[\$root, eval]

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```

- nested CEGIS loops ✓
- higher-order counterexample encoding
→ cannot use incremental solving ✗

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```

- nested CEGIS loops ✓
- higher-order counterexample encoding
→ cannot use incremental solving ✗

solution: force counterexample encodings to be first order

First-Order Increments Semantics

- always translate the counterexample encoding formula to FOL

$$\mathcal{S}(p_{cand} \wedge \mathcal{T}(f_{cex}))$$



$$\mathcal{S}(p_{cand} \wedge \mathcal{T}_{\text{FO}}(f_{cex}))$$

First-Order Increments Semantics

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$$\mathcal{S}(p_{cand} \wedge \mathcal{T}_{\text{fo}}(f_{cex}))$$

- apply the same idea of flipping \forall to \exists to implement \mathcal{T}_{fo}

```
//  $\mathcal{T}_{\text{fo}}$  : Formula → FOL
let  $\mathcal{T}_{\text{fo}}(f)$  = match  $p = \mathcal{T}(f)$  with
  | FOL →  $p$ 
  |  $\exists A$  →  $p.\text{conj} \wedge \mathcal{T}_{\text{fo}}(p.\text{existsProc})$ 
  | OR → FOL(reduce  $\vee$ , (map  $\mathcal{T}_{\text{fo}}$ ,  $p.\text{disjs}$ ).form)
```

First-Order Increments Semantics

- always translate the counterexample encoding formula to FOL

$$\mathcal{S}(p_{cand} \wedge \mathcal{T}(f_{cex}))$$

↓

$$\mathcal{S}(p_{cand} \wedge \mathcal{T}_{\text{fo}}(f_{cex}))$$

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```

- \mathcal{T}_{fo} produces strictly less constrained encoding

First-Order Increments Semantics

- always translate the counterexample encoding formula to FOL

$$\mathcal{S}(p_{cand} \wedge \mathcal{T}(f_{cex}))$$



$$\mathcal{S}(p_{cand} \wedge \mathcal{T}_{\text{fo}}(f_{cex}))$$

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  | OR → FOL(reduce  $\vee$ , (map  $\mathcal{T}_{\text{fo}}$ ,  $p.\text{disjs}$ ).form)
```

- \mathcal{T}_{fo} produces **strictly less constrained** encoding
- potential trade-off:
 - efficient incremental solving vs.
 - more CEGIS iterations (due to weaker encoding)