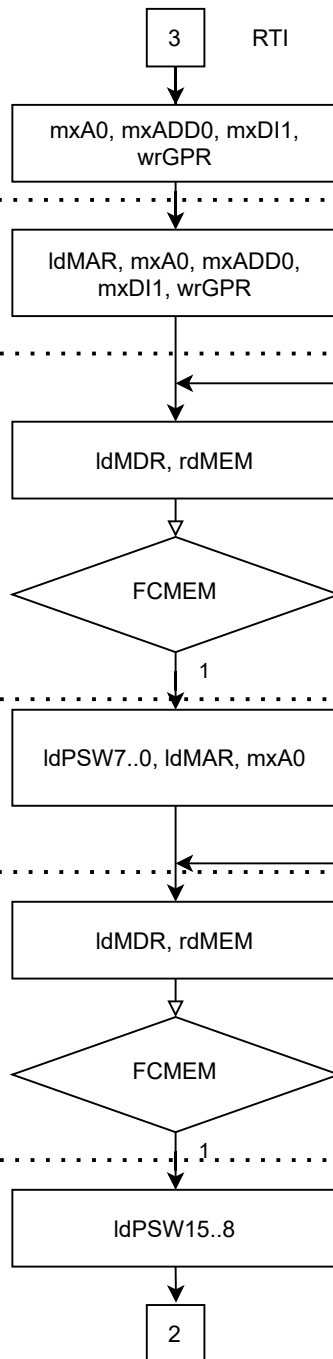
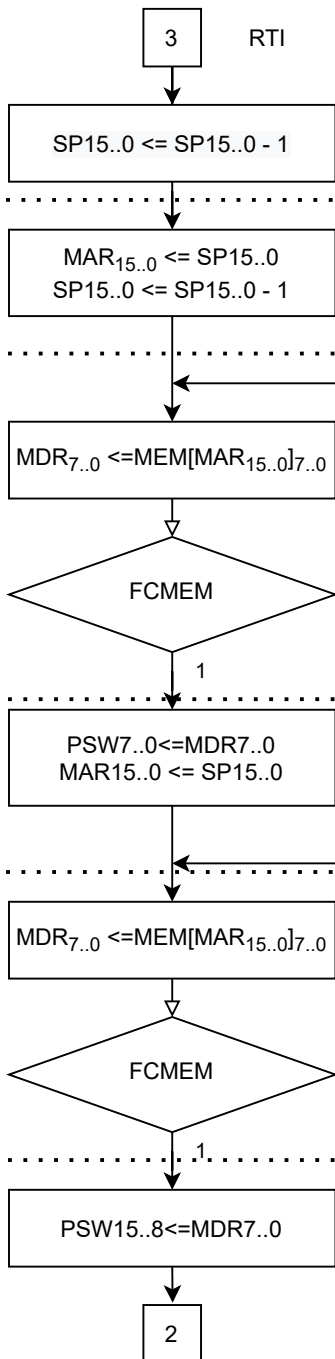


step07 ldMDR, rdMEM
br (if notFCMEM then step07)

step08 ldDWH

step09 selPC, mxDI1, mxDI0, wrGPR
br **step40**



step0A mxA0, mxADD0, mxDI1, wrGPR

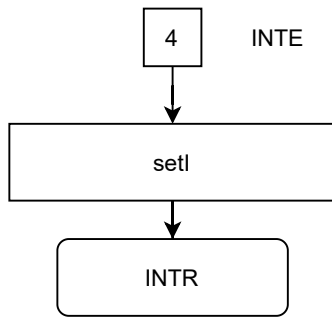
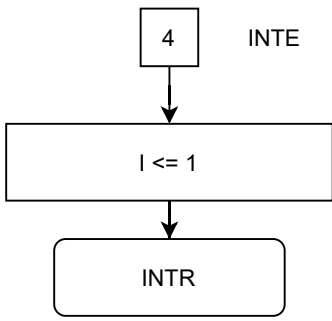
step0B ldMAR, mxA0, mxADD0, mxDI1, wrGPR

step0C ldMDR, rdMEM
br (if notFCMEM then step0C)

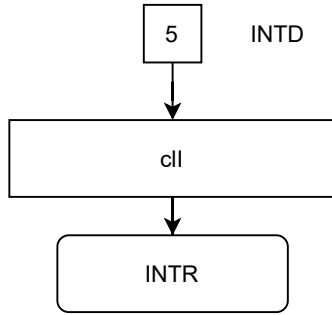
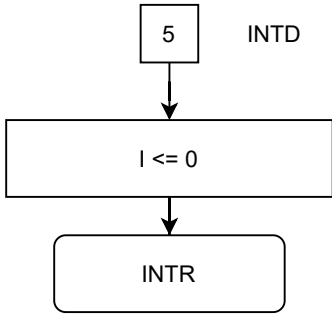
step0D ldPSW7..0, ldMAR, mxA0

step0E ldMDR, rdMEM
br (if notFCMEM then step0E)

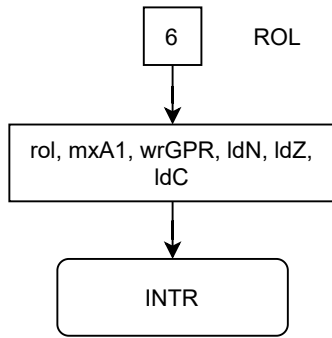
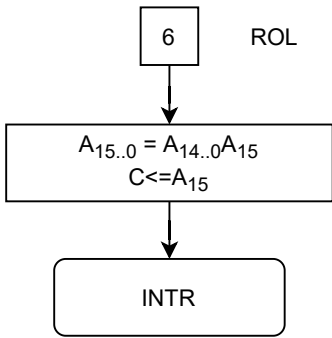
step0F ldPSW15..8
br **step03**



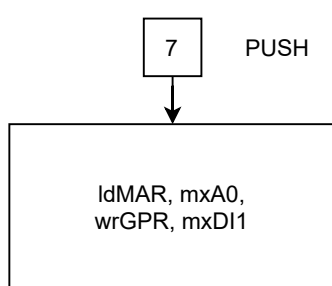
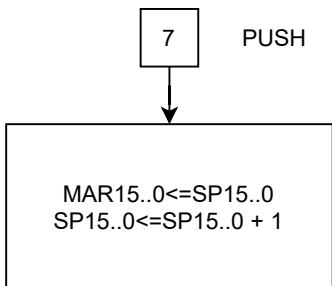
step11 setl
br **step40**



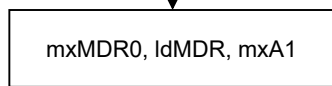
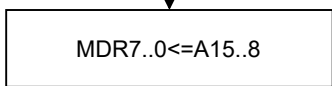
step12 cll
br **step40**



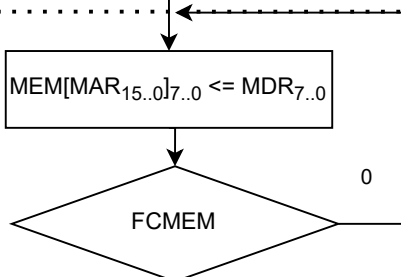
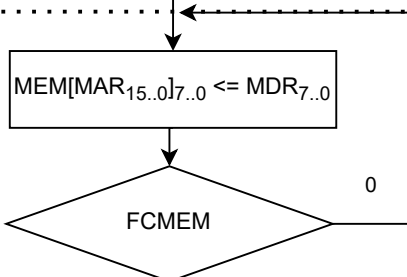
step13 rol, mxA1, wrGPR, ldN, ldZ, ldC
br **step40**



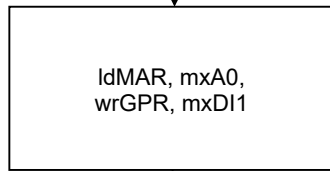
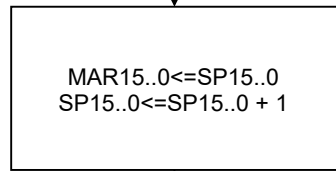
step14 ldMAR, mxA0, wrGPR, mxDI1,



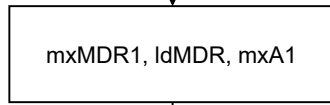
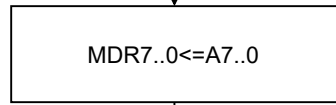
step15 mxMDR0, ldMDR, mxA1



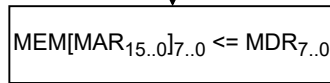
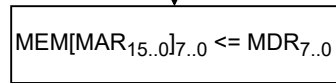
step16 wrMEM
br (if notFCMEM then **step16**)



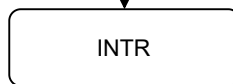
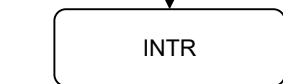
step17 ldMAR, mxA0,
wrGPR, mxDI1



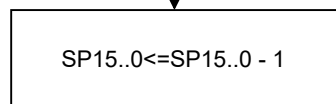
step18 mxMDR1, ldMDR, mxA1



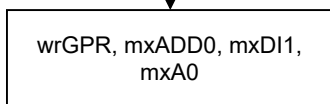
step19 wrMEM
br (not FCMEM then **step19**)



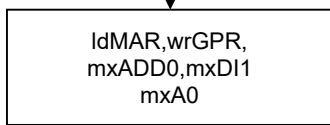
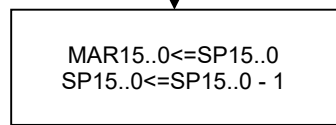
8 POP



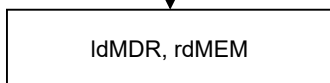
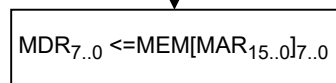
8 POP



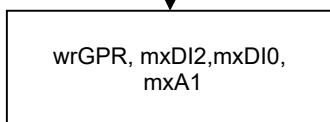
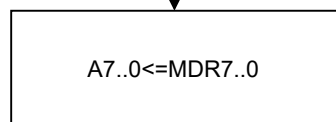
step1A wrGPR, mxADD0, mxDI1, mxA0



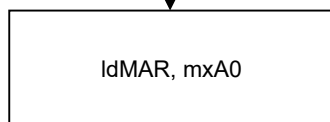
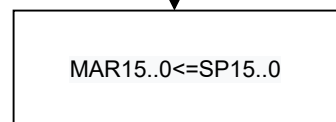
step1B ldMAR, wrGPR, mxADD0, mxDI1
mxA0



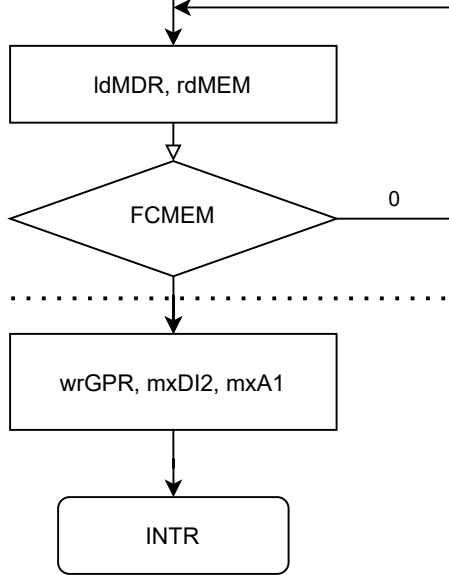
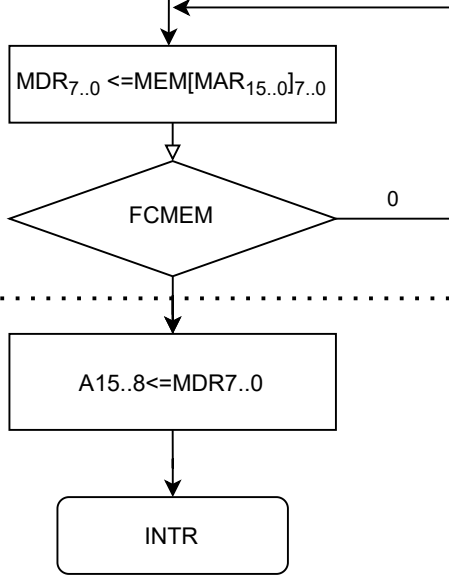
step1C ldMDR, rdMEM
br (if notFCMEM then step1C)



step1D wrGPR, mxDI2, mxA1

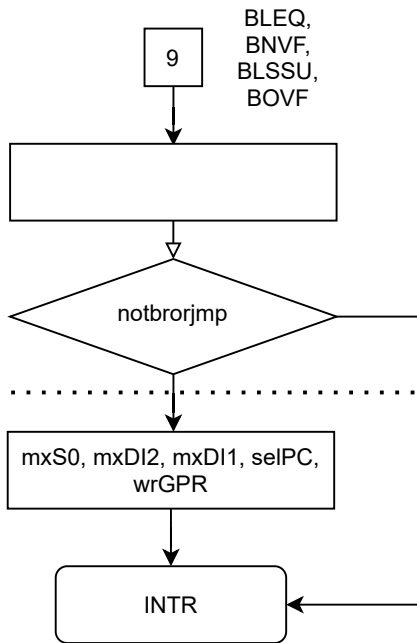
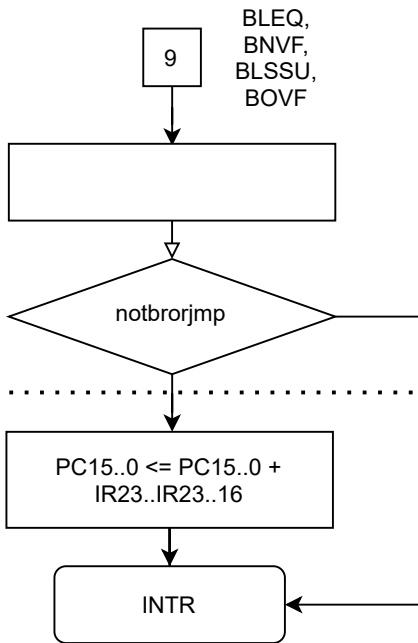


step1E ldMAR, mxA0



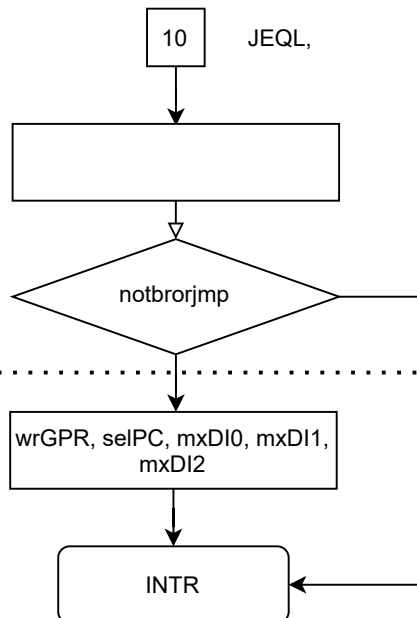
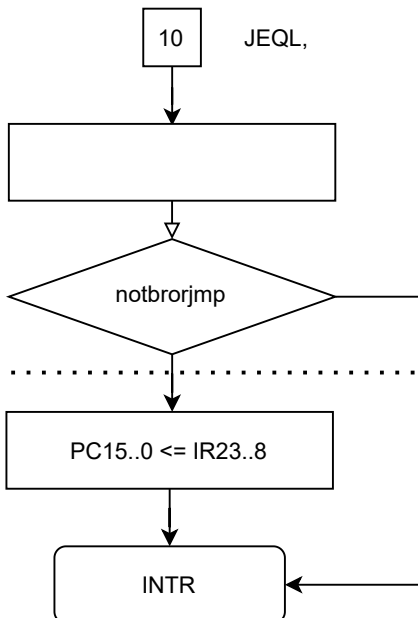
step1F ldMDR, rdMEM
br (if notFCMEM then step1F)

step20 wrGPR, mxDI2, mxDI0, mxA1
br **step40**



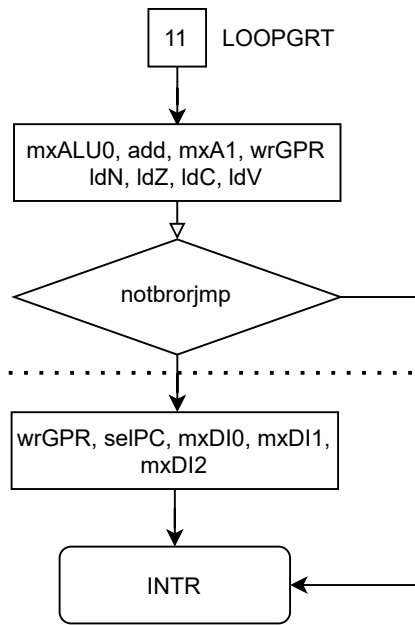
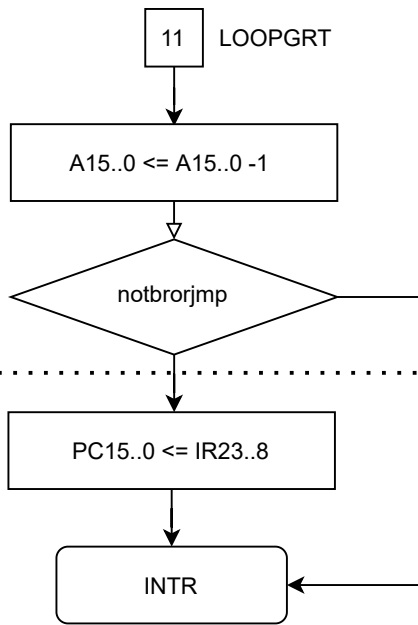
step21 br (if notbrorjmp then **step40**)

step22 mxS0, mxDI2, mxDI1, selPC, wrGPR
br **step40**



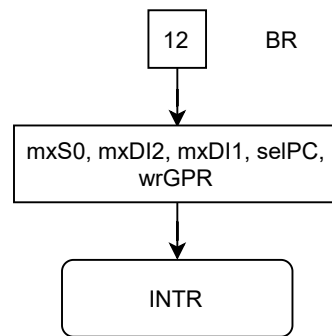
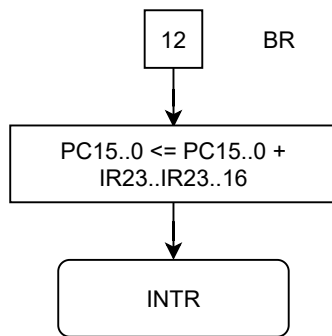
step23 br (if notbrorjmp then **step40**)

step24 wrGPR, selPC, mxDI0, mxDI1, mxDI2
br **step40**

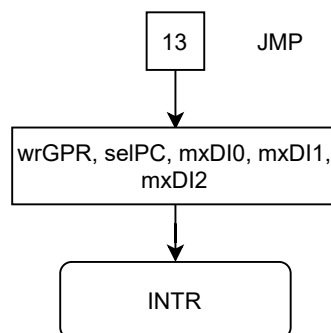
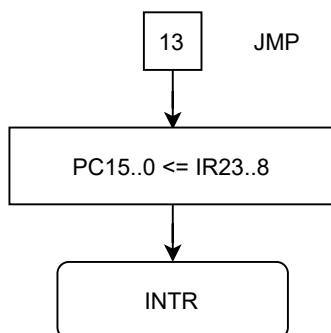


step25 mxALU0, add, mxA1, wrGPR, ,
ldN, ldZ, ldC, ldV
br (if notbrorjmp then **step40**)

step26 wrGPR, selPC, mxDI0, mxDI1,
mxDI2
br **step40**

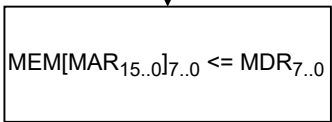
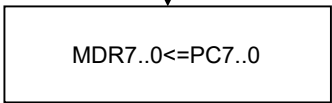
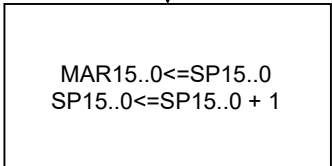
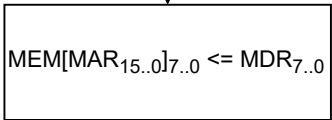
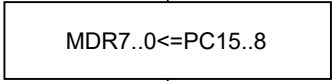
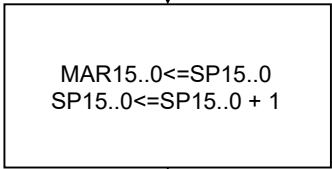


step27 mxS0, mxDI2, mxDI1, selPC,
wrGPR
br **step40**



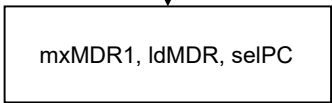
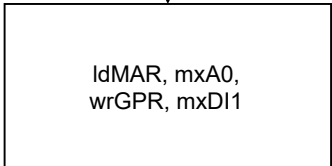
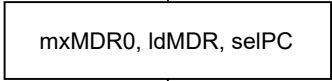
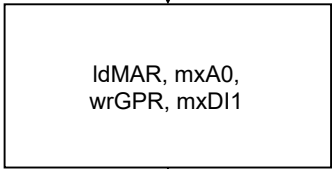
step28 wrGPR, selPC, mxDI0, mxDI1,
mxDI2
br **step40**

14 JSR



13 JMP

14 JSR



13 JMP

step29 ldMAR, mxA0,wrGPR, mxDI1,

step2A mxMDR0, ldMDR, selPC

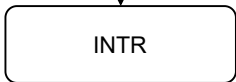
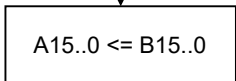
step2B wrMEM

step2C ldMAR, mxA0,
wrGPR, mxDI1

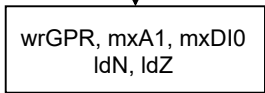
step2D mxMDR1, ldMDR, selPC

step2E wrMEM
br **step28**

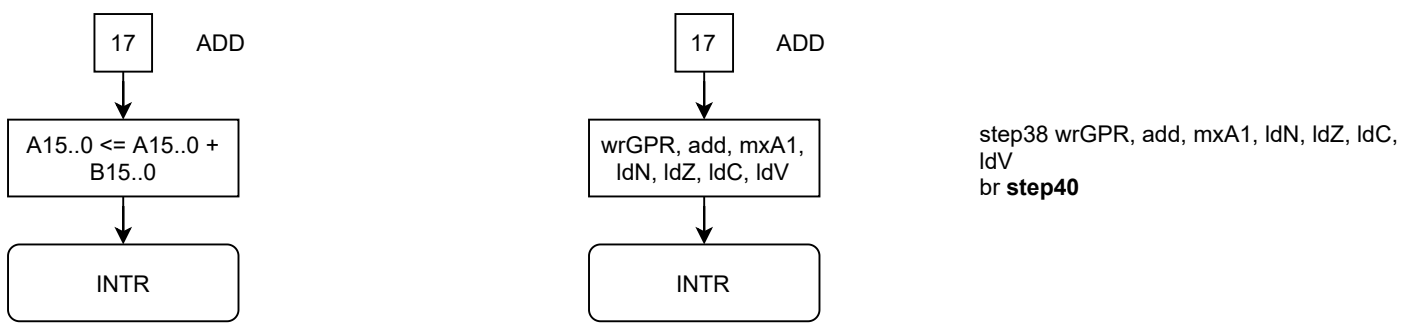
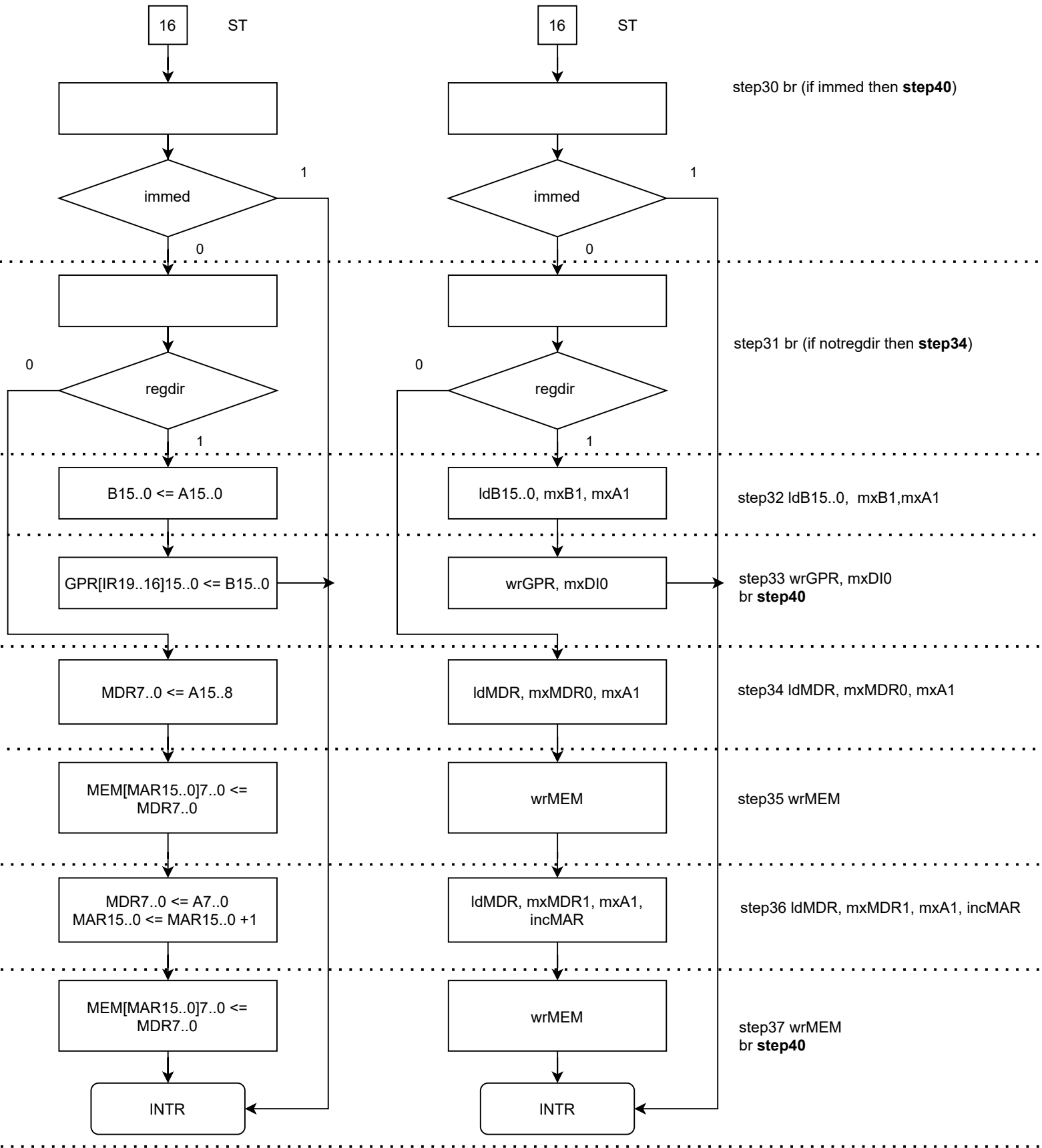
15 LD

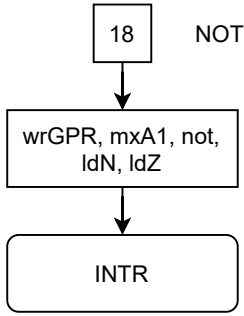
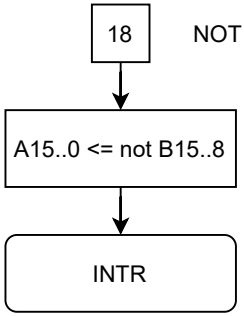


15 LD

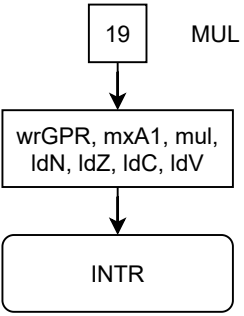
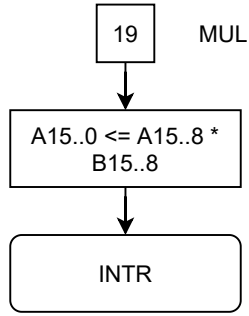


step2F wrGPR, mxA1, mxDI0, ldN, ldZ
br **step40**

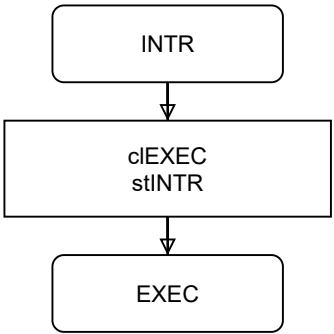
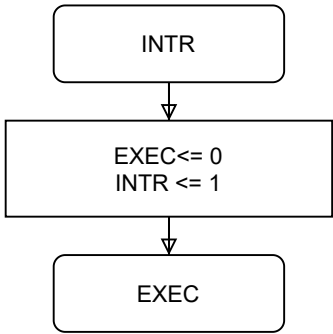




step39 wrGPR, mxA1, not, ldN, ldZ
br **step40**



step3A wrGPR, mxA1, mul, ldN, ldZ, ldC, ldV
br **step40**



step40 clEXEC, stINTR
br **step00**