

step0C br (if STORE then **step18**)

$B_{15..0} \leq GPR[IR_{19..16}]_{15..0}$

EX

ldB_{15..8}, ldB_{7..8},
mxB1

EX

step0D ldB_{15..8}, ldB_{7..0}, mxB1
br **step18**

6 predec

$B_{15..0} \leq GPR[IR_{19..16}]_{15..0}$

6 predec

ldB_{15..8}, ldB_{7..0},
mxB1

step0E ldB_{15..8}, ldB_{7..0}, mxB1

$B_{15..0} \leq B_{15..0} - 1$

decB

step0F decB

$B_{15..0} \leq B_{15..0} - 1$

decB

step10 decB

$GPR[IR_{19..16}]_{15..0} \leq B_{15..0}$
 $MAR_{15..0} \leq B_{15..0}$

wrGPR, mxDI0, ldMAR, mxMAR1

step11 wrGPR, mxDI0, ldMAR,
mxMAR1
br **step13**

8

8

7 regindpom

7 regindpom

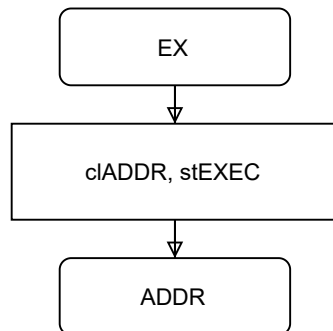
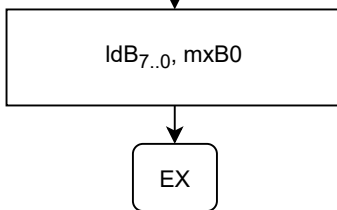
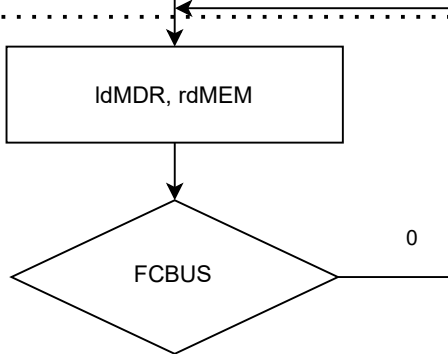
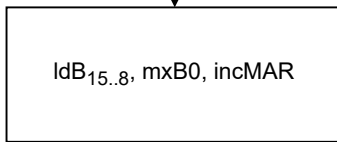
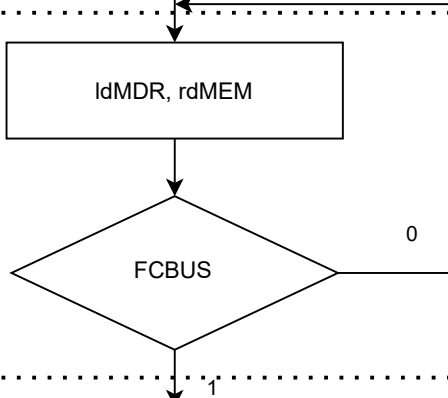
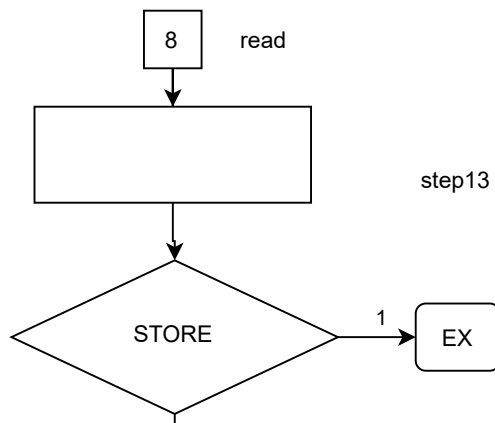
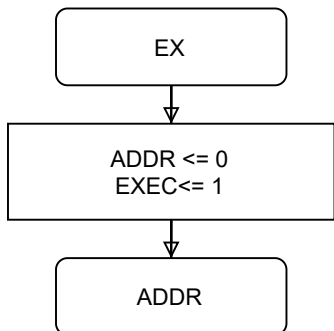
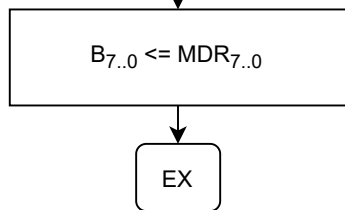
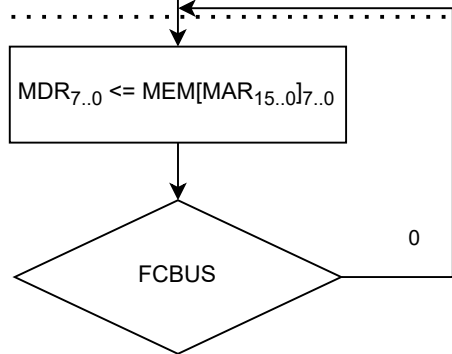
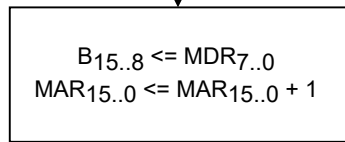
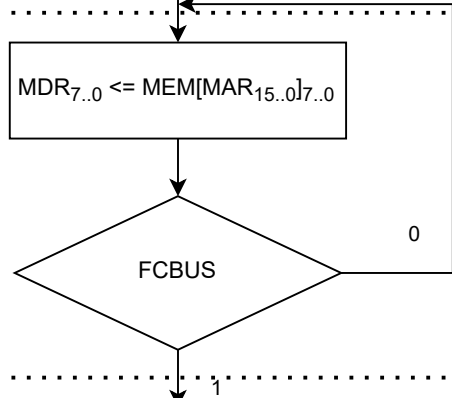
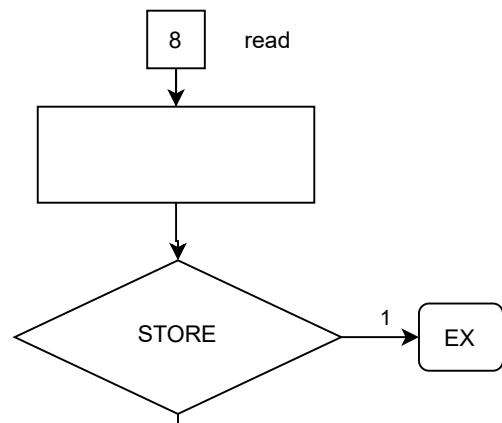
$MAR_{15..0} \leq GPR[IR_{19..16}]_{15..0} + IR_{15..} - IR_{15..8}$

ldMAR, mxMAR1, mxMAR0

step12 ldMAR, mxMAR1, mxMAR0
br step13

8

8



step13 br(if STORE then **step18**)

step14 ldMDR, rdMEM
br (if notFCBUS then step14)

step15 ldB15..8, mxB0, incMAR

step16 ldMDR, rdMEM
br (if notFCBUS then step16)

step17 ldB7..0, mxB0

step18 clADDR, stEXEC
br **step00**