### Alessandro Miragliotta

in linkedin.com/in/alessandro-miragliotta ♀ github.com/alem1r □ +39 3392295326 ℚ alessandro.miragliotta.am@gmail.com

Ex-Qualcomm Intern as Hardware Engineer and recent master graduate in Computer Engineering at Sapienza university of Rome. Graduated with a Bachelor in Computer and Control Engineering at Sapienza university in October 2021. I love System Security and Computer Architecture. Linux and Open Source lover. Interested in learning new technologies and tools if the need arises

## **EDUCATION**

2021-2024 M.S. in Computer Engineering - Sapienza University of Rome

Thesis: Pattern-Based MMIO Modeling and Fuzzing Techniques for Firmware Rehosting: A Case Study on Arm Cortex-M3 based Drone

2018-2021 B.S. in Computer and Control Engineering - Sapienza University of Rome

# WORK EXPERIENCE

September 2022 February 2023

#### Hardware Verification Engineer Intern, QUALCOMM, CORK, Ireland

- > EVA/Video team, developing HW for a custom Vision Processing Unit
- > Developed internal automation tools
- > Regression Tests for verification

C Verilog Python OOP Computer Architecture Verification ClearCase Teamwork Communication

### PROJECTS

#### RISC-V SINGLE CYCLE PROCESSOR

JANUARY 2022 - FEBRUARY 2022

github.com/alem1r/RISC-V-Single-Cycle-Processor

Implementation in Verilog of a RISC-V 32 bit Single Cycle Processor. It supports all the unprivileged instructions.

Computer Architecture | ISA RISC-V | Verilog |

#### ROBOT OPERATING SYSTEM PROGRAM TO LOCATE OBSTACLES

SEPTEMBER 2021 - OCTOBER 2021

github.com/alem1r/Collision-Avoidance-

The aim of this C++ ROS node is to locate obstacles through the laser scan and adjust the velocity in order to not collide.

C++ ROS Rviz Simulator

#### SIMPLE UNIX FILE SYSTEM IN USER SPACE

JULY 2021 - AUGUST 2021

github.com/alem1r/simple\_file\_system

Implementation of a UNIX file system in user land.

C Makefile

# **S**KILLS

Programming C,Python Disassember IDA

HDL Verilog
Development tools VS Code, Git, Clear Case

System GNU/Linux,Assembly x86,RISC-V

Other ChipWhisperer,LaTex,HTML,GNU Octave

### **CERTIFICATIONS**

- > B2 First Certificate in English (Cambridge)
- > EC-COUNCIL Reverse Engineering
- > Cisco Network IT Essentials