💌 alen@u.nus.edu 💠 🕿 +65 98998744 🖈 🛅 alenks 💠 🗘 alenks 💠 🕏 OobIMEoAAAAJ 💠 🌬 233/0571 💠 💆 alen\_k\_s 🖈 🔼 alenks.github.io

#### EDUCATION Nationa

#### National University of Singapore, Singapore

■ Doctor of Philosophy (*Ph.D.*) in Computer Science

- *Jan 2019 Aug 2024* (Expected)
- Thesis: Accelerating the Evaluation of Large Workloads on Post-Dennard Systems using Sampling
- Advisor: Dr. Trevor E. Carlson
- Areas: Processor architecture, workload characterization, dynamic program analysis, performance modeling and measurements, simulation infrastructure

#### Birla Institute of Technology & Science, Pilani, Rajasthan, India

■ Master of Engineering (*M.E.*) in Computer Science

- *Aug 2014 May 2016*
- Thesis: Performance Improvement of Multicore Scheduler in Real-Time Mixed Criticality Systems
- Advisor: Dr. Biju K. Raveendran
- Selected coursework: Advanced Computer Architecture, Advanced Algorithms and Complexity, Advanced Operating Systems, Cloud Computing, Data Mining, Real-time Systems.

#### University of Kerala, Thiruvananthapuram, Kerala, India

- Bachelor of Technology (B. Tech.) in Computer Science and Engineering
- Aug 2009 Dec 2013
- FYP: Online Object Recognition from Images using Kohonen Neural Networks

# WORK EXPERIENCE

# Intel Corporation, Massachusetts, USA

■ Research Intern

Jul 2022 – Dec 2022

- Host: Dr. Harish Patil
- Representative region validation using performance counters, sampling and performance analysis of heterogeneous CPU-GPU workloads

## National University of Singapore, Singapore

■ Research Intern

Nov 2018 - Jan 2019

• Host: Dr. Trevor E. Carlson

## NetApp, Bengaluru, India

■ Member Technical Staff II

- Jul 2016 Nov 2018
- Performance modeling of data storage devices, empirical analysis of storage protocols and workloads

## **PUBLICATIONS**

## WORKS IN PROGRESS

- [1] <u>Alen Sabu</u>, Harish Patil, Changxi Liu, Wim Heirman, and Trevor E. Carlson, "XPU-Point: Sampling Methodology to Accelerate Simulation of Heterogeneous CPU-GPU Workloads" (Submitted)
- [2] <u>Alen Sabu</u>, Zhantong Qiu, Harish Patil, Wim Heirman, Jason Lowe-Power, and Trevor E. Carlson, "Accelerated Simulation of Parallel Workloads using Loop-Bounded Checkpoints" (Ongoing)

#### **CONFERENCES & JOURNALS**

- [1] Changxi Liu\*, <u>Alen Sabu</u>\*, Akanksha Chaudhari, Qingxuan Kang, and Trevor E. Carlson, "Pac-Sim: Simulation of Multi-threaded Workloads using Intelligent, Live Sampling." in ACM Transactions on Architecture and Code Optimization (TACO), *Jun 2024*
- [2] <u>Alen Sabu</u>\*, Changxi Liu\*, and Trevor E. Carlson, "Viper: Utilizing Hierarchical Program Structure to Accelerate Multi-core Simulation." in *IEEE Access*, *Jan 2024*
- [3] <u>Alen Sabu</u>, Harish Patil, Wim Heirman, and Trevor E. Carlson, "LoopPoint: Checkpoint-driven Sampled Simulation for Multi-threaded Applications," in *The 28<sup>th</sup> IEEE International Symposium on High-Performance Computer Architecture (HPCA)*, *Apr 2022*
- [4] Harish Patil, Alexander Isaev, Wim Heirman, <u>Alen Sabu</u>, Ali Hajiabadi, and Trevor E. Carlson, "ELFies: Executable Region Checkpoints for Performance Analysis and Simulation," in *The 19<sup>th</sup> International Symposium on Code Generation and Optimization (CGO)*, *Mar 2021*
- [5] <u>Alen Sabu</u>, Biju Raveendran, and Rituparna Ghosh, "SMILEY: A Mixed-Criticality Real-Time Task Scheduler for Multicore Systems," in *The 22<sup>nd</sup> International Symposium on Distributed Simulation and Real Time Applications (DS-RT)*, Oct 2018 (Nominated best paper)

## \* Joint first authors

#### **WORKSHOPS & POSTERS**

- [1] <u>Alen Sabu</u>, Harish Patil, Wim Heirman, and Trevor E. Carlson, "ROIperf: A Framework to Rapidly Validate Workload Sampling Methodologies," in *The 1<sup>st</sup> Workshop on Computer Architecture Modeling and Simulation (CAMS)*, Oct 2023
- [2] <u>Alen Sabu</u>, Harish Patil, Wim Heirman, Alexander Isaev, and Trevor E. Carlson, "Approaching a High-Performance, General-Purpose Multi-Threaded Sampling Methodology," in *The 2<sup>nd</sup> Young Architect Workshop (YArch), Mar 2020*

#### **TUTORIALS & TALKS**

- [1] "The gem5 Tutorial: Public Release of SPEC CPU2017 ELFies for Simulation on gem5"
  - The 51<sup>st</sup> International Symposium on Computer Architecture (ISCA)

Jun 2024

- [2] "LoopPoint Tools: Sampled Simulation of Complex Multi-threaded Workloads using Sniper and gem5"
   The 29<sup>th</sup> International Symposium on High-Performance Computer Architecture (HPCA) Feb 2023
- [3] "Studies in Selection and Validation of Regions of Interest in Heterogeneous CPU-GPU Workloads"
  - VSSAD Seminar, Intel Corporation, MA, USA

Dec 2022

- [4] "LoopPoint and ELFies: Tools and Techniques to Accelerate Architecture Simulations of Complex Multi-threaded Applications using Checkpointing"
  - The 49<sup>th</sup> International Symposium on Computer Architecture (ISCA)

Jun 2022 May 2022

- International Symposium on Performance Analysis of Systems & Software (ISPASS)
- [5] "LoopPoint: Checkpoint-Driven Sampled Simulation for Multi-threaded Applications"VSSAD Seminar, Intel Corporation, MA, USA

Mar 2022

**SKILLS** 

 $C, C++, Python, Bash, \LaTeX, Git, Docker, GDB, Intel\ Pin,\ Intel\ GTPin,\ NVIDIA\ NVBit,\ Sniper\ x86\ simulator$ 

#### **AWARDS**

- Travel grant for the 56<sup>th</sup> International Symposium on Microarchitecture (MICRO'23), Canada
- Research Achievement Award 2021/2022 from the School of Computing, National University of Singapore
- Travel grant for the 49<sup>th</sup> International Symposium on Computer Architecture (ISCA'22), USA
- Travel grant for the 2<sup>nd</sup> Young Architect Workshop at ASPLOS'20, Switzerland
- NUS Graduate Research Scholarship, National University of Singapore

Jan 2019 – Till date

■ BITS Higher Degree Scholarship, Birla Institute of Technology & Science, Pilani Aug 2014 – May 2016

# PROFESSIONAL SERVICE

- Served in the Artifact Evaluation Committee of IEEE/ACM International Symposium on Microarchitecture (MICRO), 2023
- Served in the Program Committee of posters/short-papers at IEEE International Symposium on Workload Characterization (IISWC), 2023
- Served in the Artifact Evaluation Committee of IEEE/ACM International Symposium on Code Generation and Optimization (CGO), 2023
- Served as a reviewing member for Master of Computing admissions in the School of Computing, National University of Singapore, 2021

## TEACHING EXPERIENCE

Teaching Assistant for CS2030 Programming Methodology II at NUS

Spring 2021

Teaching Assistant for CS2106 Introduction to Operating Systems at NUS

Fall 2020

Teaching Assistant for CS1010E Programming Methodology at NUS

Spring 2020

■ Teaching Assistant for CSF111 Computer Programming at BITS-Pilani

Spring 2016, Spring 2015

■ Teaching Assistant for CSF342 Computer Architecture at BITS Pilani

Fall 2015

# REFERENCES

# ■ Dr. Trevor E. Carlson

Assistant Professor National University of Singapore

Singapore

# ■ Dr. Harish Patil

Principal Engineer
Intel Corporation
USA

harish.patil@intel.com

#### ■ Dr. Wim Heirman

Principal Engineer
Intel Corporation
Belgium

