```
1 #ifndef DS18B20_H_INCLUDED
 2 #define DS18B20_H_INCLUDED
 3
 4
 5 #define SKIP_ROM
                            0
   #define NO_SKIP_ROM
 6
                            1
 7
 8
9 #define RESOLUTION_9BIT
                                0x1F
10 #define RESOLUTION_10BIT 0x3F
11 #define RESOLUTION_11BIT 0x5F
12 #define RESOLUTION_12BIT 0x7F
13
14
15
16
17
           DS18B20_PortInit(void);
18 void
19  uint8_t DS18B20_Init(uint8_t mode);
         DS18B20_MeasureTemperCmd(uint8_t dev, uint8_t mode);
20 void
21 void
           DS18B20_ReadStratchpad(uint8_t dev, uint8_t mode);
22  uint8_t DS18B20_GetSign(uint8_t dev);
23
   float DS18B20_Convert(uint8_t dev);
24
25
26
   __STATIC_INLINE void Delay_us(__IO uint32_t us){
27
        us *= (SystemCoreClock / 12000000);
28
29
        while(us--) ;
30
31
32
33
34 #endif /* DS18B20_H_INCLUDED */
```