### SAPICE = SAGE + NGSPICE

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https://github.com/alessandro-bernardini/SAPICE http://alessandro-bernardini.github.io/SAPICE/

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THIS PROGRAM WAS NOT WELL TESTED!

Project home: https://github.com/alessandro-bernardini/SAPICE

Requires: ngspice revision 24; sage version 5.6. Other version of ngspice or sage should work well too.

http://www.sagemath.org/

http://ngspice.sourceforge.net/download.html

See documentation and license

2 Main Page

# Namespace Index

2.1 Packages	ò
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Here are the packages with brief descriptions (if available):	
sage circuit analysis	ç

Namespace Index

# **Class Index**

### 3.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:	
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# File Index

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### **Namespace Documentation**

#### 5.1 sage\_circuit\_analysis Namespace Reference

#### Classes

- · class SmallSignalLinearCircuit
- · class WrongUse
- class WrongData

#### **Functions**

- · def extract\_value
- · def simplify\_sum
- def simplify\_polynomial
- def simplify\_rational\_func
- · def rational\_func
- · def get\_inductor\_data

#### **Variables**

- tuple RESISTOR\_EXPR = re.compile( $'^{\land}(\s)*(R|r)\w+'$ )
- tuple CAPACITOR\_EXPR = re.compile('^(\s)\*(C|c)\w+')
- tuple INDUCTOR\_EXPR = re.compile('^(\s)\*(L|I)\w+')
- tuple  $V_{EXPR} = re.compile('^(\s)*(V|v)\w+')$
- tuple I\_EXPR = re.compile('^(\s)\*(I|i)\w+')
- tuple VCCS\_EXPR = re.compile( $'^{\land}(\s)*(G|g)\w+'$ )
- tuple BJT EXPR = re.compile( $^{\land}(\s)*(Q|q)\w+^{\backprime}$ )
- tuple KIND\_EXPR = re.compile('^(\s)\*(K|k)\w+')
- tuple DATA\_FIELD = re.compile('\s+\\w+\=[-\+]?\\w\*\.?\\w+[-\+]?\\d\*|\\s+\{\\w+\}')
- tuple TEMP\_EXPR = re.compile('^(\s)\*(\.temp)\s+\w+', re.IGNORECASE)

#### 5.1.1 Function Documentation

5.1.1.1 def sage\_circuit\_analysis.extract\_value ( data )

extracts numerical value from a data field. For example 0.003 from 3m. data must be a stripped string

- 5.1.1.2 def sage\_circuit\_analysis.get\_inductor\_data ( line, ignore\_all\_ic, set\_default\_ic\_to\_zero )
- 5.1.1.3 def sage\_circuit\_analysis.rational\_func ( large\_expression )

Returns a tuple (result, polinom-result) where result is an approximation of the polinom polinom given as a sage expression. The polynom is considered in the variable variable Default: variable=sage.var('s')

dictonary is a dictionary that provides numerical values for the symbolic variables in the coefficeints of the polinom.

treshold is a number in [0,1[ that fixes a treshhold: for treshhold=0 result will be equal to polinom.

Every coefficient of the polynomial (coefficient made by a sum of terms) will be simplified invoking simplify\_sum.

# 5.1.1.5 def sage\_circuit\_analysis.simplify\_rational\_func ( rational\_func, dictionary, treshhold = 0, variable = sage.var('s'), safe\_check = True )

simplifies a rational function rational\_func (a sage expression) in the variable variable (default sage.var('s')).

dictonary is a dictionary that provides numerical values for the symbolic variables in the coefficeints of the polinom.

treshold is a number in [0,1[ that fixes a treshhold: for treshhold=0 result will be equal to polinom.

A simplified numerator and denominator will be computed invoking simplify\_polinomial

The result will be a dictionaty (result, rational\_func - result) with result a simplified version of rational\_func

#### 5.1.1.6 def sage\_circuit\_analysis.simplify\_sum ( expr, dictionary, treshhold = 0 )

Returns a tuple (simplified\_expr, expr-simplified\_expr) where simplified\_expr is an approximation of expr.

expr is a symbolic sage expression made of terms added together (a sum of terms)

dictonary is a dictionary that provides numerical values for the symbolic variables in the expression  $\exp$ 

treshold is a number in [0,1[ that fixes a treshhold.

If the numerical value of a given term is lower than the numerical value of the term that evaluates to the maximum (along all terms) multiplied with the trashhold then the corresponding term is ignored.

#### 5.1.2 Variable Documentation

- 5.1.2.1 tuple sage\_circuit\_analysis.BJT\_EXPR = re.compile( $(\c^{(s)})$ ) w+')
- 5.1.2.2 tuple sage\_circuit\_analysis.CAPACITOR\_EXPR = re.compile( $(\scalebox{0.5}) \times (\scalebox{0.5}) \times (\sc$

- 5.1.2.3 tuple sage\_circuit\_analysis.DATA\_FIELD = re.compile('\s+\w+\=[-\+]?\w\*\.?\w+[-\+]?\d\*|\s+[-\+]?\\w\*\.?\\w+\}')
- 5.1.2.4 tuple sage\_circuit\_analysis.I\_EXPR = re.compile( $'^{\land}(\s)*(I|i)\w+'$ )
- 5.1.2.5 tuple sage\_circuit\_analysis.INDUCTOR\_EXPR = re.compile( $'^{\land}(\s)*(L|I)\w+'$ )
- 5.1.2.6 tuple sage\_circuit\_analysis.KIND\_EXPR = re.compile( $(\)^(\)*(K|k)\)$ )
- 5.1.2.7 tuple sage\_circuit\_analysis.RESISTOR\_EXPR = re.compile( $'^(\s)*(R|r)\w+'$ )
- 5.1.2.8 tuple sage\_circuit\_analysis.TEMP\_EXPR = re.compile('^(\s)\*(\.temp)\s+\w+', re.IGNORECASE)
- 5.1.2.9 tuple sage\_circuit\_analysis.V\_EXPR = re.compile( $(\)^{(\)} (\) *(\) \) w+')$
- 5.1.2.10 tuple sage\_circuit\_analysis.VCCS\_EXPR = re.compile( $^{\land}(\s)*(G|g)\w+'$ )

Namespace	Documen	ıtation
Hamespace	Documen	latioi

### **Class Documentation**

### 6.1 sage\_circuit\_analysis.SmallSignalLinearCircuit Class Reference

#### **Public Member Functions**

- def \_\_init\_\_
- · def clone
- · def solve\_nodal\_equations\_symb
- def solve\_nodal\_equations\_num
- · def impedance
- · def transimpedance
- def z\_parameters
- def dict\_default\_vals
- def print\_information
- def print\_lin\_circuit
- def get\_lin\_circuit\_as\_string
- def write\_lin\_circuit\_to\_file
- def export\_lin\_circuit\_graph
- · def export\_lin\_circuit\_string
- def print\_symbols
- def write\_circuit\_graph\_dot

#### **Public Attributes**

- circuit file
- original\_circuit\_file
- spice\_batch\_output\_file

#### **Static Public Attributes**

- int pos = 0
- tuple data\_temp = DATA\_FIELD.findall(line)
- list temperature = data\_temp[0]
- tuple bjt\_lineptr = BJT\_EXPR.match(line)
- list bjt\_line\_data = line[bjt\_lineptr.end():]
- list bjt\_id = line[:bjt\_lineptr.end()]
- string operating\_region = 'unknown'
- tuple data\_bjt = DATA\_FIELD.findall(bjt\_line\_data)
- list data\_bjt\_strip = []

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```
list model = data_bjt_strip[3]
• bjt_model_desc_reached = False
• model_present_bjt_reached = False
• skipnextline = False
• flags = re.IGNORECASE)!None:
• matchline = \
• bit reached = False
present_bjt_reached = False
• add_eq_sol = \

    tuple kind lineptr = KIND EXPR.match(line)

• list kind line data = line[kind lineptr.end():]

    list kind id = line[:kind lineptr.end()]

• tuple data_kind = DATA_FIELD.findall(kind_line_data)
• list data kind strip = []
• tuple ind_lineptr = INDUCTOR_EXPR.match(line2)
• list ind id = line2[:ind lineptr.end()]
• ignore all ic = ignore all ic,
• set default ic to zero = set default ic to zero)

    string type = 'L'

· dictionary coupled_to
list value = self.coupled_inductors_data[ind_id]
• list cpld_inductor_data = self.coupled_inductors_data[cpld_inductor]
dictionary cpld inductor node = {}
• int K coeff = 0

    list inductor node0 = self.coupled inductors data[inductor]

• list inductor_node1 = self.coupled_inductors_data[inductor]
• tuple K_coeff = sage.var(self.coupled_inductors_matrix[cpld_inductor][inductor])
dictionary temp_dict = {}
dictionary cpld_voltage_subst = {}
list simpl_temp = []
• tuple i_lineptr = I_EXPR.match(line)

    list i line data = line[i lineptr.end():]

list i_id = line[:i_lineptr.end()]
• tuple data i = DATA FIELD.findall(i line data)
• list data i strip = []
• id = i id.
list ngspice_line_data = data_i_strip[0:2]

    tuple res_lineptr = RESISTOR_EXPR.match(line)

list res_line_data = line[res_lineptr.end():]
• list resistor id = line[:res lineptr.end()]

    tuple data res = DATA FIELD.findall(res line data)

list data_res_strip = []
• list id = resistor id, valuedata res strip[2:]

    tuple cap_lineptr = CAPACITOR_EXPR.match(line)

list cap_line_data = line[cap_lineptr.end():]

    list capacitor id = line[:cap lineptr.end()]

    tuple data_cap = DATA_FIELD.findall(cap_line_data)

list data_cap_strip = []
• list ind_line_data = line[ind_lineptr.end():]
list inductor_id = line[:ind_lineptr.end()]
• tuple data ind = DATA FIELD.findall(ind line data)

 list data ind strip = []

• tuple vccs lineptr = VCCS EXPR.match(line)

    list vccs line data = line[vccs lineptr.end():]
```

• list vccs\_id = line[:vccs\_lineptr.end()]

- tuple data\_vccs = DATA\_FIELD.findall(vccs\_line\_data)
- list data\_vccs\_strip = []
- data vccs strip = data vccs strip\
- dictionary subs zero ic = {}

#### 6.1.1 Detailed Description

```
PROJECT HOME:
    https://github.com/alessandro-bernardini/SAPICE
    http://alessandro-bernardini.github.io/SAPICE/
This class implements nodal analysis for linear electrical circuits.
If a nonlinear circuit is given then the linearized small signal circuit is
considered.
This class is designed to be used WITHIN the sage computer algebra system
    http://www.sagemath.org/
and in conjunction with ngspice (open source version of spice):
    http://ngspice.sourceforge.net/
So you have to install sage and ngspice first (see relative documentation).
Then run
    sage
from the shell
then in the sage prompt import the present module
    import sage_circuit_analysis
and the create a SmallSignalLinearCircuit object
    circuit = sage_circuit_analysis.SmallSignalLinearCircuit('circuitfile.cir')
with circuitfile.cir a spice netllist (see additional information below
and in the help of the constructor).
You can read-in a ngspice netlist and typically you will provide the operating
point data and other information via an ngspice batch output file
that you have to generate with the command (from the shell):
    ngspice -b circuitfile.cir -o batchoutput.log
where circuitfile.cir is the spice netlist
and batchoutput.log is the log file containing the output data of interest.
Both files circuitfile.cir and batchoutput.log must be passed
to a SmallSignalLinearCircuit object when invoking the constructor.
The nodal equations can be solved both numerically (if numerical values
are provided) and symbolically.
You can compute impedances and two port network parameters and do
the computation of poles and zeroes if a symbolic closed form solutions
exists.
It is possible to compute symbolic approximations to the exact solution
where only dominant terms are considered.
NOTE: numerical solutions usually means a polynom or a rational function
dependent on the complex variable s and on independent sources.
Usually all quantities are complex.
```

#### 6.1.2 Constructor & Destructor Documentation

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6.1.2.1 def sage\_circuit\_analysis.SmallSignalLinearCircuit.\_\_init\_\_ ( self, filename = None, spice\_batch\_output\_file = None, circuit\_netlist = None, check\_operating\_region = True, set\_default\_ic\_to\_zero = True, ignore\_all\_ic = False)

```
filename='circuitfile.cir'
where circuitfile.cir is a ngspice (spice) netlist.
spice_batch_output_file='batchoutput.log'
a file needed when operating point data must be considered
You have to run from the shell the command
ngspice -b circuitfile.cir -o batchoutput.log
fog generating the batchoutput.log file
in alternative
circuit_netlist=CIRCUIT_STRING
can be used where CIRCUIT_STRING is a string containing
the circuit netlist (in place of circuitfile.cir)
with check_operating_region=True a linearized model in dependence of the
operating region will be choosen for each semiconductor devices.
Otherwise it will be assumed that a default model is valid. (for
\ensuremath{\mathsf{BJT}} transistors the active region small signal model will be this
default model)
ignore_all_IC=True will ignore all initial conditions for both symbolic
and numeric computations
set_default_ic_to_zero=True will set initial conditions to zero for
capacitors or inductors where no initial condition is specified
```

#### 6.1.3 Member Function Documentation

#### 6.1.3.1 def sage\_circuit\_analysis.SmallSignalLinearCircuit.clone ( self )

returns a deepcopy of the object

#### 6.1.3.2 def sage\_circuit\_analysis.SmallSignalLinearCircuit.dict\_default\_vals ( self )

```
returns a pair of dictionaries (dict_num, dict_sym) where dict_num is a dictionary containing as keys the circuit parameters and as values the corrisponding numerical values

dict_sym is a dictionary containing as keys the circuit parameters for which a numerical value was not explicitly determined and as values the symbolic expressions associated with the considered circuit parameter.

If all numerical values can be explicitly computed then dict_sym will be empty.
```

### 6.1.3.3 def sage\_circuit\_analysis.SmallSignalLinearCircuit.export\_lin\_circuit\_graph ( self, shorts = None, remove = None, with\_substitutions = True )

```
returns a graph (a networkx object) of the linear circuit shorts is a dictionary {'node1':'node2', 'node3':'node4'} where node1 will be shorted with node2 and node3 will be shorted with node 4 etc.

A key node should not also be a value node for preventing conflicts, so when shorting multiple nodes together the proper ordering is important. remove is a list of components to remove (replace with open circuit) For example remove=['R1','C1'] will remove the resistor R1 and the capacitor C1 from the netlist.
```

```
with_substitutions=True will explicitly consider numerical values resulting from the operating point of nonlinear devices with_substitutions=False will leave the values of parameters resulting from the operating point of nonlinear devices as a symbolic expression
```

## 6.1.3.4 def sage\_circuit\_analysis.SmallSignalLinearCircuit.export\_lin\_circuit\_string ( self, shorts = None, remove = None, with\_substitutions = True, write\_to\_file = None )

```
returns a string describing the netlist of the linear(ized) circuit

shorts is a dictionary {'node1':'node2', 'node3':'node4'}

where node1 will be shorted with node2

and node3 will be shorted with node 4

etc.

A key node should not also be a value node for preventing conflicts, so
when shorting multiple nodes together the proper ordering is important.

remove is a list of components to remove (replace with open circuit)

For example remove=['R1','C1']

will remove the resistor R1 and the capacitor C1

from the netlist.

with_substitutions=True

will explicitly consider numerical values resulting from the operating point

of nonlinear devices

with_substitutions=False will leave the values of parameters resulting from the
operating point of nonlinear devices as a symbolic expression
```

#### 6.1.3.5 def sage\_circuit\_analysis.SmallSignalLinearCircuit.get\_lin\_circuit\_as\_string ( self )

```
returns a string containing the linear circuit (or linearized circuit) netlist
```

# 6.1.3.6 def sage\_circuit\_analysis.SmallSignalLinearCircuit.impedance ( self, port, with\_substitutions = True, symbolic = True)

```
computes the impedance between two nodes in the circuit
port=('node1','node2')
is the pair of nodes considered for the impedance computations, for example
port=('0','1')
will compute the impedance between node '1' (if a node '1' is given
in the circuit) and ground (node '0').
with_substitutions=True
(default) will consider the actual values of all small signal circuit parameters
that results from the consideration of the operating point of nonlinear devices.
Otherwise those parameters will be left symbolic.
(affects the computation of numerical impedance values)
symbolic=True
impedance is computed symbolically. (symbolic=False will compute a numeric
impedance value)
All internal sources and all the initial conditions will be ignored (set to zero appropriately).
See also the help for the transimpedance method.
```

#### 6.1.3.7 def sage\_circuit\_analysis.SmallSignalLinearCircuit.print\_information ( self )

prints some information about the circuit

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#### 6.1.3.8 def sage\_circuit\_analysis.SmallSignalLinearCircuit.print\_lin\_circuit ( self )

prints the linear (or linearized) circuit netlist

#### 6.1.3.9 def sage\_circuit\_analysis.SmallSignalLinearCircuit.print\_symbols ( self )

displays information on the symbols adopted by the program (incomplete)

### 6.1.3.10 def sage\_circuit\_analysis.SmallSignalLinearCircuit.solve\_nodal\_equations\_num ( self, set\_ind\_ss\_src\_to\_zero = False )

Returns a numeric solutions of the nodal equations.

The solution will be a list containing a list of equations that have on the left hand side the nodal voltages and on the right hand side the symbolic expression representing the solution.

set\_ind\_ss\_src\_to\_zero=True will set all the independent small signal
sources (in the small signal linearized circuit) to zero when computing
the solution

### 6.1.3.11 def sage\_circuit\_analysis.SmallSignalLinearCircuit.solve\_nodal\_equations\_symb ( self, set\_ind\_ss\_src\_to\_zero = False )

Returns a symbolic solutions of the nodal equations.

internal sources or initial conditions are ignored.

The solution will be a list containing a list of equations that have on the left hand side the nodal voltages and on the right hand side the symbolic expression representing the solution.

set\_ind\_ss\_src\_to\_zero=True will set all the independent small signal
sources (in the small signal linearized circuit) to zero when computing
the solution

# 6.1.3.12 def sage\_circuit\_analysis.SmallSignalLinearCircuit.transimpedance ( self, port\_Lin, port\_V\_out, with\_substitutions = True, symbolic = True)

computes the trans-impedance between two nodes in the circuit

port\_I\_in=('node1','node2')
is the pair of nodes considered for the current input
where the (positive) impressed input current flows into node2 (and out of node1)

port\_V\_out=('node3','node4')
is the pair of nodes considered for the output voltage:
output\_voltage = Vnode4 - Vnode3

the transimpedance will then be the output voltage/input current

symbolic=True
computes a symbolic expression as solution

with\_substitutions=True
consider the operating point data (affects numerical results with symbolic=False).

```
6.1.3.13 def sage_circuit_analysis.SmallSignalLinearCircuit.write_circuit_graph_dot ( self, output_graph_file =
         'circuit_graph.dot', with_substitutions = True, edge_labels = True )
generates a dot file in which a representation of the circuit graph is given.
output_graoh_file='circuit_graph.dot'
file in which the graph representation will be written.
with_substitutions=True
will consider numerical values resulting from the operating point of nonlinear devices
edge labels=True
will labels edge with informations.
6.1.3.14 def sage_circuit_analysis.SmallSignalLinearCircuit.write_lin_circuit_to_file ( self, filename )
writes the linear circuit netlist to file 'filename'
6.1.3.15 def sage_circuit_analysis.SmallSignalLinearCircuit.z_parameters ( self, port_in, port_out, with_substitutions =
        True, symbolic = True )
returns a sage matrix containing the two port network z-parameters
port_in=('node1', 'node2')
port_out=('node3', 'node4')
where nodei is a node identifier
for any port
port_x=('nodea', 'nodeb')
the positive port voltages are nodeb - nodea
and the positive currents are flowing into nodeb (and out of nodea)
6.1.4 Member Data Documentation
6.1.4.1 sage_circuit_analysis.SmallSignalLinearCircuit.add_eq_sol = \ [static]
6.1.4.2 list sage_circuit_analysis.SmallSignalLinearCircuit.bjt_id = line[:bjt_lineptr.end()] [static]
6.1.4.3 list sage_circuit_analysis.SmallSignalLinearCircuit.bjt_line_data = line[bjt_lineptr.end():] [static]
6.1.4.4 tuple sage_circuit_analysis.SmallSignalLinearCircuit.bjt_lineptr = BJT_EXPR.match(line) [static]
6.1.4.5 sage_circuit_analysis.SmallSignalLinearCircuit.bjt_model_desc_reached = False [static]
6.1.4.6
       sage_circuit_analysis.SmallSignalLinearCircuit.bjt_reached = False [static]
6.1.4.7 list sage_circuit_analysis.SmallSignalLinearCircuit.cap_line_data = line[cap_lineptr.end():] [static]
6.1.4.8 tuple sage_circuit_analysis.SmallSignalLinearCircuit.cap_lineptr = CAPACITOR_EXPR.match(line) [static]
6.1.4.9 list sage_circuit_analysis.SmallSignalLinearCircuit.capacitor_id = line[:cap_lineptr.end()] [static]
6.1.4.10 sage_circuit_analysis.SmallSignalLinearCircuit.circuit_file
6.1.4.11 dictionary sage_circuit_analysis.SmallSignalLinearCircuit.coupled_to [static]
```

Initial value:

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```
6.1.4.12 list sage_circuit_analysis.SmallSignalLinearCircuit.cpld_inductor_data = self.coupled_inductors_data[cpld_inductor]
         [static]
6.1.4.13 dictionary sage_circuit_analysis.SmallSignalLinearCircuit.cpld_inductor_node = {} [static]
6.1.4.14 dictionary sage_circuit_analysis.SmallSignalLinearCircuit.cpld_voltage_subst = {} [static]
         tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_bjt = DATA_FIELD.findall(bjt_line_data) [static]
6.1.4.16 list sage_circuit_analysis.SmallSignalLinearCircuit.data_bjt_strip = [] [static]
6.1.4.17 tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_cap = DATA_FIELD.findall(cap_line_data) [static]
6.1.4.18 list sage_circuit_analysis.SmallSignalLinearCircuit.data_cap_strip = [] [static]
         tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_i = DATA_FIELD.findall(i line data) [static]
6.1.4.19
6.1.4.20
         list sage_circuit_analysis.SmallSignalLinearCircuit.data_i_strip = [] [static]
6.1.4.21 tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_ind = DATA_FIELD.findall(ind_line_data) [static]
6.1.4.22 list sage_circuit_analysis.SmallSignalLinearCircuit.data_ind_strip = [] [static]
         tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_kind = DATA_FIELD.findall(kind_line_data) [static]
         list sage_circuit_analysis.SmallSignalLinearCircuit.data_kind_strip = [] [static]
6.1.4.25 tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_res = DATA_FIELD.findall(res_line_data) [static]
6.1.4.26 list sage_circuit_analysis.SmallSignalLinearCircuit.data_res_strip = [] [static]
6.1.4.27 tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_temp = DATA_FIELD.findall(line) [static]
6.1.4.28
         tuple sage_circuit_analysis.SmallSignalLinearCircuit.data_vccs = DATA_FIELD.findall(vccs_line_data)
         [static]
         list sage_circuit_analysis.SmallSignalLinearCircuit.data_vccs_strip = [] [static]
6.1.4.30
         sage_circuit_analysis.SmallSignalLinearCircuit.data_vccs_strip = data_vccs_strip \ [static]
         sage_circuit_analysis.SmallSignalLinearCircuit.flags = re.IGNORECASE)!None: [static]
         list sage_circuit_analysis.SmallSignalLinearCircuit.i_id = line[:i_lineptr.end()] [static]
6.1.4.33
         list sage_circuit_analysis.SmallSignalLinearCircuit.i_line_data = line[i_lineptr.end():] [static]
         tuple sage_circuit_analysis.SmallSignalLinearCircuit.i_lineptr = I_EXPR.match(line) [static]
6.1.4.34
6.1.4.35 list sage_circuit_analysis.SmallSignalLinearCircuit.id = i_id, [static]
6.1.4.36 list sage_circuit_analysis.SmallSignalLinearCircuit.id = resistor_id,valuedata_res_strip[2:] [static]
```

```
6.1.4.37
         sage_circuit_analysis.SmallSignalLinearCircuit.ignore_all_ic = ignore_all_ic, [static]
6.1.4.38
         list sage_circuit_analysis.SmallSignalLinearCircuit.ind_id = line2[:ind_lineptr.end()] [static]
6.1.4.39 list sage_circuit_analysis.SmallSignalLinearCircuit.ind_line_data = line[ind_lineptr.end():] [static]
6.1.4.40 tuple sage_circuit_analysis.SmallSignalLinearCircuit.ind_lineptr = INDUCTOR_EXPR.match(line2) [static]
6.1.4.41 list sage_circuit_analysis.SmallSignalLinearCircuit.inductor_id = line[:ind_lineptr.end()] [static]
6.1.4.42 list sage_circuit_analysis.SmallSignalLinearCircuit.inductor_node0 = self.coupled_inductors_data[inductor]
          [static]
6.1.4.43 list sage_circuit_analysis.SmallSignalLinearCircuit.inductor_node1 = self.coupled_inductors_data[inductor]
          [static]
6.1.4.44 int sage_circuit_analysis.SmallSignalLinearCircuit.K_coeff = 0 [static]
6.1.4.45 tuple sage_circuit_analysis.SmallSignalLinearCircuit.K_coeff = sage.var(self.coupled_inductors_matrix[cpld_-
         inductor][inductor]) [static]
6.1.4.46 list sage_circuit_analysis.SmallSignalLinearCircuit.kind_id = line[:kind_lineptr.end()] [static]
6.1.4.47 list sage_circuit_analysis.SmallSignalLinearCircuit.kind_line_data = line[kind_lineptr.end():] [static]
6.1.4.48 tuple sage_circuit_analysis.SmallSignalLinearCircuit.kind_lineptr = KIND_EXPR.match(line) [static]
6.1.4.49
         sage_circuit_analysis.SmallSignalLinearCircuit.matchline = \ [static]
6.1.4.50 list sage_circuit_analysis.SmallSignalLinearCircuit.model = data_bjt_strip[3] [static]
         sage_circuit_analysis.SmallSignalLinearCircuit.model_present_bjt_reached = False [static]
6.1.4.51
6.1.4.52 list sage_circuit_analysis.SmallSignalLinearCircuit.ngspice_line_data = data_i_strip[0:2] [static]
         string sage_circuit_analysis.SmallSignalLinearCircuit.operating_region = 'unknown' [static]
6.1.4.54
         sage_circuit_analysis.SmallSignalLinearCircuit.original_circuit_file
6.1.4.55 int sage_circuit_analysis.SmallSignalLinearCircuit.pos = 0 [static]
         sage_circuit_analysis.SmallSignalLinearCircuit.present_bjt_reached = False [static]
6.1.4.56
6.1.4.57 list sage_circuit_analysis.SmallSignalLinearCircuit.res_line_data = line[res_lineptr.end():] [static]
6.1.4.58
         tuple sage_circuit_analysis.SmallSignalLinearCircuit.res_lineptr = RESISTOR_EXPR.match(line) [static]
         list sage_circuit_analysis.SmallSignalLinearCircuit.resistor_id = line[:res_lineptr.end()] [static]
6.1.4.59
         sage_circuit_analysis.SmallSignalLinearCircuit.set_default_ic_to_zero = set_default_ic_to_zero) [static]
6.1.4.61
         list sage_circuit_analysis.SmallSignalLinearCircuit.simpl_temp = [] [static]
6.1.4.62 sage_circuit_analysis.SmallSignalLinearCircuit.skipnextline = False [static]
6.1.4.63 sage_circuit_analysis.SmallSignalLinearCircuit.spice_batch_output_file
```

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```
6.1.4.64 dictionary sage_circuit_analysis.SmallSignalLinearCircuit.subs_zero_ic = {} [static]
6.1.4.65 dictionary sage_circuit_analysis.SmallSignalLinearCircuit.temp_dict = {} [static]
6.1.4.66 list sage_circuit_analysis.SmallSignalLinearCircuit.temperature = data_temp[0] [static]
6.1.4.67 string sage_circuit_analysis.SmallSignalLinearCircuit.type = 'L' [static]
6.1.4.68 list sage_circuit_analysis.SmallSignalLinearCircuit.value = self.coupled_inductors_data[ind_id] [static]
6.1.4.69 list sage_circuit_analysis.SmallSignalLinearCircuit.vccs_id = line[:vccs_lineptr.end()] [static]
6.1.4.70 list sage_circuit_analysis.SmallSignalLinearCircuit.vccs_line_data = line[vccs_lineptr.end():] [static]
6.1.4.71 tuple sage_circuit_analysis.SmallSignalLinearCircuit.vccs_lineptr = VCCS_EXPR.match(line) [static]
The documentation for this class was generated from the following file:
```

· sage\_circuit\_analysis.py

### 6.2 sage\_circuit\_analysis.WrongData Class Reference

**Public Member Functions** 

def init

#### **Public Attributes**

• data

#### 6.2.1 Constructor & Destructor Documentation

6.2.1.1 def sage\_circuit\_analysis.WrongData.\_\_init\_\_ ( self, data )

#### 6.2.2 Member Data Documentation

6.2.2.1 sage\_circuit\_analysis.WrongData.data

The documentation for this class was generated from the following file:

sage\_circuit\_analysis.py

#### 6.3 sage\_circuit\_analysis.WrongUse Class Reference

**Public Member Functions** 

def init

#### **Public Attributes**

• data

- 6.3.1 Constructor & Destructor Documentation
- 6.3.1.1 def sage\_circuit\_analysis.WrongUse.\_\_init\_\_ ( self, data )
- 6.3.2 Member Data Documentation
- 6.3.2.1 sage\_circuit\_analysis.WrongUse.data

The documentation for this class was generated from the following file:

• sage\_circuit\_analysis.py

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### **File Documentation**

#### 7.1 sage\_circuit\_analysis.py File Reference

#### Classes

- · class sage\_circuit\_analysis.SmallSignalLinearCircuit
- class sage\_circuit\_analysis.WrongUse
- · class sage\_circuit\_analysis.WrongData

#### **Packages**

• namespace sage\_circuit\_analysis

#### **Functions**

- def sage\_circuit\_analysis.extract\_value
- · def sage\_circuit\_analysis.simplify\_sum
- · def sage circuit analysis.simplify polynomial
- · def sage circuit analysis.simplify rational func
- def sage\_circuit\_analysis.rational\_func
- · def sage\_circuit\_analysis.get\_inductor\_data

#### **Variables**

- tuple sage\_circuit\_analysis.RESISTOR\_EXPR = re.compile('^(\s)\*(R|r)\w+')
- tuple sage\_circuit\_analysis.CAPACITOR\_EXPR = re.compile('^(\s)\*(C|c)\w+')
- tuple sage\_circuit\_analysis.INDUCTOR\_EXPR = re.compile('^(\s)\*(L|I)\w+')
- tuple sage\_circuit\_analysis.V\_EXPR = re.compile('^(\s)\*(V|v)\w+')
- tuple sage\_circuit\_analysis.I\_EXPR = re.compile('^(\s)\*(I|i)\w+')
- tuple sage\_circuit\_analysis.VCCS\_EXPR = re.compile('^(\s)\*(G|g)\w+')
- tuple sage\_circuit\_analysis.BJT\_EXPR = re.compile('^(\s)\*(Q|q)\w+')
- tuple sage\_circuit\_analysis.KIND\_EXPR = re.compile('^(\s)\*(K|k)\w+')
- tuple sage\_circuit\_analysis.DATA\_FIELD = re.compile('\s+\w+\=[-\+]?\w\*\.?\w+[-\+]?\d\*|\s+\{\w+\}') \+]?\d\*|\s+\{\w+\}')
- tuple sage\_circuit\_analysis.TEMP\_EXPR = re.compile('^(\s)\*(\.temp)\s+\w+', re.IGNORECASE)

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