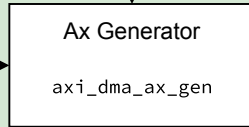


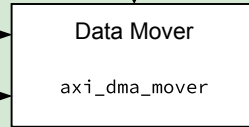
creates 1D contiguous descriptors:  
{src and dst address, number of bytes,  
src and dst mem attrs (AxCACHE)}



creates AXI-compliant ARs und AWs



issues given ARs and AWs  
and realigns data beats



reads

writes

AXI Port

parametrizable number  
of input dimensions

**reusable, uniform components**

**default provided  
but projects  
can customize**