	0	4	8	12	16	20
/tb_risc_v/rst_i						
/tb_risc_v/clk_i						
/tb_risc_v/pc_i	00400000		00400004 00400008 0040000C 00400010 00400014 00400018 0040001C 00...
/tb_risc_v/instruction_i	00700813		0FC10217 FFC20213 0FC10297 01028293 400006B7 FFF68693 02080863 00...
/tb_risc_v/addr_f	4026466304		4026466... 4 0 12 28 80...
/tb_risc_v/addr_i	0		7 2685009... 2685009... 2685010... 2685010... 10...
/tb_risc_v/rw_i						
/tb_risc_v/en_i						
/tb_risc_v/din_mem_i	0		7 0
/tb_risc_v/dout_mem_i	X		
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}		

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Signal											
/tb_risc_v/rst_i											
/tb_risc_v/clk_i											
/tb_risc_v/pc_i	00...	00400024	00400028		0040002C	00400030	00400034	00400038	0040003C	00400040	00400044
/tb_risc_v/instruction_i	00...	41F45493	00944533		0014F493	00950533	00420213	FFF80813	00D525B3	FC058EE3	000506B3
/tb_risc_v/addr_f	80...	8052408...	4026466...	0	4026466304		4026466...	4026466...	4026466...	4	4026466...	4026466...
/tb_risc_v/addr_i	10...	1073741...	0	2685009...	0		10	0	10	2685009...	6	1
/tb_risc_v/rw_i											
/tb_risc_v/en_i											
/tb_risc_v/din_mem_i	0									2685009...	0	1073741...
/tb_risc_v/dout_mem_i	X			10							
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}										

24   28   32   36   40   44

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Signal											
/tb_risc_v/rst_i											
/tb_risc_v/clk_i											
/tb_risc_v/pc_i	00400048	0040004C	0040001C	00400020	00400024	00400028		0040002C	00400030	00400034	00400038	00...
/tb_risc_v/instruction_i	FD5FF0EF	00D2A023	02080863	00022403	41F45493	00944533		0014F493	00950533	00420213	FFF80813	00...
/tb_risc_v/addr_f	4026466304		4026466...	4030660...	4026466304		4	4026466...	4026466...	4026466...	4026466...	40...
/tb_risc_v/addr_i	0		10	4194380	0		2685009...	0	4294967...	46	1	47
/tb_risc_v/rw_i											
/tb_risc_v/en_i											
/tb_risc_v/din_mem_i	0									4194380	-1
/tb_risc_v/dout_mem_i	10					-47					
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}										

48   52   56   60   64   68

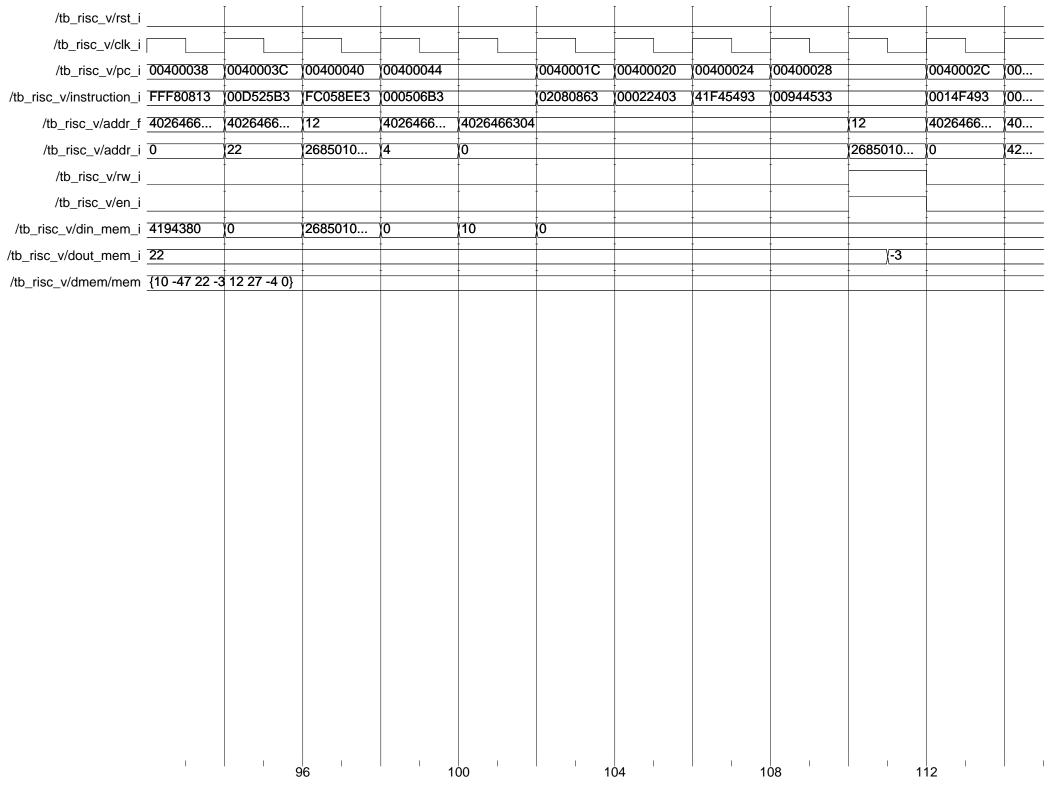Entity:tb_risc_v Architecture:struct Date: Fri Feb 19 03:35:27 PM CET 2021   Row: 3 Page: 3

Signal										
/tb_risc_v/rst_i										
/tb_risc_v/clk_i										
/tb_risc_v/pc_i	00...	00400040	00400044	0040001C	00400020	00400024	00400028	0040002C	00400030	00400034
/tb_risc_v/instruction_i	00...	FC058EE3	000506B3	02080863	00022403	41F45493	00944533	0014F493	00950533	00420213
/tb_risc_v/addr_f	40...	8	4026466...	4026466304				8	4026466304		4026466...
/tb_risc_v/addr_i	47	2685010...	5	0				2685010...	0		22
/tb_risc_v/rw_i										
/tb_risc_v/en_i										
/tb_risc_v/din_mem_i	-1	2685009...	0	10	0					1
/tb_risc_v/dout_mem_i	-47							22		
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}									

72   76   80   84   88   92

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Signal											
/tb_risc_v/rst_i											
/tb_risc_v/clk_i											
/tb_risc_v/pc_i	00400038	0040003C	00400040	00400044		0040001C	00400020	00400024	00400028		0040002C	00...
/tb_risc_v/instruction_i	FFF80813	00D525B3	FC058EE3	000506B3		02080863	00022403	41F45493	00944533		0014F493	00...
/tb_risc_v/addr_f	4026466...	4026466...	12	4026466...	4026466304						12	4026466...	40...
/tb_risc_v/addr_i	0	22	2685010...	4	0						2685010...	0	42...
/tb_risc_v/rw_i												
/tb_risc_v/en_i												
/tb_risc_v/din_mem_i	4194380	0	2685010...	0	10	0						
/tb_risc_v/dout_mem_i	22										-3	
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}											

96　100　104　108　112

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Signal											
/tb_risc_v/rst_i											
/tb_risc_v/clk_i											
/tb_risc_v/pc_i	00...	00400034	00400038	0040003C	00400040	0040001C		00400044	00400048	0040004C	0040001C	00400020
/tb_risc_v/instruction_i	00...	00420213	FFF80813	00D525B3	FC058EE3	02080863		000506B3	FD5FF0EF	00D2A023	02080863	00022403
/tb_risc_v/addr_f	40...	4026466...	4026466...	4026466...	16	4026466...	4026466...	4026466304			4026466...	4030660...
/tb_risc_v/addr_i	42...	2	1	3	2685010...	3	1	0			3	4194380
/tb_risc_v/rw_i											
/tb_risc_v/en_i											
/tb_risc_v/din_mem_i	0		4194380	-1	2685010...	0	10	0				
/tb_risc_v/dout_mem_i	-3											
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}											

116   120   124   128   132   136

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Signal											
/tb_risc_v/rst_i											
/tb_risc_v/clk_i											
/tb_risc_v/pc_i	00400024	00400028		0040002C	00400030	00400034	00400038	0040003C	00400040	00400044	00...
/tb_risc_v/instruction_i	41F45493	00944533		0014F493	00950533	00420213	FFF80813	00D525B3	FC058EE3	000506B3	02...
/tb_risc_v/addr_f	4026466304		16	4026466304		4026466...	4026466...	4026466...	20	4026466...	4026466304
/tb_risc_v/addr_i	0		2685010...	0		12	0	12	2685010...	2	0
/tb_risc_v/rw_i											
/tb_risc_v/en_i											
/tb_risc_v/din_mem_i	0					1	4194380	0	2685010...	0	3	0
/tb_risc_v/dout_mem_i	-3		12								
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}										

140   144   148   152   156   160

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Signal												
/tb_risc_v/rst_i												
/tb_risc_v/clk_i												
/tb_risc_v/pc_i	00...	00400020	00400024	00400028		0040002C	00400030	00400034	00400038	0040003C	00400040	0040001C
/tb_risc_v/instruction_i	02...	00022403	41F45493	00944533		0014F493	00950533	00420213	FFF80813	00D525B3	FC058EE3	02080863
/tb_risc_v/addr_f	4026466304			20	4026466304		4026466...	4026466...	4026466...	24		4026466...
/tb_risc_v/addr_i	0			2685010...	0		27	0	27	2685010...		1
/tb_risc_v/rw_i												
/tb_risc_v/en_i												
/tb_risc_v/din_mem_i	0							4194380	0	2685010...		0
/tb_risc_v/dout_mem_i	12			27								
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}											

164   168   172   176   180   184

Signal	Values
/tb_risc_v/rst_i	
/tb_risc_v/clk_i	
/tb_risc_v/pc_i	0040001C / 00400020 / 00400024 / 00400028 / 0040002C / 00400030 / 00400034 / 00400038 / 0040003C / 00400040 / 00...
/tb_risc_v/instruction_i	02080863 / 00022403 / 41F45493 / 00944533 / 0014F493 / 00950533 / 00420213 / FFF80813 / 00D525B3 / FC058EE3 / 02...
/tb_risc_v/addr_f	4026466304 / 24 / 4026466... / 4026466... / 4026466... / 4026466... / 4026466... / 28 / 40...
/tb_risc_v/addr_i	0 / 2685010... / 0 / 4294967... / 3 / 1 / 4 / 2685010... / 0
/tb_risc_v/rw_i	
/tb_risc_v/en_i	
/tb_risc_v/din_mem_i	3 / 0 / 4194380 / -1 / 2685010... / 0
/tb_risc_v/dout_mem_i	27 / -4
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}

188   192   196   200   204

Signal										
/tb_risc_v/rst_i										
/tb_risc_v/clk_i										
/tb_risc_v/pc_i	0040001C	00400020	0040004C	00400050	00400054	00400050	00400054	00400050	00400054	00400050	00400054
/tb_risc_v/instruction_i	02080863	00022403	00D2A023	000000EF	00000013	000000EF	00000013	000000EF	00000013	000000EF	00000013
/tb_risc_v/addr_f	4026466304					28	4030660...	4026466...	4030660...	4026466...	4030660...
/tb_risc_v/addr_i	0					2685010...	4194388	0	4194388	0	4194388
/tb_risc_v/rw_i										
/tb_risc_v/en_i										
/tb_risc_v/din_mem_i	0	3	0			3	0				
/tb_risc_v/dout_mem_i	-4										
/tb_risc_v/dmem/mem	{10 -47 22 -3 12 27 -4 0}					{10 -47 22 -3 12 27 -4 3}					

208   212   216   220   224   228

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