

1

2

A

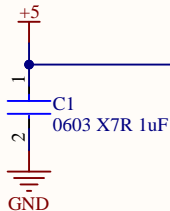
B

Main Board[4B] Rectified Audio IN Signal

Main Board[4B] 1REF\_IN 1ref

Main Board[4B] 2REF\_IN 2ref

Main Board[4B] 3REF\_IN 3ref



1

A

Main Bo

Main Bo

Main Bo

Main Bo

B

Main Bo

1

2

A



B

1

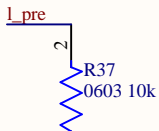
2

A

Main Board[2A] L\_IN l\_in

Main Board[2A] R\_IN r\_in

B

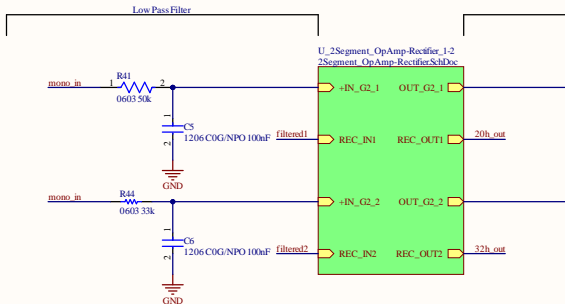


1

2

Main Board[2A] MONO\_IN mono\_in

A



B



1

2

A

Filter Bank[2A] +IN\_G2\_1 +in\_a

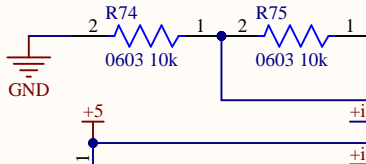
Filter Bank[2A] +IN\_G2\_2 +in\_d

Filter Bank[2A] REC\_IN1 +in\_b

Filter Bank[2B] REC\_IN2 +in\_c

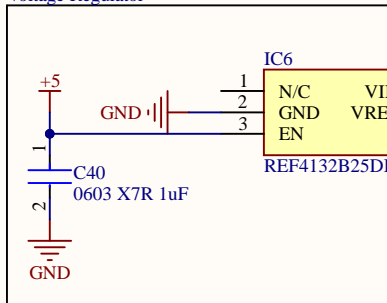
Segment 1

B



A

## Voltage Regulator

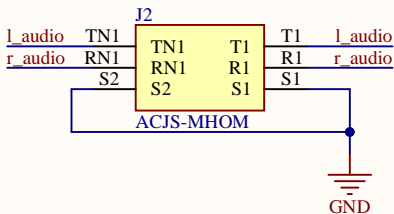


B



A

Audio In/Out



B

