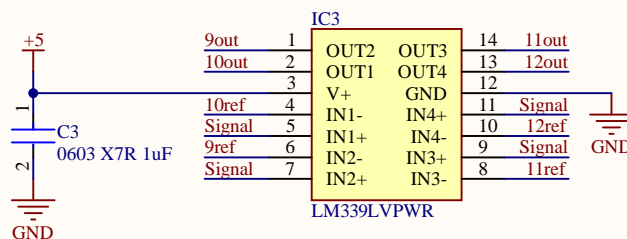
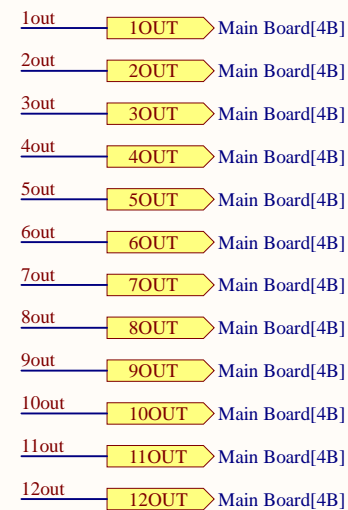
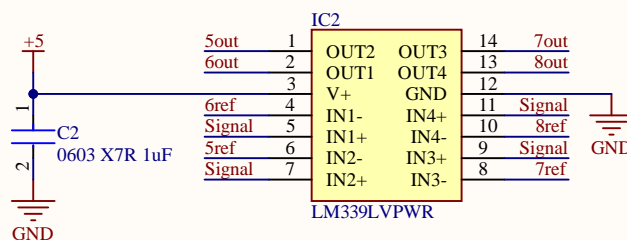
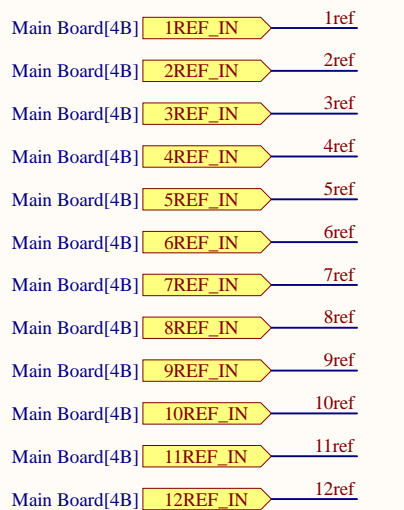
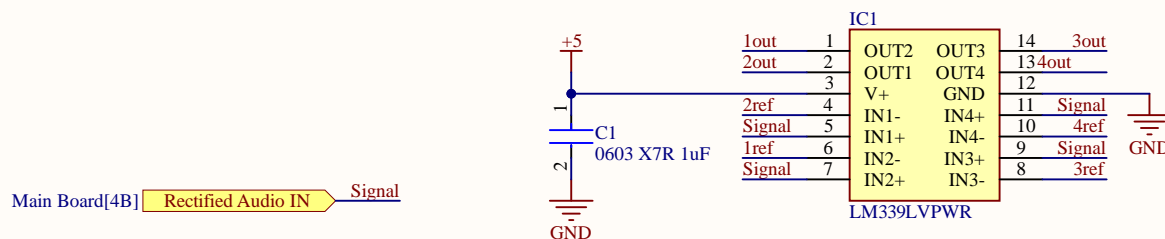
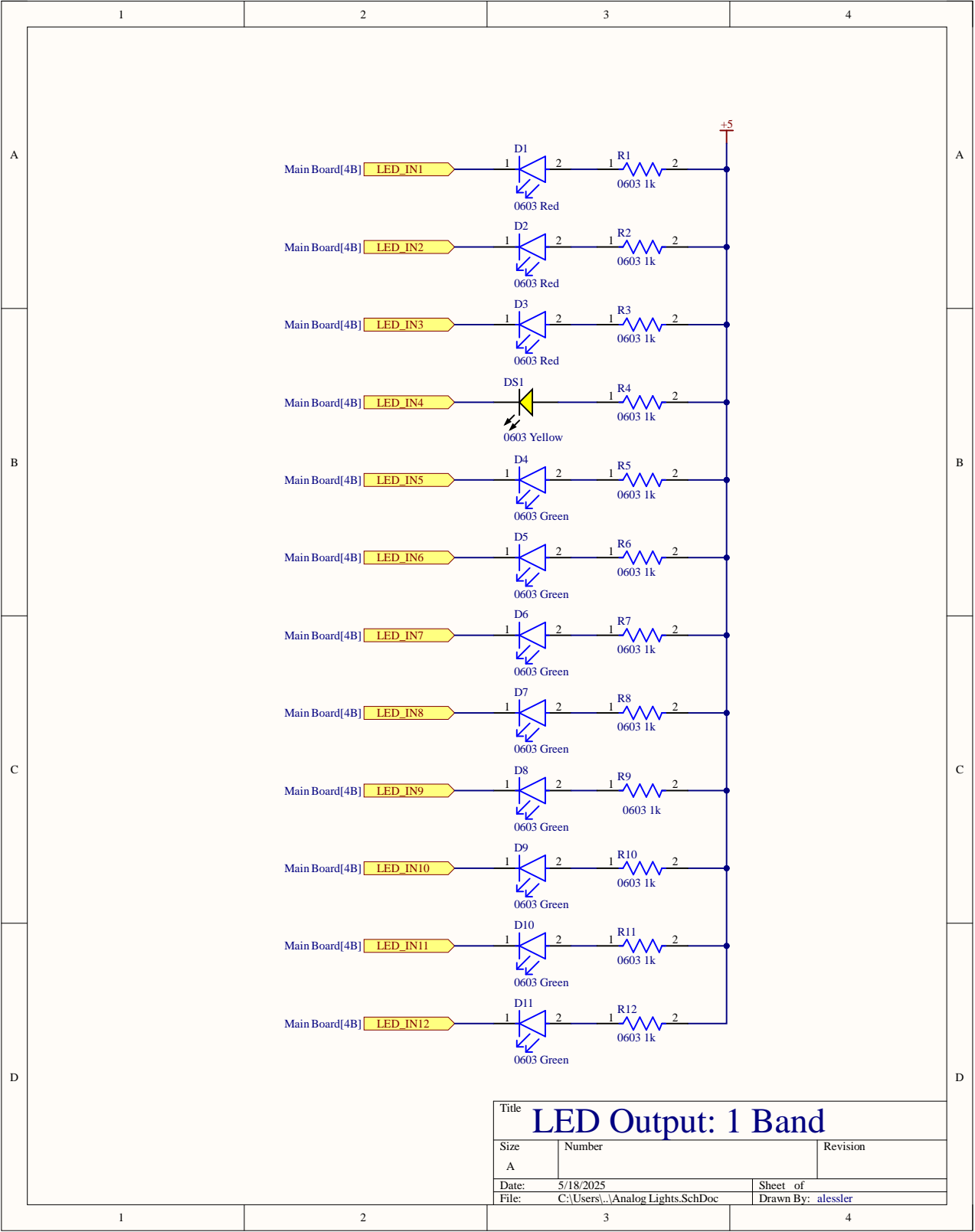
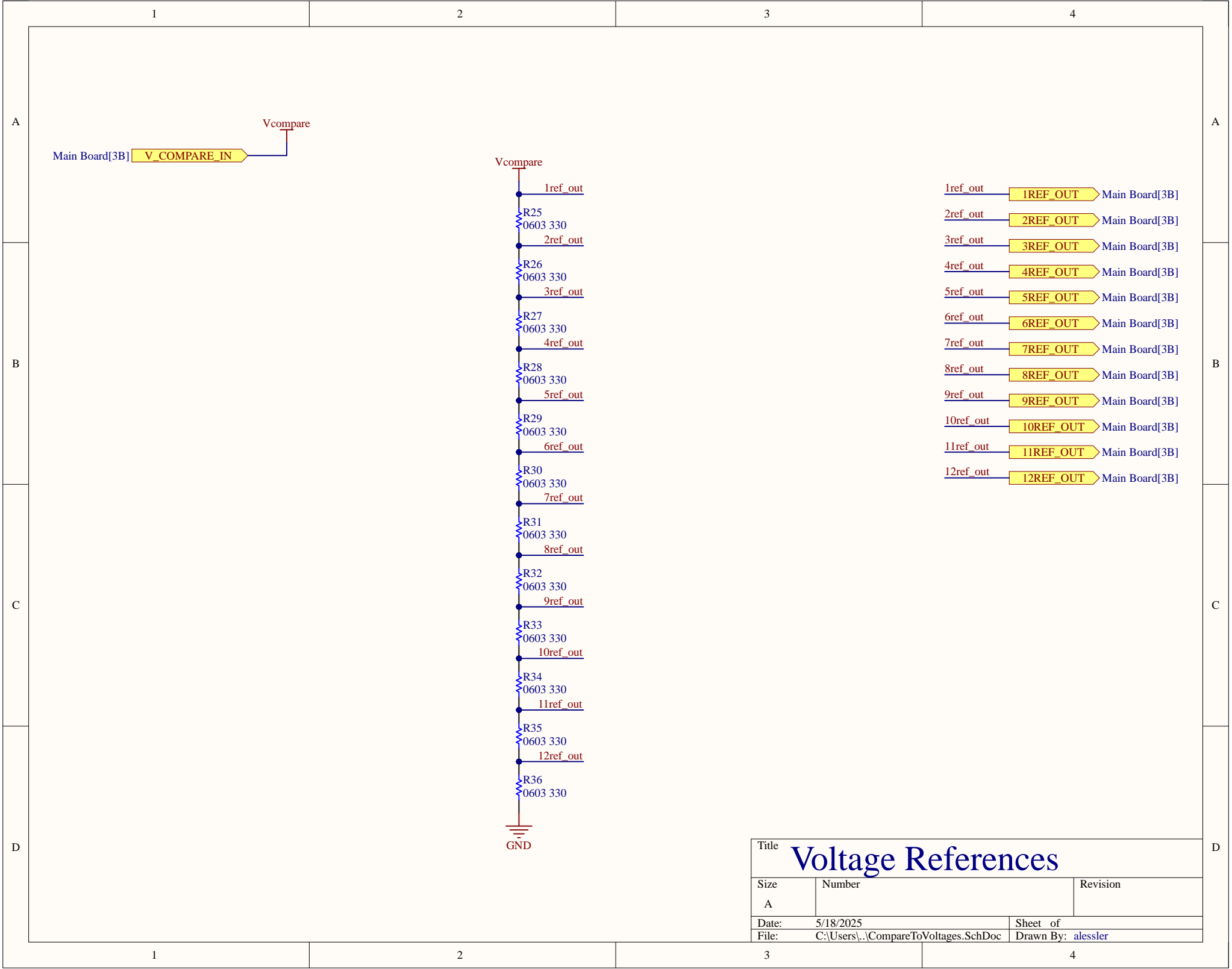


Main Board			
No.	Number	Revision	
1	1.0	Initial	
2	1.1	Minor	
3	1.2	Major	



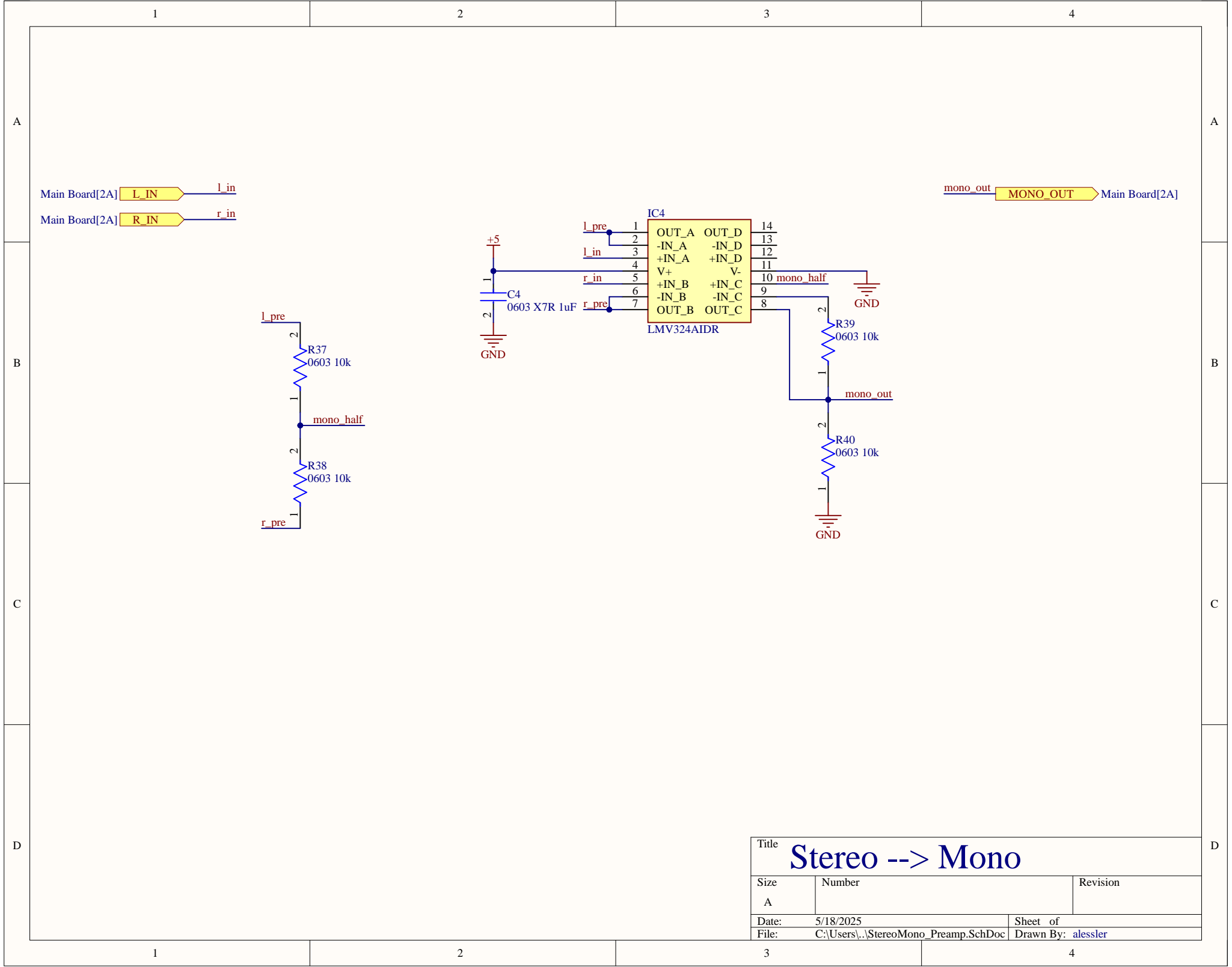
Title			Comparators: 1 Band		
Size	Number			Revision	
A					
Date:	5/18/2025			Sheet of	
File:	C:\Users\... \Comparator_Segment.SchDoc			Drawn By: alesssler	



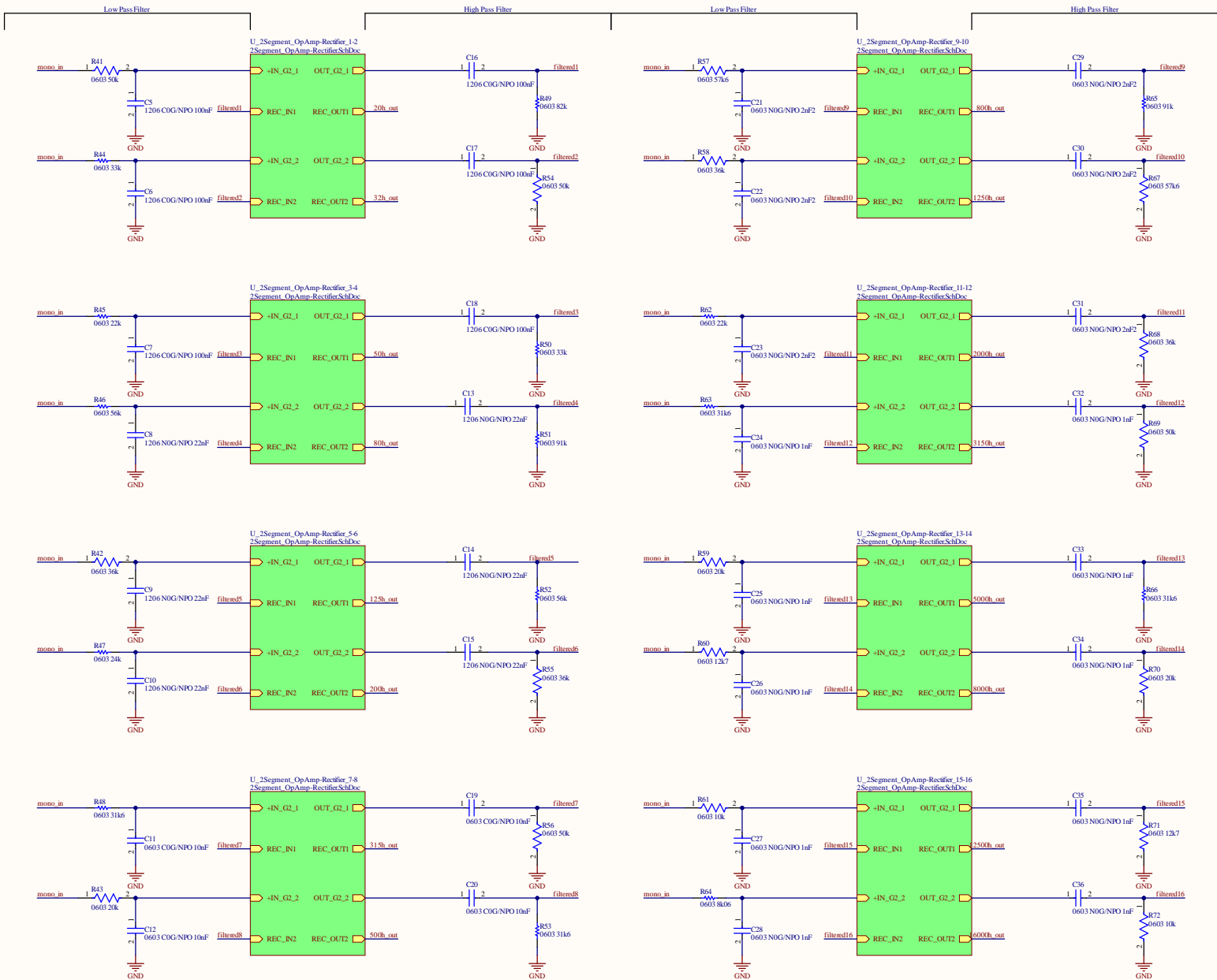


Voltage References

Size	Number	Revision
A		
Date:	5/18/2025	Sheet of
File:	C:\Users\...\CompareToVoltages.SchDoc	Drawn By: alessler



Main Board[2A] MONO_IN mono_in



- 20Hz_out 20Hz_OUT Main Board[3A]
- 32Hz_out 32Hz_OUT Main Board[3A]
- 50Hz_out 50Hz_OUT Main Board[3A]
- 80Hz_out 80Hz_OUT Main Board[3A]
- 125Hz_out 125Hz_OUT Main Board[3A]
- 200Hz_out 200Hz_OUT Main Board[3A]
- 315Hz_out 315Hz_OUT Main Board[3A]
- 500Hz_out 500Hz_OUT Main Board[3A]
- 800Hz_out 800Hz_OUT Main Board[3A]
- 1250Hz_out 1250Hz_OUT Main Board[3A]
- 2000Hz_out 2000Hz_OUT Main Board[3A]
- 3150Hz_out 3150Hz_OUT Main Board[3A]
- 5000Hz_out 5000Hz_OUT Main Board[3A]
- 8000Hz_out 8000Hz_OUT Main Board[3A]
- 12500Hz_out 12500Hz_OUT Main Board[3A]
- 16000Hz_out 16000Hz_OUT Main Board[3A]

Title		
Filters: All Bands		
Size	Number	Revision
C		
Date:	5/18/2025	Sheet of
File:	C:\Users\... \Filter Bank_SchDoc	Drawn By: alexsdr

A

B

C

D

A

B

C

D

Filter Bank[2A] **+IN_G2_1** **+in_a**

Filter Bank[2A] **+IN_G2_2** **+in_d**

Filter Bank[2A] **REC_IN1** **+in_b**

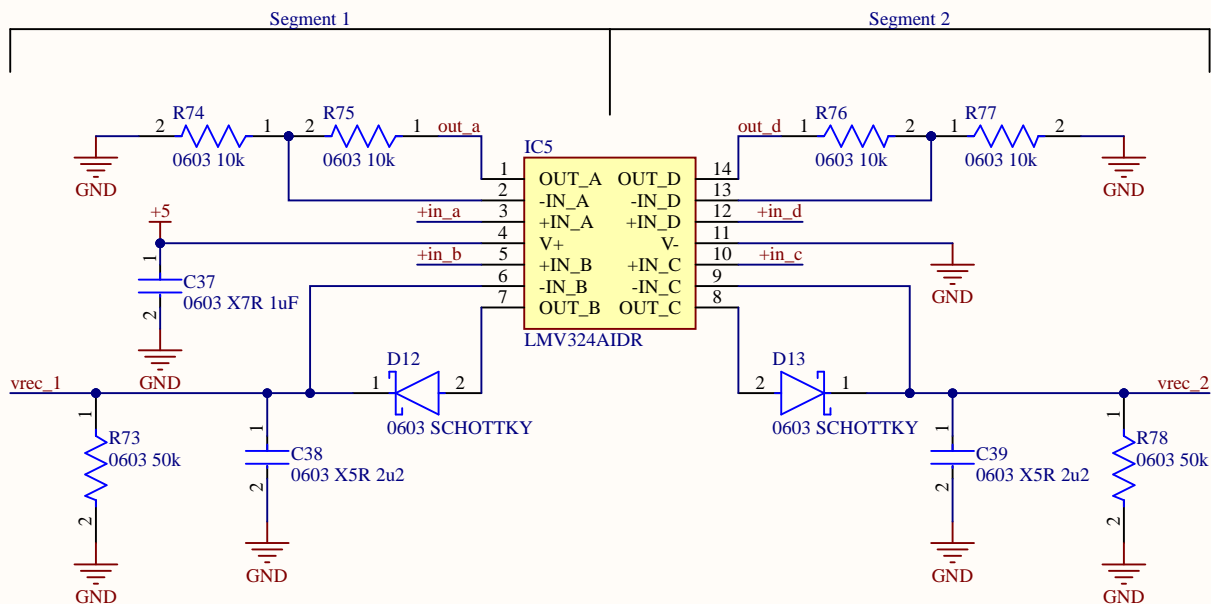
Filter Bank[2B] **REC_IN2** **+in_c**

out_a **OUT_G2_1** Filter Bank[3A]

out_d **OUT_G2_2** Filter Bank[3A]

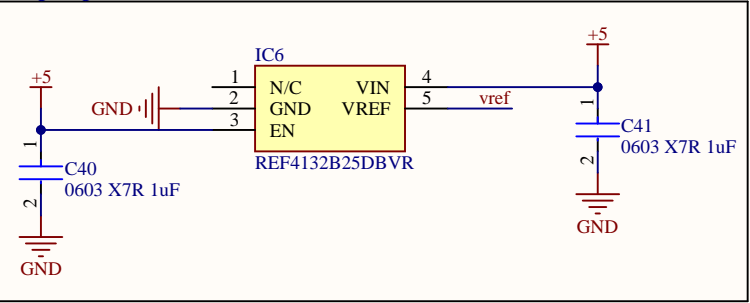
vrec_1 **REC_OUT1** Filter Bank[3A]

vrec_2 **REC_OUT2** Filter Bank[3B]

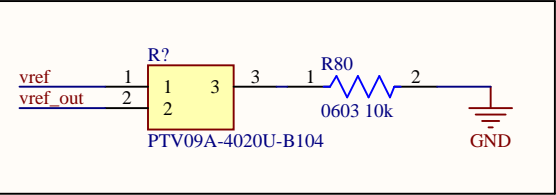


Title			Signal Rectification: 2 Bands		
Size	Number		Revision		
A					
Date:	5/18/2025		Sheet of		
File:	C:\Users\...\2Segment_OpAmp-Rectifier.SchDoc		Drawn By: alessler		

Voltage Regulator



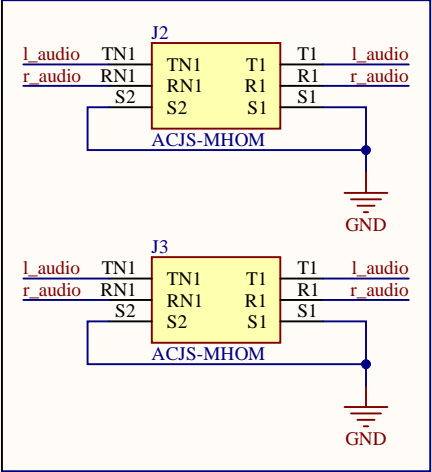
Potentiometer



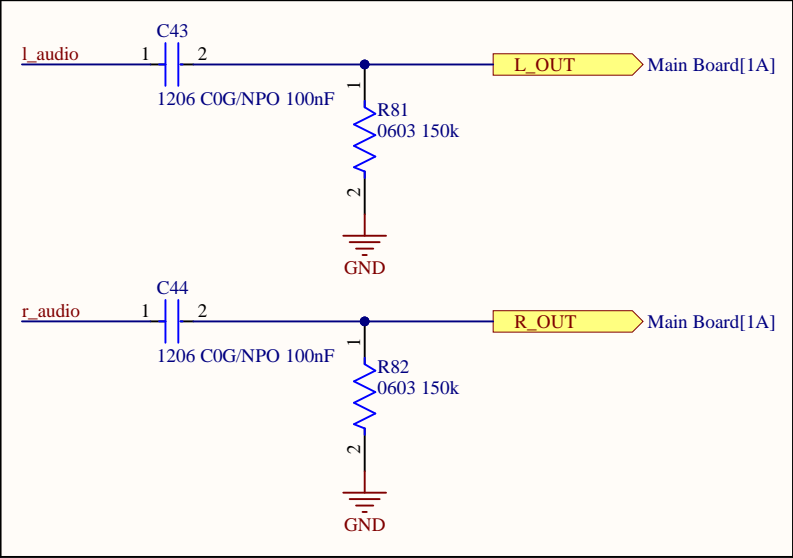
vref_out → VREF_OUT → Main Board[2A]

Title			Voltage Regulator	
Size	Number		Revision	
A				
Date:	5/18/2025		Sheet of	
File:	C:\Users\...\Adjustable Reference Voltage.SchDoc		Drawn By: alessler	

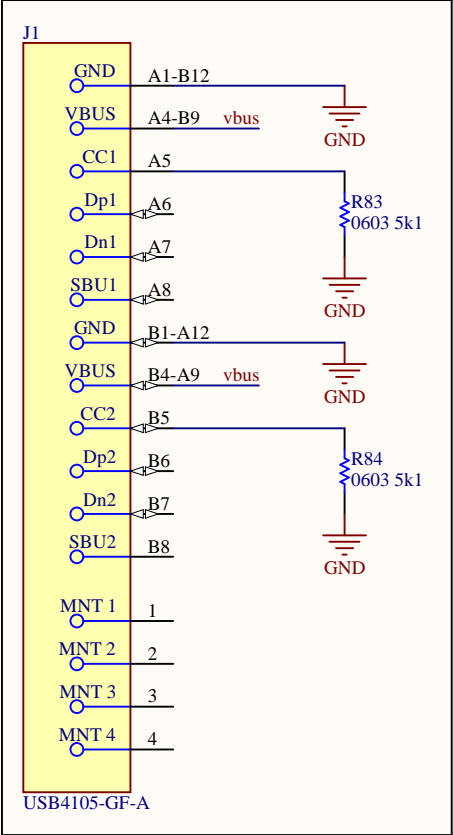
Audio In/Out



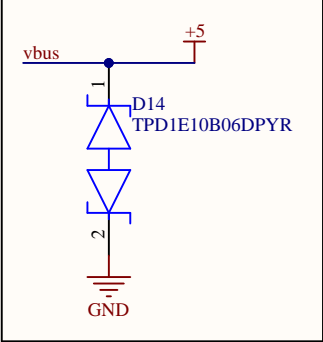
Audio Coupling



USB-C Power Input



ESD Protection



Title		
Board I/O		
Size	Number	Revision
A		
Date:	5/18/2025	Sheet of
File:	C:\Users\...\Board I-O.SchDoc	Drawn By: alessler