

[illegible]

# SALIDAS

Diagram illustrating the pin connections for the SALIDAS header, showing connections for J2 and J3 headers.

**J2 (Conn\_01x15) Pin Connections:**

Pin	Function
1	ADC_EX
2	ADC
3	GPIO_10
4	GPIO_9
5	MOSI
6	CS0
7	MISO
8	SCLK
9	EN
10	nRST
11	
12	
13	
14	
15	GND

**J3 (Conn\_01x15) Pin Connections:**





Pin	Function
1	GPIO_16
2	GPIO_5
3	GPIO_4
4	GPIO_0
5	GPIO_2
6	
7	
8	GPIO_14
9	GPIO_12
10	GPIO_13
11	GPIO_15
12	RXD
13	TXD
14	
15	GND

Power connections are shown for +5V, +3.3V, and +3.3V, with GND connections at the bottom.

## ALIMENTACION

The diagram illustrates the power supply circuit for the AP1117-33. The main circuit consists of a 5V input connected to the VI pin of the AP1117-33 regulator. The regulator's GND pin is connected to ground. The output of the regulator (VO pin) is connected to a 3.3V output. A 10uF/25v capacitor (C3) is connected between the 5V input and ground. Another 10uF capacitor (C6) is connected between the 3.3V output and ground. A PWR\_FLAG pin is also shown connected to ground. To the right, a separate circuit shows a VBUS input connected to a 1N5819 diode, which is connected to a +5V output.

# AGUJEROS DE SUJECION

	H1 MountingHole		H3 MountingHole
	H2 MountingHole		H4 MountingHole

Id: 1/1