Alexander Hegedus-Adkin

Engineering student & UKESF scholar, Imperial College London

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Education

Imperial College London — **September 2024 - present**
Electronic and Information Engineering — MEng
70.4% (Y1)
https://www.imperial.ac.uk

King Edward VI School, Soton — **September 2019 - June 2024**
A-Level — Secondary/6th Form
A*A*A*A
https://kes.school

A*, A*, A* (Maths, Further Maths, Computer Science), A (Physics) at A-Level

Experience

Imperial College London — **October 2025 - Present**
Undergraduate Teaching Assistant (UTA) — South Kensington, London, UK
https://www.imperial.ac.uk/

Currently working as a UTA for Digital Electronics and Computer Architecture (DECA). This involves me helping explain basic digital electronics concepts to students and walking them through where they get stuck, as well as supervising and being a helpful resource during lab sessions.

Projects

FRED/FREDDO — **November - December 2025/Present**
RISC-V hart in SystemVerilog
https://github.com/ELEC50010-IAC-Ridgewell-Team12/fred

Worked on μarch design, ALU and branch prediction for a 5-stage pipelined RV32I hart.

STEVE — **October - December 2025**
*nix shell
https://github.com/ELEC50014-SoftwareSystems-AloeVera/steve

Created a *nix shell in C with support for I/O redirection, pipes, batch processing, and (limited support for) subshells.

EEE SeaBoat 2025 — **May - June 2025**
https://github.com/hakanmerdan/EEESeaBoat2025

Helped produce a remote-controlled rover capable of sensing and identifying infrared, radio, ultrasonic, and magnetic data. Worked on embedded programming, motor control, and sensor integration to connect sensors to a moving, controllable platform.

Internet

alex-ha-192

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Summary

I am a skilled and hard-working Electronic Engineering student currently pursuing opportunities in software development (particularly backend) and digital design (particularly RTL design using HDLs). I can bring a wealth of academic experience and competence, and a hunger to take that success to the next level.

Skills

Hardware Engineering

SystemVerilog, PCB, HDL, Computer Architecture, RISC-V

Programming

C++, C, UNIX

Soft Skills

Communication

Communicating requirements and deadlines clearly, Raises potential issues promptly

Independent learner

Willing to devote time to external research and upskilling, Interest for many areas of programming and design

Languages

English — Native — ●●●●●
German — A1 — ●○○○○

Awards

Academic Scholarship
King Edward VI School, Soton
September 2019
https://kes.school

UKESF Scholarship
UKESF
December 2025
https://www.ukesf.org/

Scholarship/internship program involving work on electronics outreach.