



Alexander Hegedus-Adkin

Engineering student & UKESF scholar, Imperial College London

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🌐 <https://alex-ha.com>

○ Internet ○



GitHub

alex-ha-192



LinkedIn

Alex Hegedus-Adkin

○ Skills ○

Hardware Engineering

SystemVerilog, PCB design, HDL, Computer Architecture, RISC-V

Programming

C++, C, UNIX

○ Soft Skills ○

Communication

Communicating requirements and deadlines clearly, Raises potential issues promptly

Independent learner

Willing to devote time to external research and upskilling, Interest for many areas of programming and design

○ Languages ○

English **German**

Native A1

○ Awards ○

Academic Scholarship

September 2019

King Edward VI School, Soton
<https://kes.school>

UKESF Scholarship

December 2025

UKESF
Scholarship/internship program involving work on electronics outreach.
<https://www.ukesf.org/>

Summary

I am a skilled and hard-working Electronic Engineering student currently pursuing opportunities in software development (particularly backend) and digital design (particularly RTL design using HDLs). I can bring a wealth of academic experience and competence, and a hunger to take that success to the next level.

Education

Imperial College London

Electronic and Information Engineering

MEng • 70.4% (Y1)

September 2024 - present

<https://www.imperial.ac.uk>

King Edward VI School, Soton

A-Level

Secondary/6th Form • A*A*A*A

September 2019 - June 2024

A*, A*, A* (Maths, Further Maths, Computer Science), A (Physics) at A-Level
<https://kes.school>

Experience

Imperial College London

Undergraduate Teaching Assistant (UTA)

South Kensington, London, UK

October 2025 - Present

Currently working as a UTA for Digital Electronics and Computer Architecture (DECA). This involves me helping explain basic digital electronics concepts to students and walking them through where they get stuck, as well as supervising and being a helpful resource during lab sessions.

<https://www.imperial.ac.uk/>

Projects

FRED/FREDDO RISC-V hart

November - December 2025/Present

Worked on march design, ALU and branch prediction for a 5-stage pipelined RV32I hart.

<https://github.com/ELEC50010-IAC-Ridgewell-Team12/fred>

STEVE shell

October - December 2025

Created a *nix shell in C with support for I/O redirection, pipes, batch processing, and (limited support for) subshells.

<https://github.com/ELEC50014-SoftwareSystems-AloeVera/steve>

First Year Group Project

May - June 2025

Helped produce a remote-controlled rover capable of sensing and identifying infrared, radio, ultrasonic, and magnetic data. Worked on embedded programming, motor control, and sensor integration to connect sensors to a moving, controllable platform.

<https://github.com/hakanmerdan/EEESeaBoat2025>