Alexander Hegedus-Adkin

Engineering student & UKESF scholar, Imperial College London

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https://alex-ha.com

Summary

I am a skilled Electronic Engineering student currently pursuing opportunities in digital design (particularly RTL). I can bring a wealth of experience and competence, and a hunger to take that success to the next level.

Internet

GitHub
alex-ha-192

LinkedIn
Alex Hegedus-Adkin

Skills

Hardware Engineering
SystemVerilog, PCB design, HDL, Computer Architecture, RISC-V

Programming
C++, C, UNIX

Soft Skills

Communication
Communicating requirements and deadlines clearly, Raises potential issues promptly

Independent learner
Willing to devote time to external research and upskilling, Interest for many areas of programming and design

Languages

English
Native

German
A1

Awards

Academic Scholarship September 2019
King Edward VI School, Soton
https://kes.school

UKESF Scholarship December 2025
UKESF
Scholarship/internship program involving work on electronics outreach.
https://www.ukesf.org/

Education

Imperial College London MEng • 70.4% (Y1)
Electronic and Information Engineering September 2024 - present

83% - Digital Electronics and Computer Architecture
https://www.imperial.ac.uk

King Edward VI School, Soton Secondary/6th Form • A*A*A*A
A-Level September 2019 - June 2024

A*, A*, A* (Maths, Further Maths, Computer Science), A (Physics) at A-Level
https://kes.school

Projects

FRED/FREDDO RISC-V hart November - December 2025/Present
Worked on μarch design, ALU and branch prediction for a 5-stage pipelined RV32I hart.
https://github.com/ELEC50010-IAC-Ridgewell-Team12/fred

STEVE shell October - December 2025
Created a *nix shell in C with support for I/O redirection, pipes and batch processing.
https://github.com/ELEC50014-SoftwareSystems-AloeVera/steve

First Year Group Project May - June 2025
Helped produce a remote-controlled rover capable of sensing IR, radio, ultrasonic, and magnetic data. Worked on embedded programming/sensor integration.
https://github.com/hakanmerdan/EEESeaBoat2025

Experience

Imperial College London South Kensington, London, UK
Undergraduate Teaching Assistant (UTA) October 2025 - Present
Currently working as a UTA for Digital Electronics and Computer Architecture (DECA). This involves me helping explain basic digital electronics concepts to students and walking them through where they get stuck, as well as supervising and being a helpful resource during lab sessions.
https://www.imperial.ac.uk/