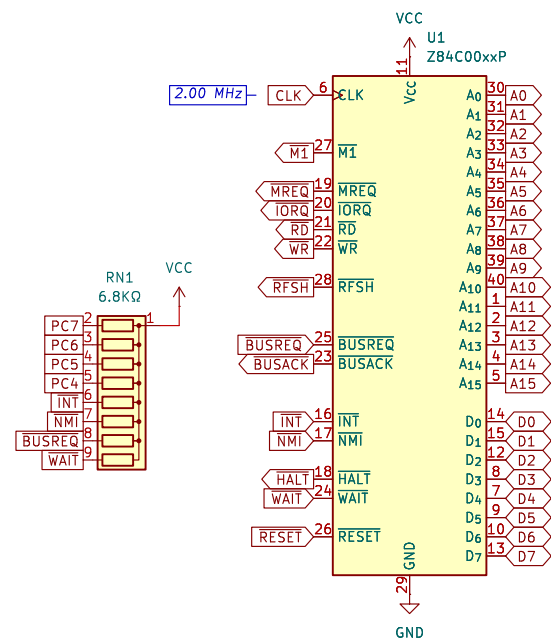
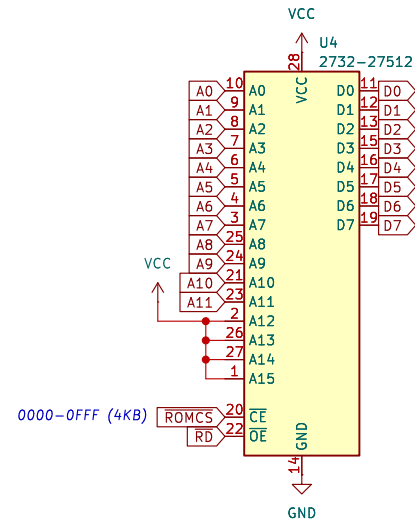


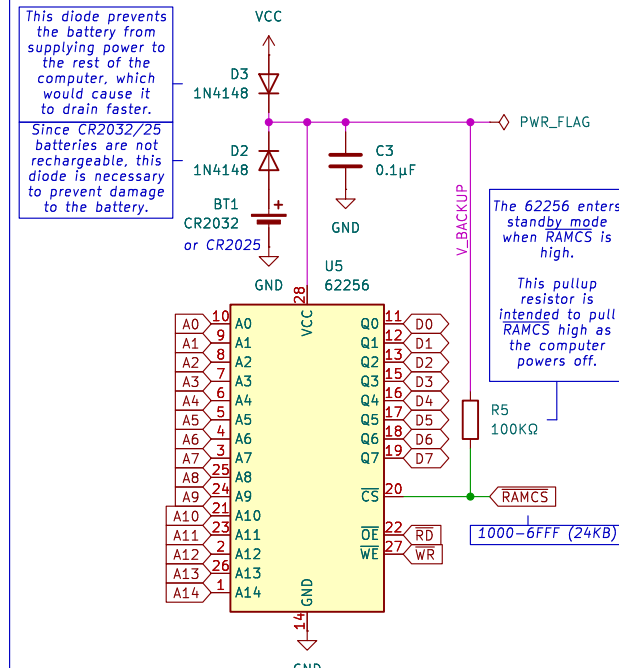
CPU (2.00 MHz)



ROM (4KB, 0000–0FFF)

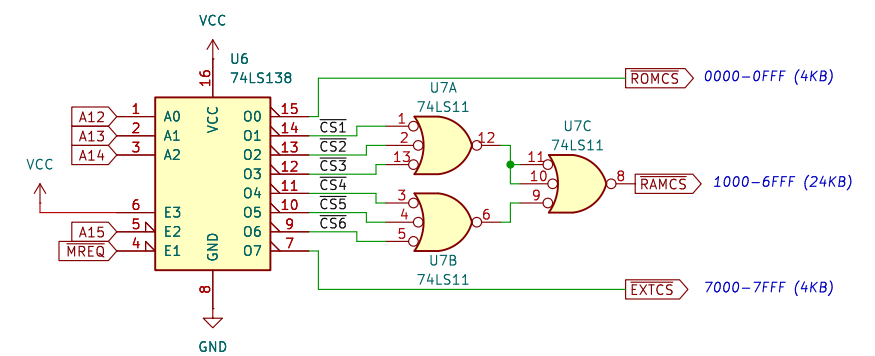


Battery-Backed RAM (24KB usable, 1000–6FFF)

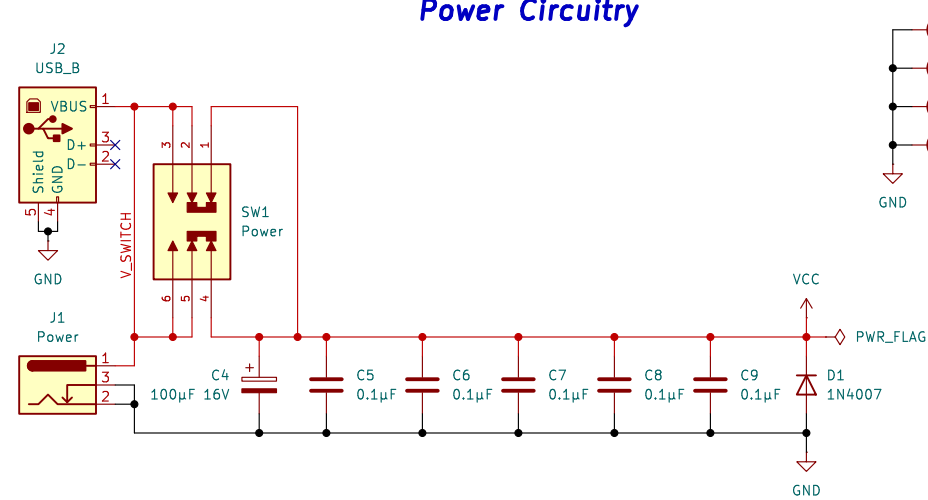


Memory Select

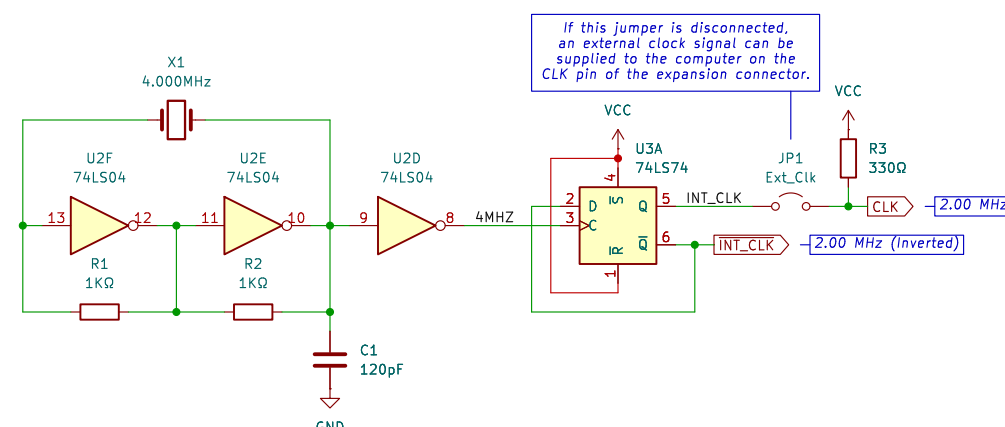
In addition to 7000–7FFF, the upper 32KB of memory (8000–FFFF) is also free for expansion.



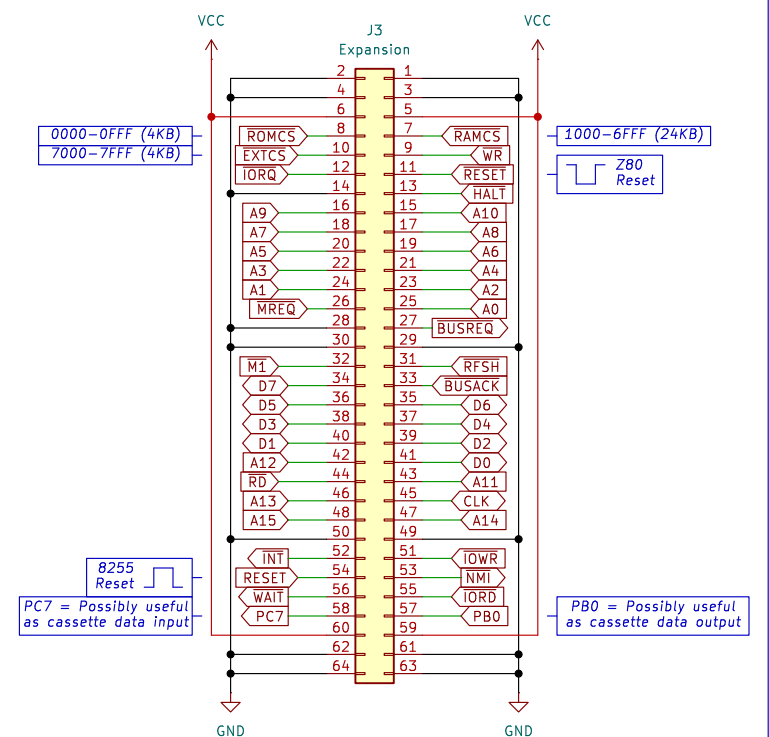
Power Circuitry



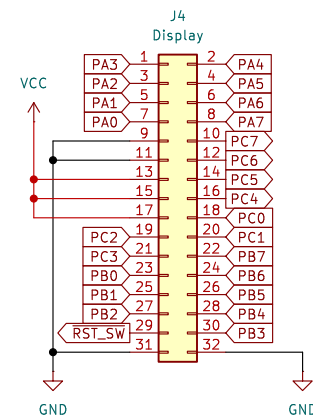
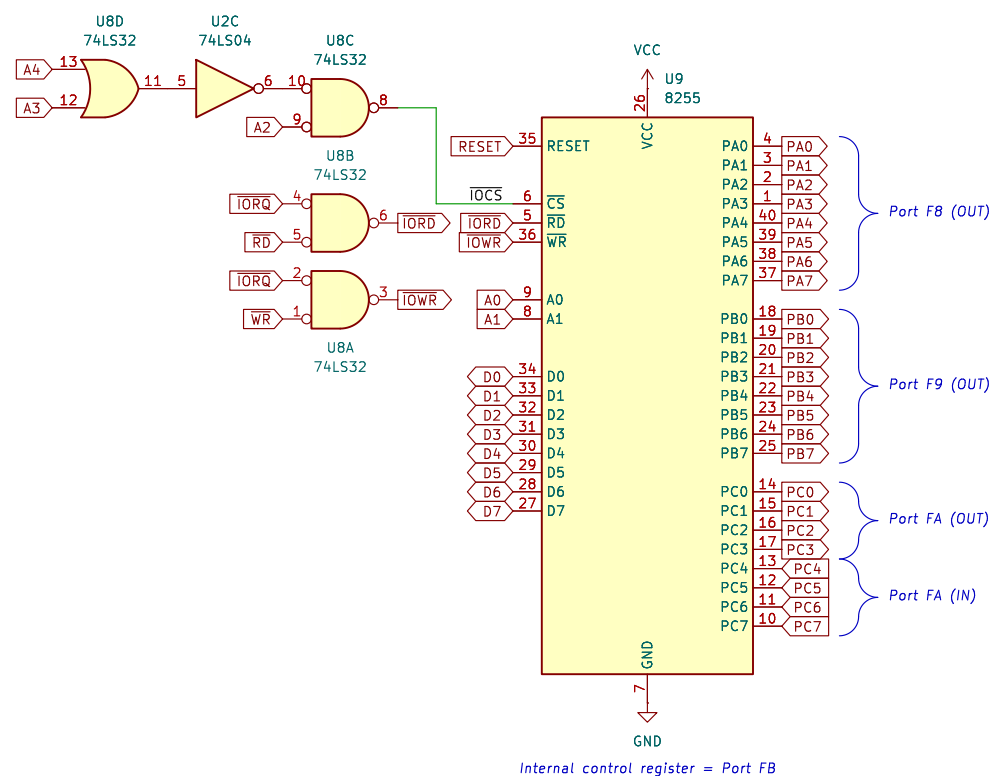
Internal Clock Generator (2.000 MHz)



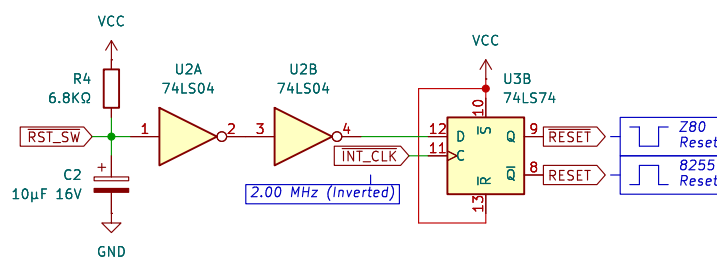
System Expansion Connector



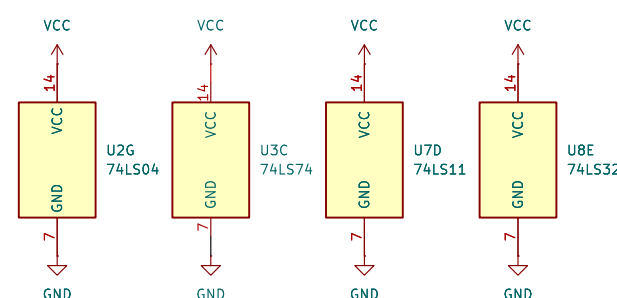
I/O Controller



Synchronous Reset Circuit



IC Power Pins



Circuit designed by Jaroslav T. Hyan. 1KB ROM/1KB RAM version first published in "Mikroprocesor Z80 a jeho aplikace" (Czechoslovakia, 1984), then upgraded to 1KB ROM/2KB RAM in "Radioamatérské konstrukce 4" (Czechoslovakia, 1990). This version has 4KB ROM/24KB RAM, while maintaining compatibility with the original memory map.

Alex J. Lowry (alex-j-lowry.github.io)

Sheet: /
File: Savia_84.kicad_sch

Title: Savia 84 (System)

Size: User Date: 2025-11-11 Rev: 1
KiCad E.D.A. 9.0.5 Id: 1/1