

54321

Децимальный номер	DKST 70.1.2	Название проекта	Разработал	Кол-во исполнений
Описание варианта 1	Описание варианта 6			
Описание варианта 2	Описание варианта 7			
Описание варианта 3	Описание варианта 8			
Описание варианта 4	Описание варианта 9			
Описание варианта 5	Описание варианта 10			

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Номер ревизии	Дата изменения	Автор изменения	Содержание изменения
1	28.08.12	Kondratov	Initial revision
2	21.10.13	Kondratov	Added X1800 w periph., U6, changed U104,I/O supply changed to 5V

Note: All new or moved CPU's nets are marked with BLUE colour  
New and changed partnumbers and values are marked with RED colour

Detailed List of corrections in Rev.2 :

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1) Changed R810 to 11,6kOm (Vout=4.15B)  
2) Change R602,R603,R610,R611,R618,R619,R626 to 3,3kOm  
3) Changed R10 to 4,7kOm  
4) Changed R511 to 17,4 kOm 1%  
5) Changed R635, R637, R613 to 330 Ohm  
6) Removed D500,D507  
8) Connected ADC\_I, ADC\_U,SIM900 signals to CPU pins  
9) Changed type of U104  
10)Connected 1W IN/OUT signals to MAT/CAP CPU pins  
11) I/O switches are powered from 5V now  
12) R208 (DEVICE\_ID) was changed to reflect Ver.2  
13) Added U6 (AND logic for RS232/485 RX signals)  
14) UART1\_RTS and CTS are reconnected to other CPU pins  
15) EN\_I2CREP signal is removed from sch and CPU's pin

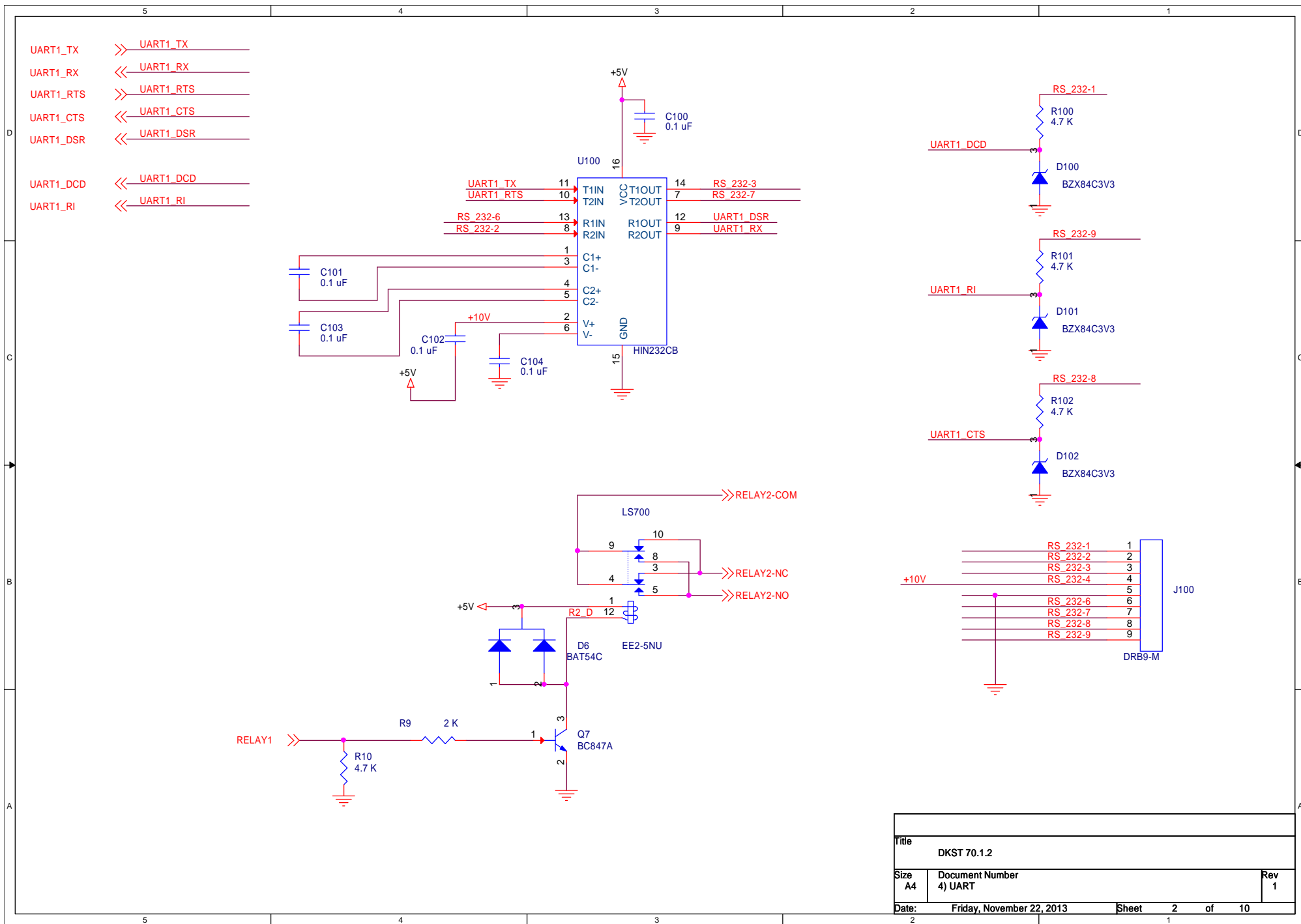
TODO -Implement changes in 1W driver

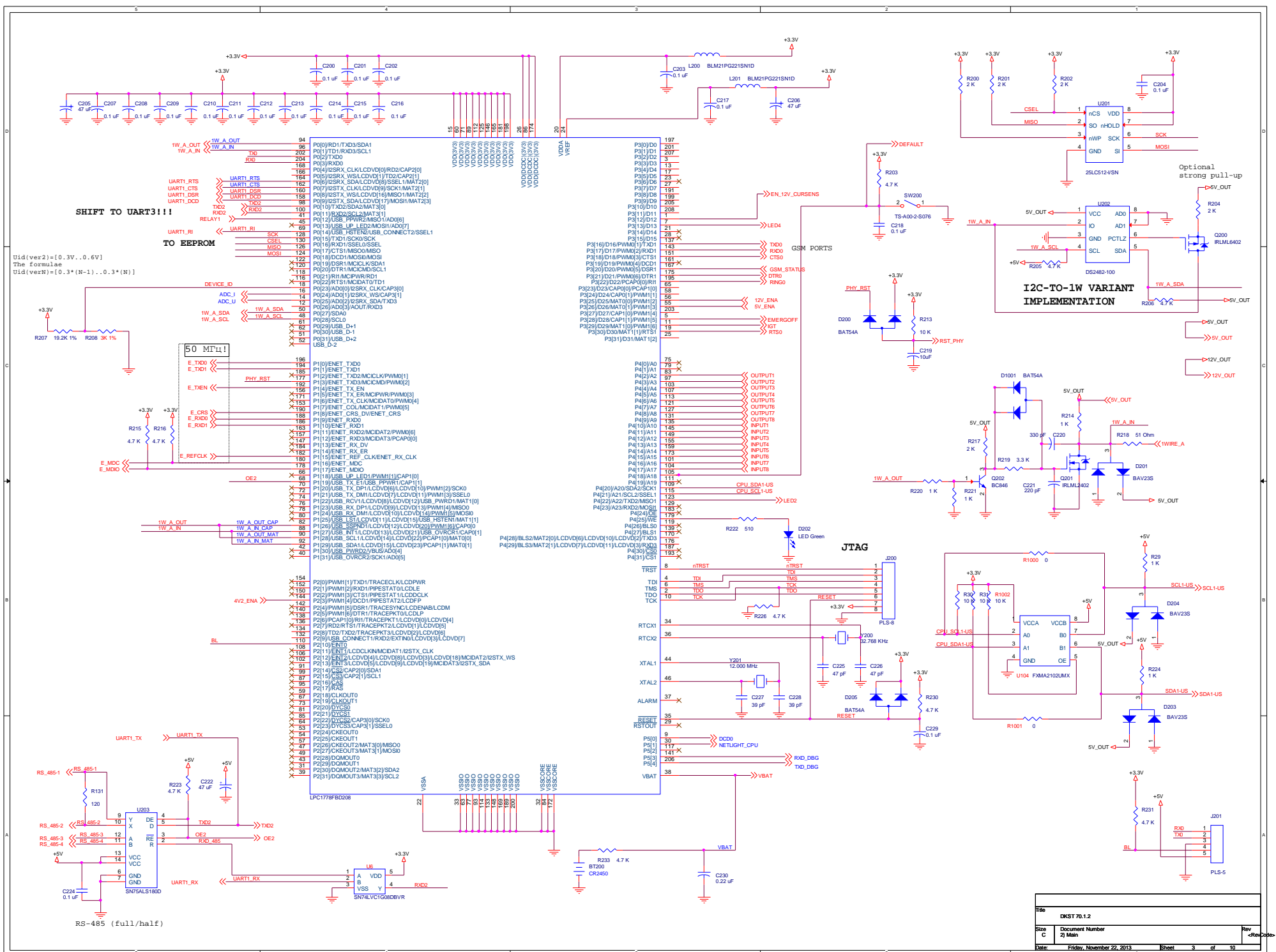
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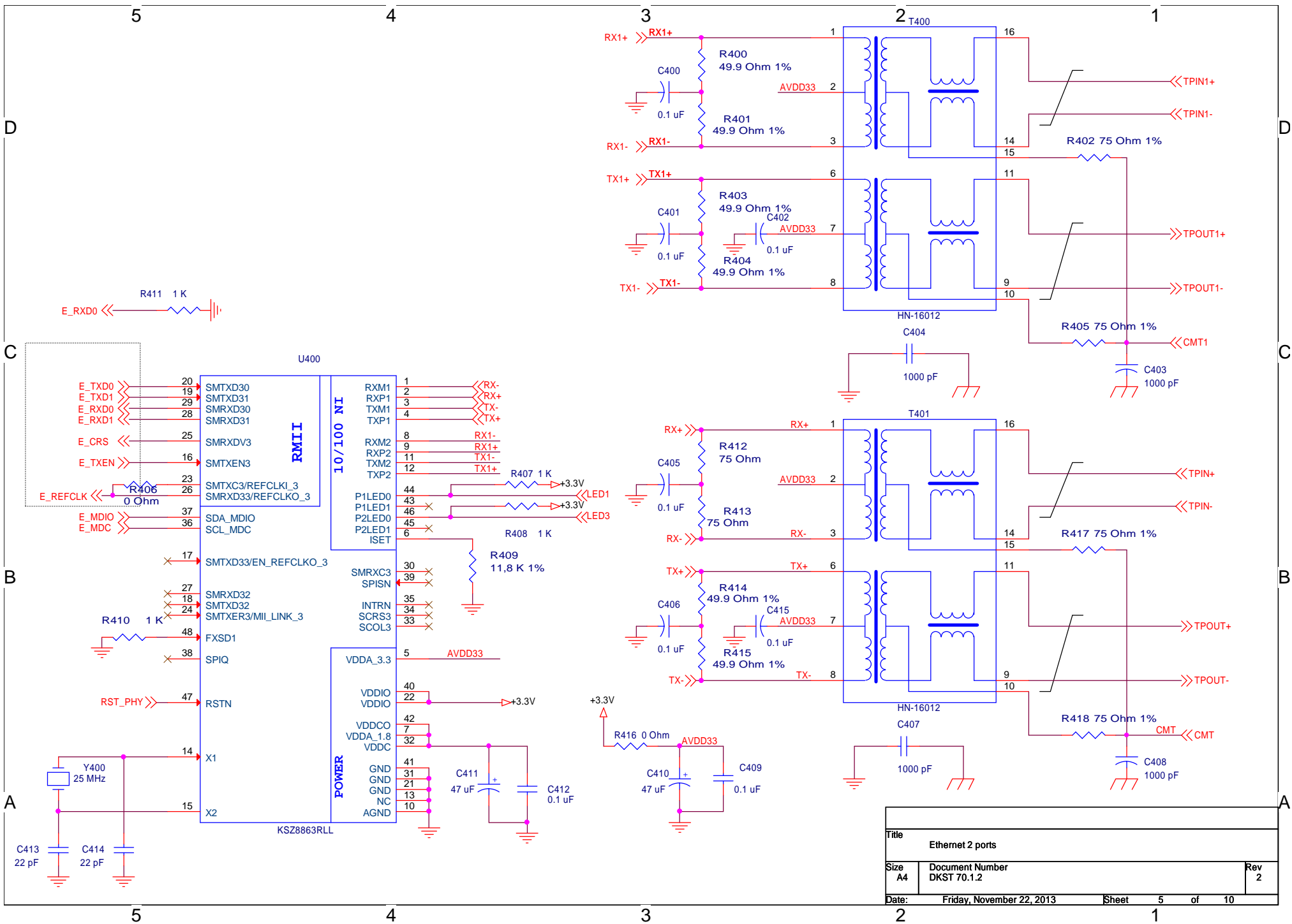
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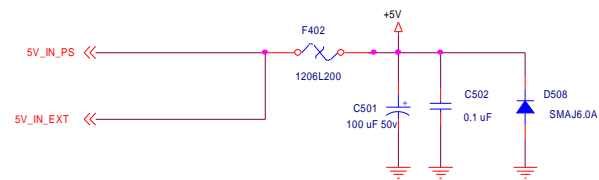




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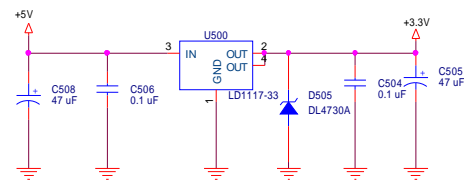
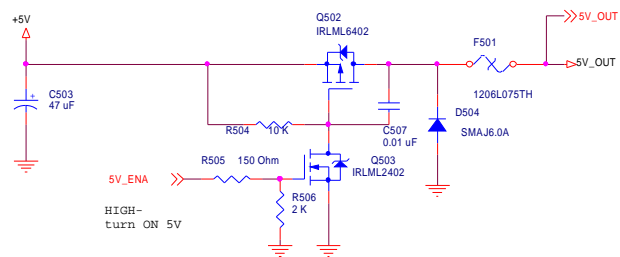
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12V\_OUT  
12V\_OUT  
5V\_OUT  
5V\_OUT

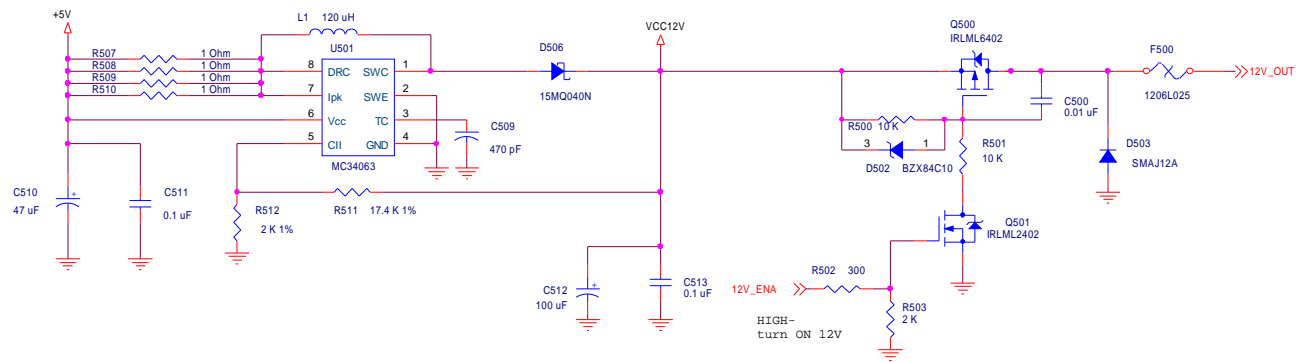
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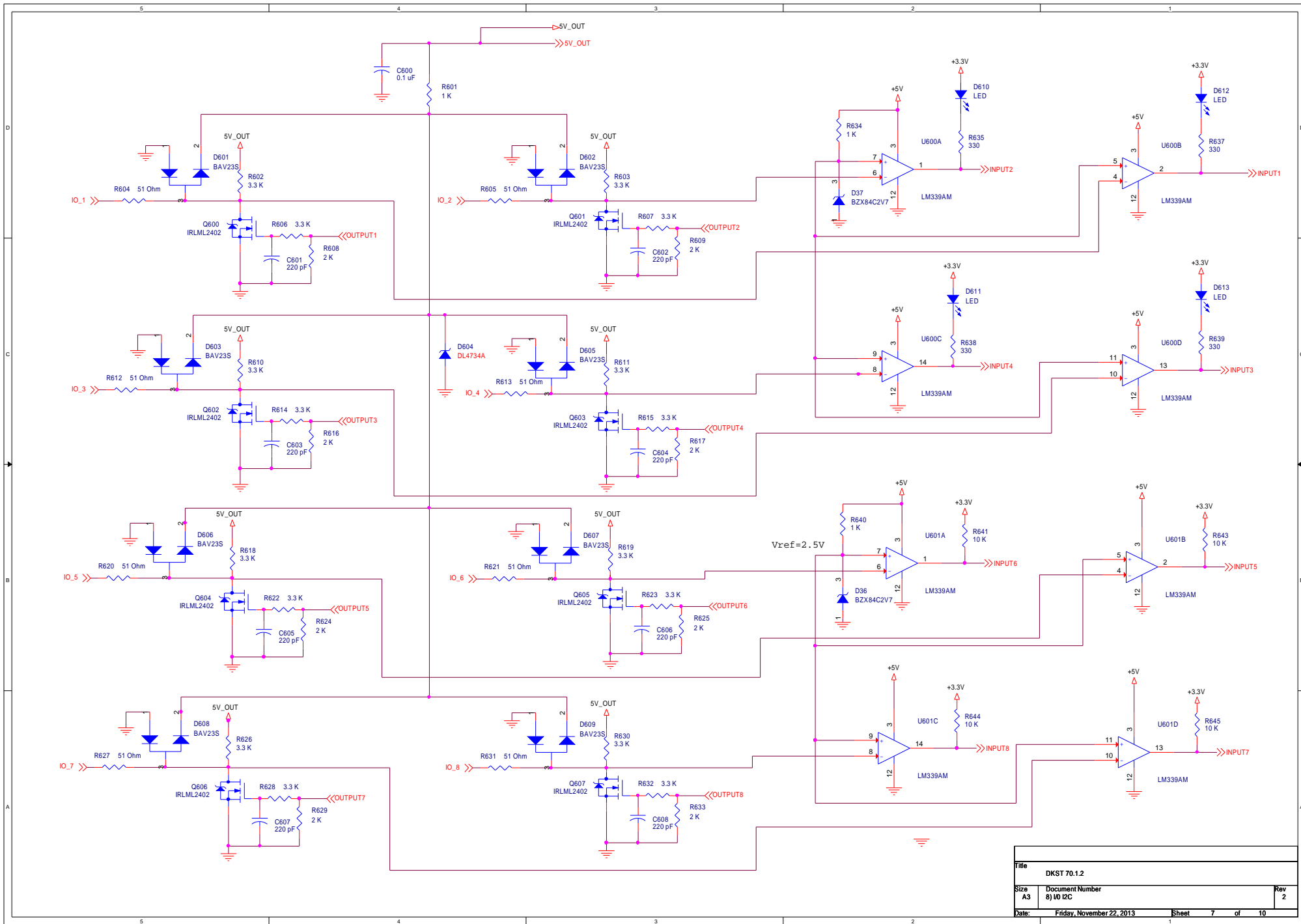


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