## ADMUX – ADC Multiplexer Selection Register

Bit
(0x7C)

Read/Write

Initial Value



REFS1

R/W

REFS0

R/W

**ADLAR** 

R/W

MUX3

R/W

R

MUX2

R/W

MUX1

R/W

MUX0

R/W

**ADMUX**