DDRB – The Port B Data Direction Register Bit 7 6 5 4 3 2 1 0

R/W

R/W

R/W

R/W

Bit	1	6	5	4	3	2	1	Ü	
0x04 (0x24)	DDB7	DDB6	DDB5	DDB4	DDB3	DDB2	DDB1	DDB0	DDRE

R/W

Read/Write

Initial Value

R/W

R/W

R/W