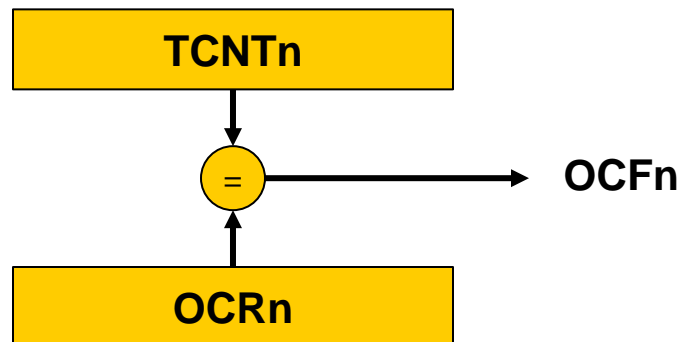


# Peripheral Modules: TIMERS

## Events on timers (cont.)

### Compare match:

The **OCRn** register (Output Compare Register) can be loaded with a value between 0 and maximum value. OCRn and TCNTn are compared in each clock cycle, a match produces the event, which is indicated by the **OCFn** (Output Compare Flag) flag.



The resource can be configured so that the timer restarts after a match (**CTC mode**, clear timer on compare match) and so that a dedicated terminal can be adjusted, cleaned or switched automatically.

In the ATMega328P there are two comparison registers for each timer, so that 6 events can be generated and in 6 dedicated terminals you can have an automatic response.