**Semnale de control MIPS32**

Semnale de Branch opționale: ? ϵ {gez, ne, gtz}, se va înlocui ? cu o valoare din paranteză, dacă e cazul

Tipuri de operații care se pun în paranteză la ALUOp si ALUCtrl:

(+), (-), (&), (|), (^), (<<*l*), (<<*lv*), (>>*l*), (>>*a*), (<)

Semnificații: & - AND, | - OR, ^ *- XOR, l* *- logic, a - aritmetic, v - cu variabilă*

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| **Instrucțiune** | **Opcode**  *Instr[31-26]* | **RegDst** | **ExtOp** | **ALUSrc** | **Branch** | **Br\_?**  (opțional) | **Jump** | **JmpR** (opțional) | **MemWrite** | **MemtoReg** | **Reg Write** | **ALUOp[2:0]** | **function**  *Instr[5-0]* | **ALUCtrl[2:0]** |
| ADD | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000000 | 010(+) |
| SUB | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000001 | 110(-) |
| SLL | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000010 | 011(<<l) |
| SRL | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000011 | 100(>>l) |
| AND | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000100 | 000(&) |
| OR | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000101 | 001(|) |
| XOR | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000110 | 101(^) |
| SRA | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 010(R) | 000111 | 111(>>a) |
| ADDI | 001000 | 0 | 1 | 1 | 0 |  | 0 |  | 0 | 0 | 1 | 000(+) | XXXXXX | 010(+) |
| LW | 100011 | 0 | 1 | 1 | 0 |  | 0 |  | 0 | 1 | 1 | 000(+) | XXXXXX | 010(+) |
| SW | 101011 | X | 1 | 1 | 0 |  | 0 |  | 1 | X | 0 | 000(+) | XXXXXX | 010(+) |
| BEQ | 000100 | X | 1 | 0 | 1 |  | 0 |  | 0 | X | 0 | 001(-) | XXXXXX | 110(-) |
| ORI | 001101 | 0 | 0 | 1 | 0 |  | 0 |  | 0 | 0 | 1 | 011(|) | XXXXXX | 001(|) |
| ANDI | 001100 | 0 | 0 | 1 | 0 |  | 0 |  | 0 | 0 | 1 | 111(&) | XXXXXX | 000(&) |
| J | 000010 | X | X | X | 0 |  | 1 |  | 0 | X | 0 | XXX | XXXXXX | XXX |
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URL: <https://drive.google.com/file/d/1SI7x2Gp_2m3SEkwnXuGt4ns4voYzpGBH/view?usp=sharing>