

MPI HYBRID & ACCELERATOR WORKING GROUP UPDATE

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HYBRID & ACCELERATOR WORKING GROUP



Mission: Improve interoperability of MPI with other programming models

Active topics:

- Continuations proposal <u>#6</u>
- 2. Clarification of thread ordering rules #117
- 3. Integration with accelerator programming models:
 - 1. Accelerator info keys #3
 - 2. Stream/Graph Based MPI Operations #5
 - 3. Accelerator bindings for partitioned communication #4
 - 4. Partitioned communication buffer preparation (shared with Persistence WG) #264

More information: https://github.com/mpiwg-hybrid/hybrid-issues/wiki

COMPLETION CONTINUATIONS

- Treat the completion of an MPI operation as continuation of some activity

 Interoperability with asynchronous and multithreaded programming models
 - Register callbacks that continue the activity upon completion of an MPI operation

```
MPI Request cont req;
MPIX Continue init(&cont req);
                                                             void release event(MPI Status status, void *data)
omp event handle t event;
int value;
                                                               omp event handle t event = (omp event handle t)(uintptr t)data;
                                                               omp fulfill event(event);
  MPI Request req;
  MPI Irecv(&value, ..., &req);
  MPIX Continue(&req, &release event, event, MPI STATUS NULL, cont req);
  // process value
```

"Callback-based completion notification using MPI Continuations," Joseph Schuchart, Christoph Niethammer, José Gracia, George Bosilca, Parallel Computing, 2021. "MPI Detach - Asynchronous Local Completion," Joachim Protze, Marc-André Hermanns, Ali Demiralp, Matthias S. Müller, Torsten Kuhlen. EuroMPI '20.

STREAM TRIGGERED NEIGHBOR EXCHANGE

Simple Ring Exchange Using a CUDA Stream

```
MPI Request send req, recv req;
MPI Status sstatus, rstatus;
for (i = 0; i < NITER; i++) {
 if (i > 0) {
   MPI Wait enqueue(recv req, &rstatus, MPI CUDA STREAM, stream);
   MPI Wait enqueue(send req, &sstatus, MPI CUDA STREAM, stream);
 kernel<<<..., stream>>>(send buf, recv buf, ...);
 if (i < NITER - 1) {
   MPI_Irecv_enqueue(&recv_buf, ..., &recv_req, MPI_CUDA_STREAM, stream);
   MPI Isend enqueue(&send buf, ..., &send req, MPI CUDA STREAM, stream);
cudaStreamSynchronize(stream);
```

stream kernel Isend Irecv Wait Wait kernel Isend Irecv

ACCELERATOR AWARE INFO

Improve Interoperability With Accelerator Models

Challenges for users of accelerator-aware MPI today:

- Check whether accelerator awareness is supported
- Discover whether MPI is using a performance path for accelerator memory (e.g. system has the right config to do direct communication with the accelerator)
- Reduce overhead from hybrid programming

Possible solution:

- mpi_hybrid_model = { "CUDA", "HIP", "SYCL", "OpenMP", etc. }
- mpi_accelerator_zero_copy = { "true", "false" }
- mpi_assert_accelerator = { device_ids }
- mpi_assert_memory_kind = { "host", "device" }



CUDA BINDINGS FOR MPI PARTITIONED APIS

```
int MPI Psend init(const void *buf, int partitions, MPI Count count,
                    MPI Datatype datatype, int dest, int tag, MPI Comm comm, MPI Info info,
                    MPI Request *request)
int MPI Precv init(void *buf, int partitions, MPI Count count,
                    MPI Datatype datatype, int source, int tag, MPI Comm comm, MPI Info info,
                    MPI Request *request)
int MPI [start,wait][ all](...)
                                                                                            Keep host only
                                                                                        Add device bindings
  device int MPI Pready(int partition, MPI Request request)
  device int MPI Pready range(int partition low, int partition high, MPI Request request)
  device int MPI Pready list(int length, const int array of partitions[], MPI Request request)
 device int MPI Parrived(MPI Request request, int partition, int *flag)
```

KERNEL TRIGGERED COMMUNICATION USAGE

Partitioned Neighbor Exchange

```
Host Code
                                                            Device Code
MPI_Request req[2];
MPI Psend_init(..., &req[0]);
MPI Precv init(..., &req[1]);
                                              device
while (...) {
                                            void MPI Pready(int idx, MPI Request req);
 MPI Startall(2, req);
 MPI Pbuf prepare all(2, req);
                                              global__ kernel(..., MPI_Request *req) {
                                               int i = my partition(...);
  kernel<<<..., s>>>(..., req);
                                              // Compute and fill partition i
  cudaStreamSynchronize(s);
                                              // then mark i as ready
 MPI Waitall(2, req);
                                              MPI Pready(i, req[0]);
MPI Request free(&req[0]);
MPI Request free(&req[1]);
```

KERNEL & STREAM TRIGGERED COMMUNICATION USAGE

Partitioned Neighbor Exchange

Host Code

```
MPI_Request req[2];
MPI_Psend_init(..., &req[0]);
MPI_Precv_init(..., &req[1]);
while (...) {
  MPI Startall enqueue(2, req, ...);
  MPI_Pbuf_prepare_all_enqueue(2, req, ...);
  kernel<<<..., s>>>(..., req);
 cudaStreamSynchronize(s);
  MPI_Waitall_enqueue(2, req, ...);_
MPI Request free(&req[0]);
MPI Request free(&req[1]);
```

Device Code

```
__device__
void MPI_Pready(int idx, MPI_Request req);

__global__ kernel(..., MPI_Request *req) {
  int i = my_partition(...);
  // Compute and fill partition i
  // then mark i as ready
  MPI_Pready(i, req[0]);
}
```

Moving to stream eliminates overhead from stream synchronization



Thank you!

Wednesdays 10-11am US Eastern Time

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