# #349: Dynamic Window Displacements

RMA WG Plenary Presentation June 6, 2013

https://svn.mpi-forum.org/trac/mpi-forum-web/ticket/349

## What's the problem?

- Dynamic windows require arithmetic on MPI\_Aint values that represent addresses
  - Indexing into an array
  - Accessing a field in a struct
- Arithmetic on MPI\_Aints that represent addresses cannot be done portably
  - MPI\_Aints are signed integers, but addresses ain't (are not)
  - Value is relative to MPI\_BOTTOM, which varies across processes and language bindings (e.g., F\*\*\*\*N)
    - MPI\_BOTTOM is address of a variable in a common block
  - Arithmetic can overflow!
  - Arithmetic overflow behavior is not specified

## Dynamic Window Displacement Arithmetic Example

#### Rank p

#### Rank q

```
MPI_Aint array_disp;
...
MPI_Bcast(&array_disp,p);
...
MPI_Get(array_disp +
    i*sizeof(double), ...);
```

This can overflow!

## **Proposed Solution**

 Add a function that can "safely" add an address and a displacement

- Implementation:
  - Must correctly handle overflows
  - Just add them, if system is two's complement

## Proposed Text (draft)

MPI\_Aint MPI\_Aint\_add(MPI\_Aint base, MPI\_Aint disp)

This function produces a new MPI\_Aint value that is equivalent to the sum of the base and disp arguments, where base represents an address and disp represents a signed integer displacement. The value of base may be relative to a non-zero value of MPI\_BOTTOM that is unknown at the process performing the call to MPI\_Aint\_add. The addition is performed in a manner that results in the correct MPI\_Aint representation of the output address, as if the process that originally produced base had called:

MPI\_Get\_address((char \*) base + disp, &out\_addr)

### Straw Polls

- How would you vote for this proposal?
  - (14) yes, (0) no, (4) abstain

- Can we handle this as an erratum?
  - (9) yes, (0) no, (10) abstain