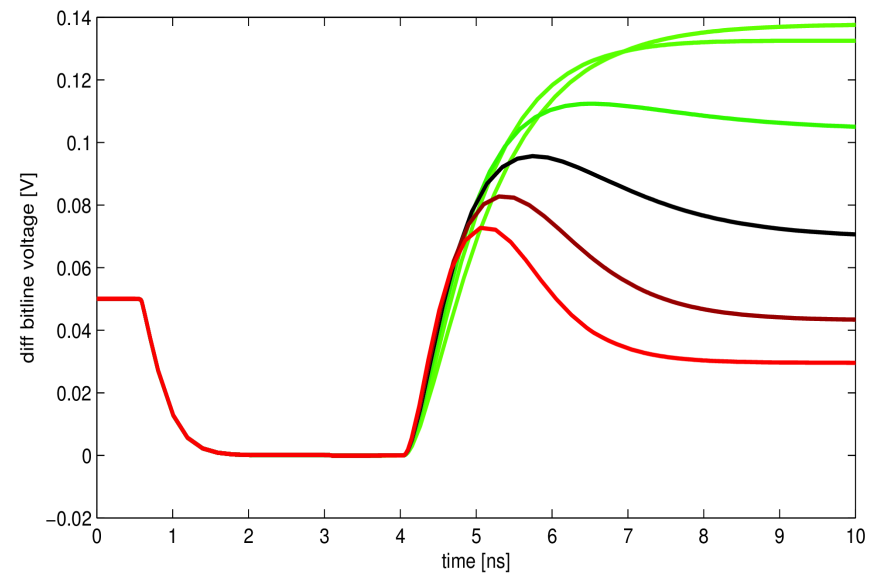
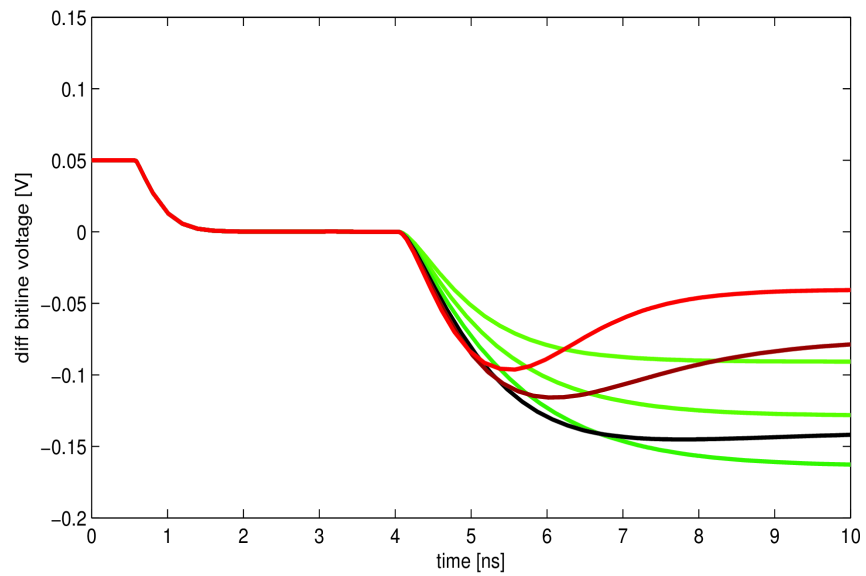
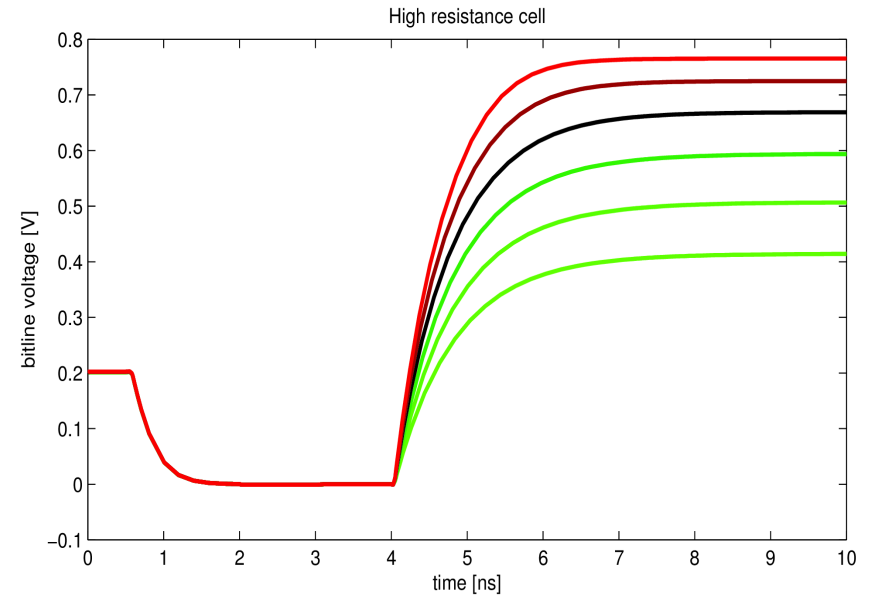
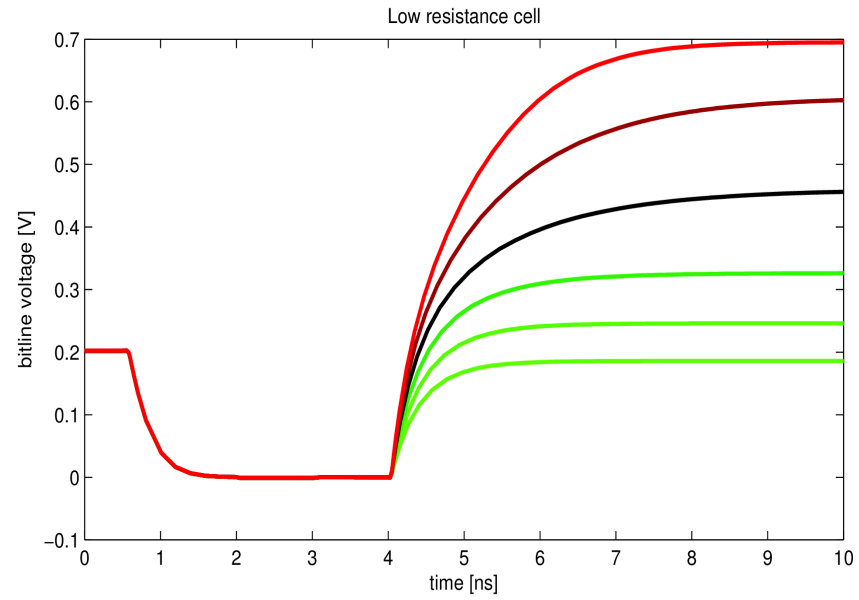
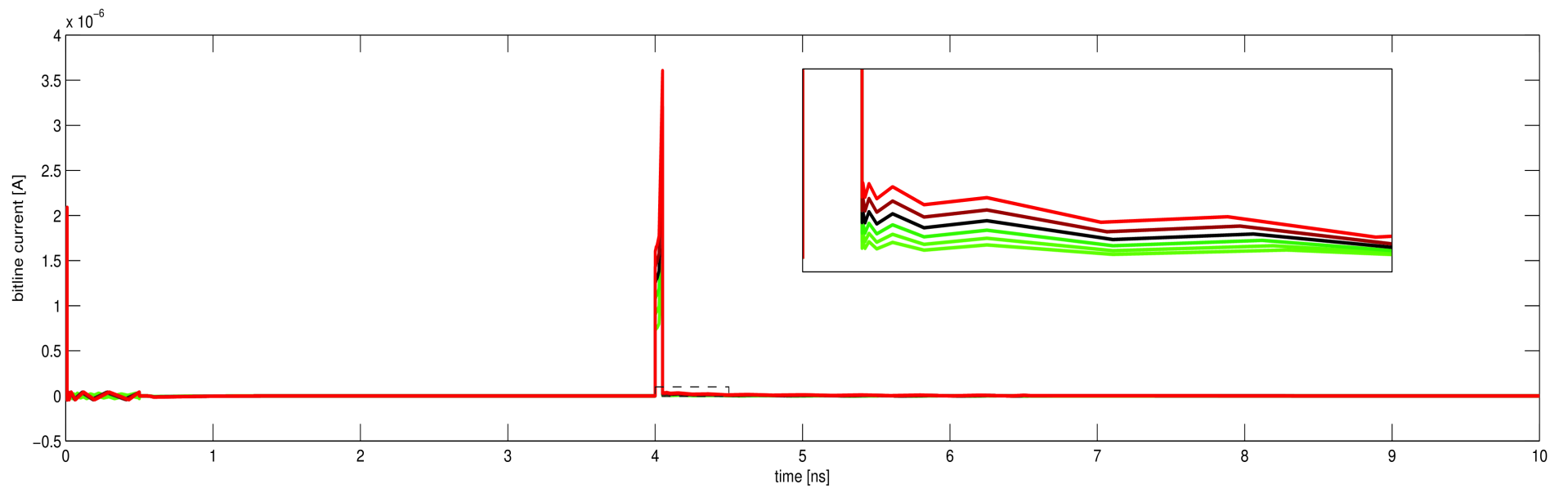
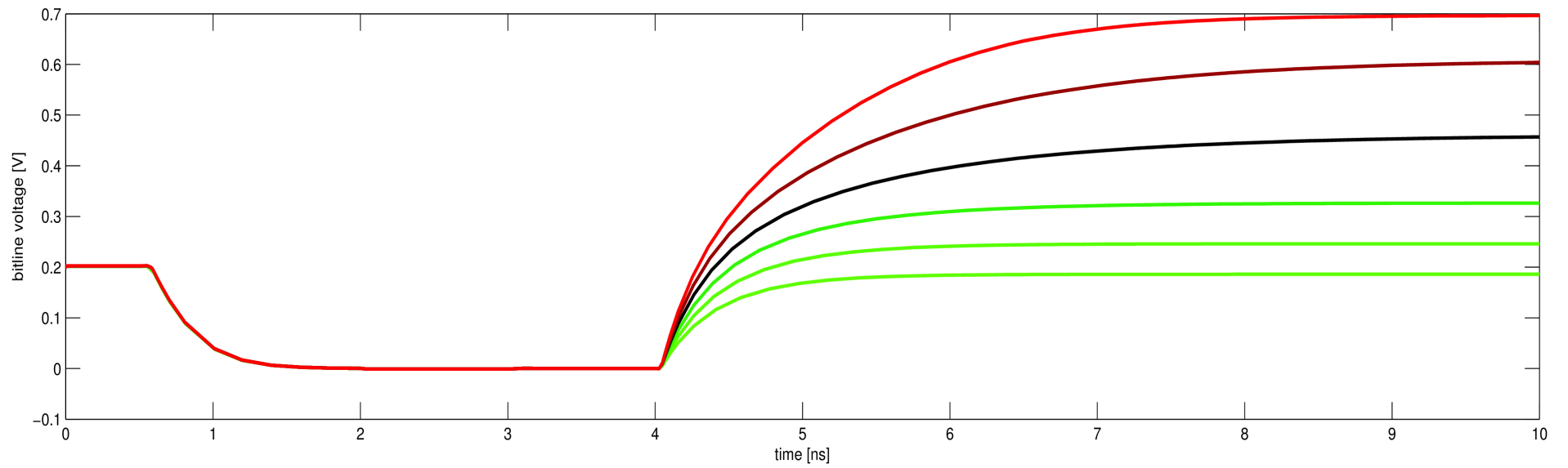


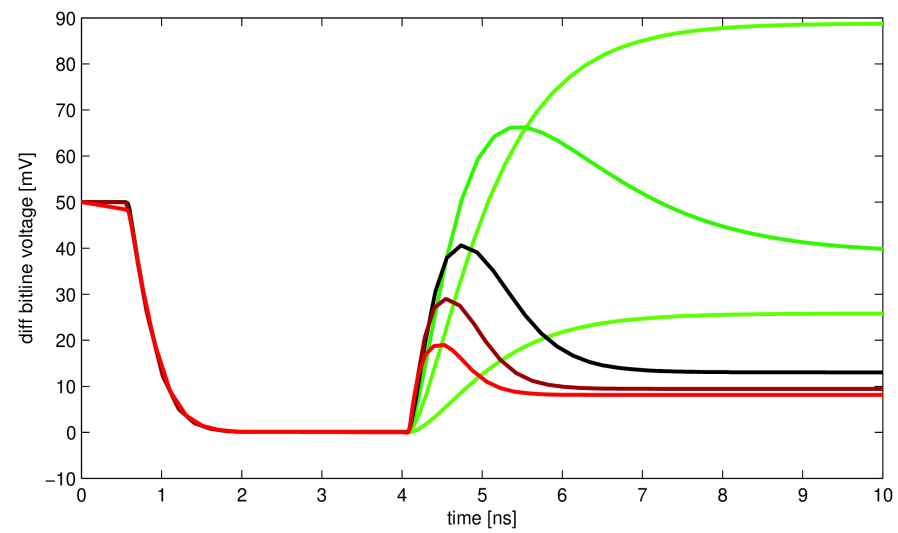
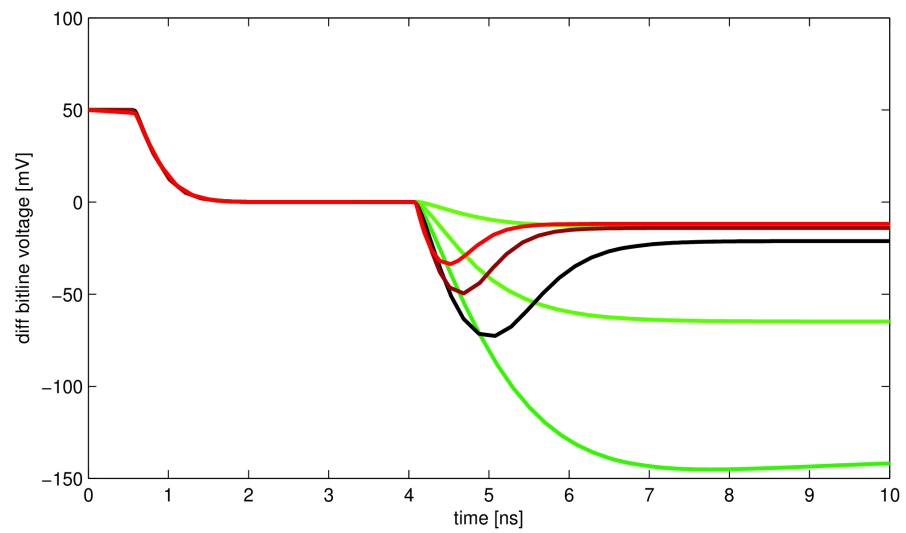
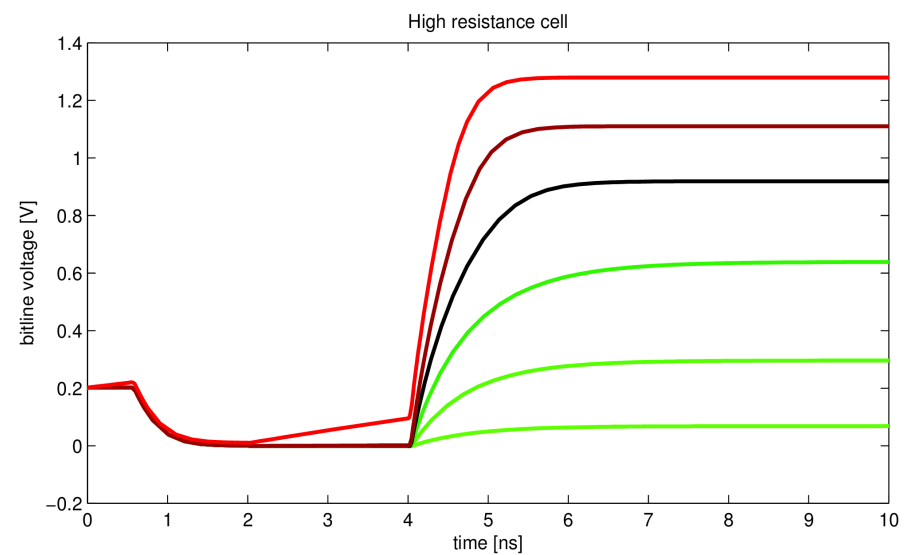
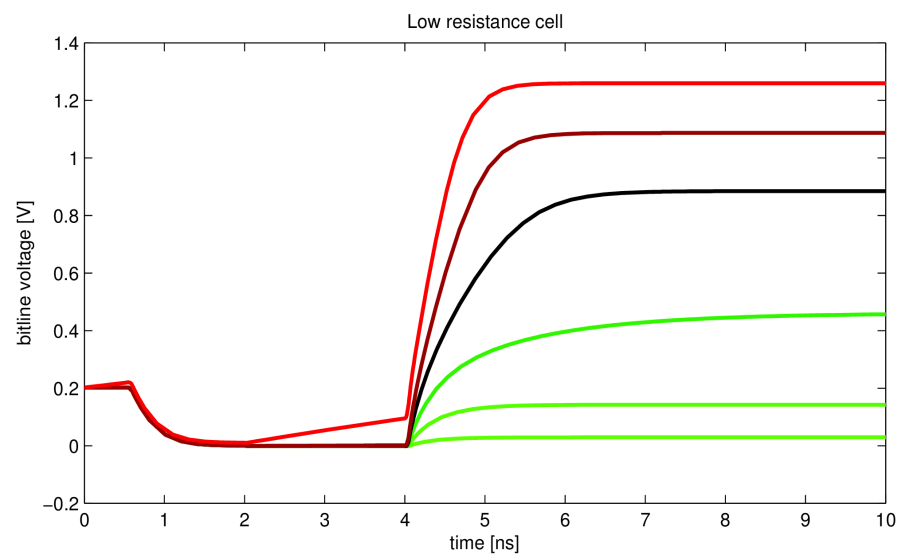
Effect of sizing Pmos switch



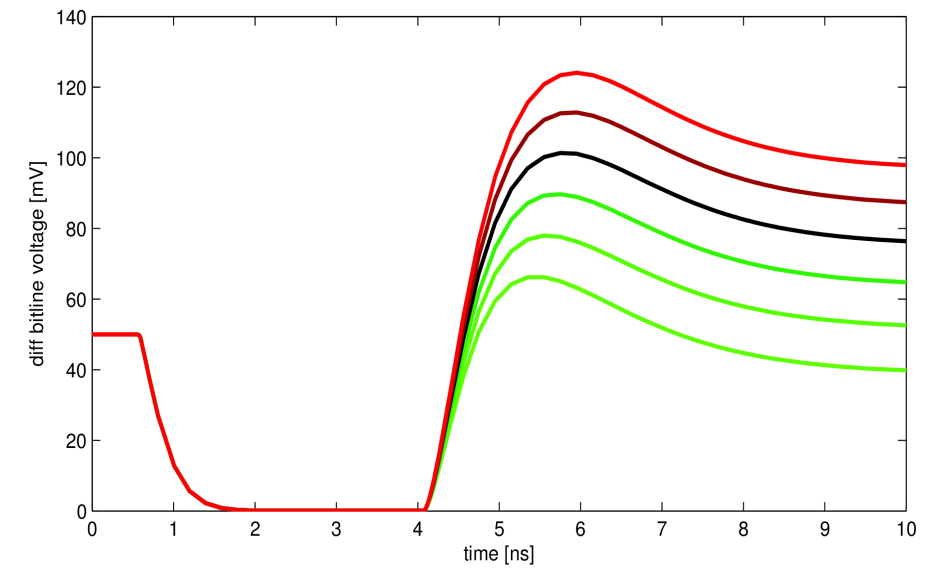
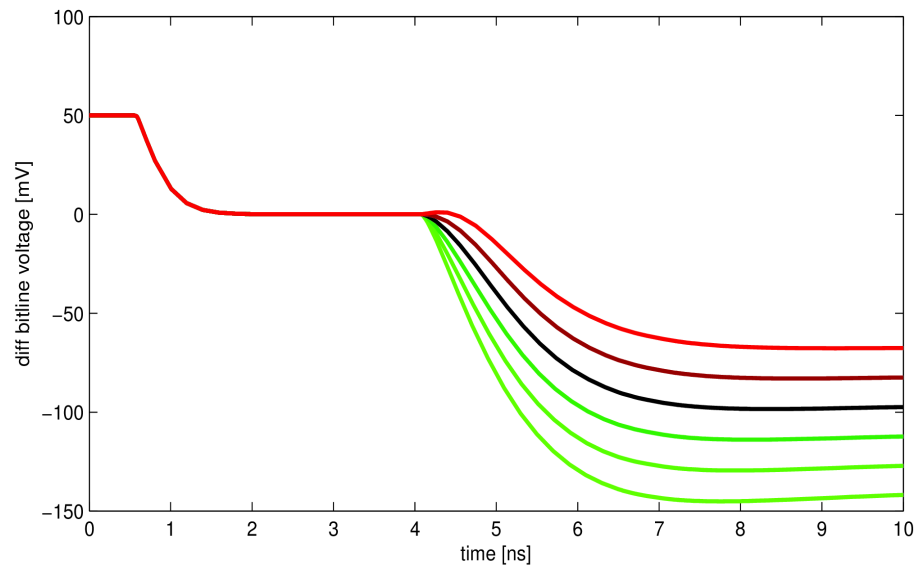
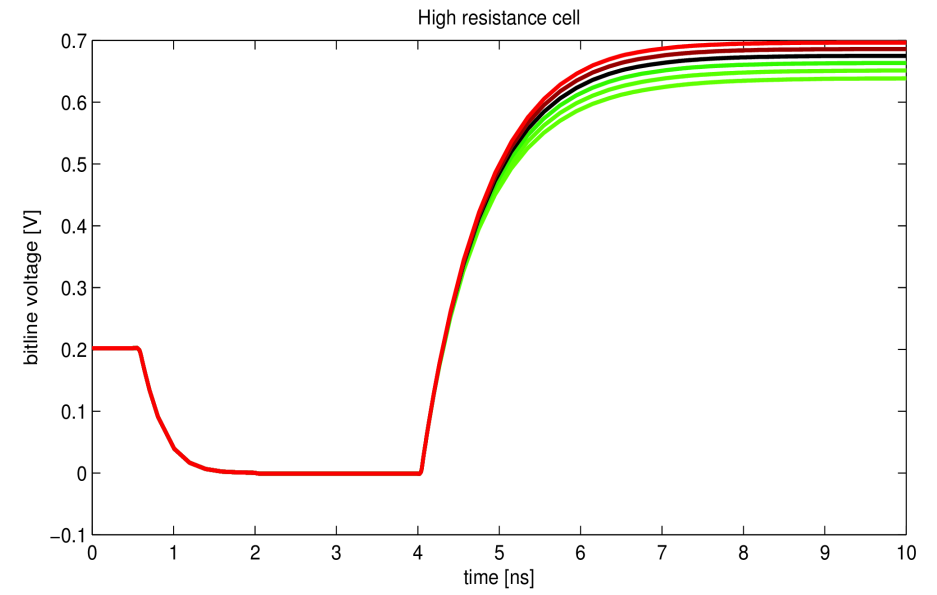
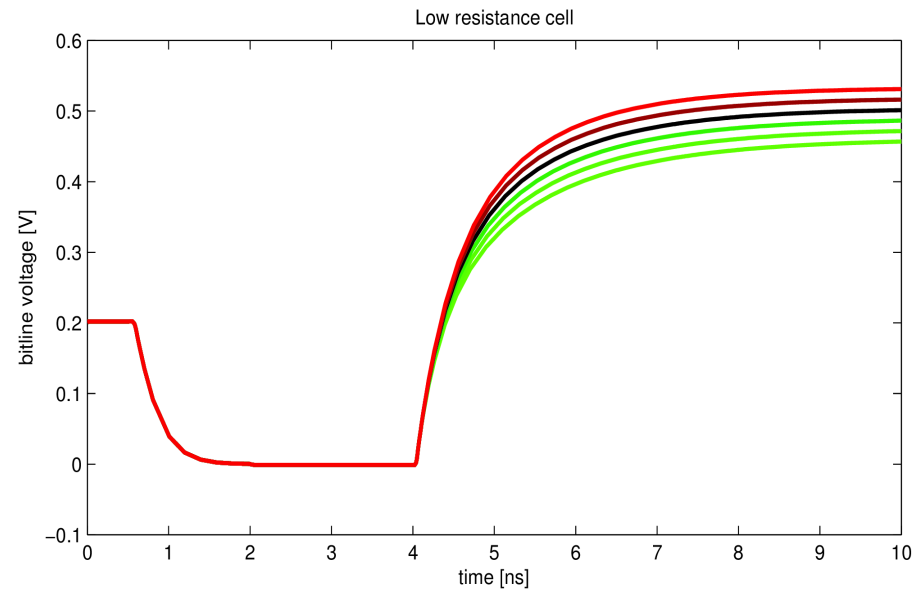
Effect of sizing Pmos switch (2)



Effect of Vload



Effect of sizing mem resistance



Effect of Cloud

