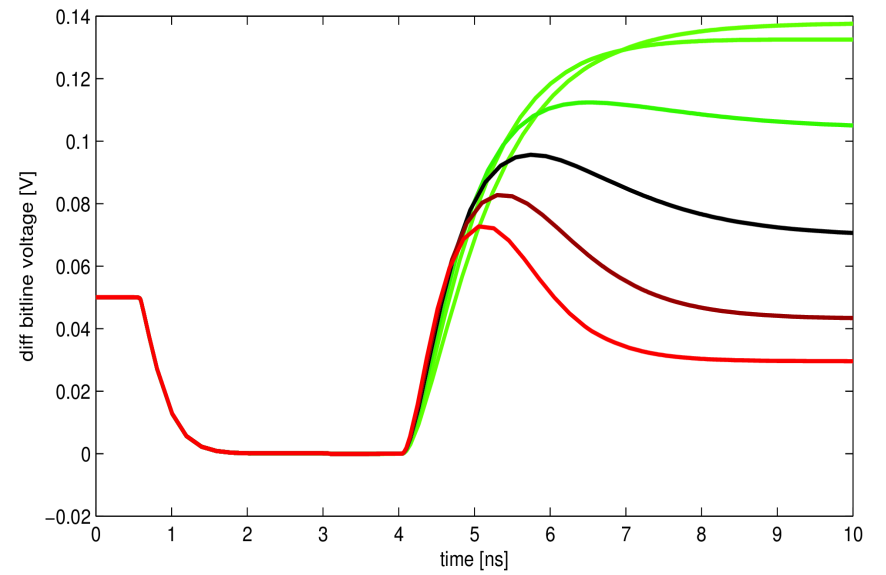
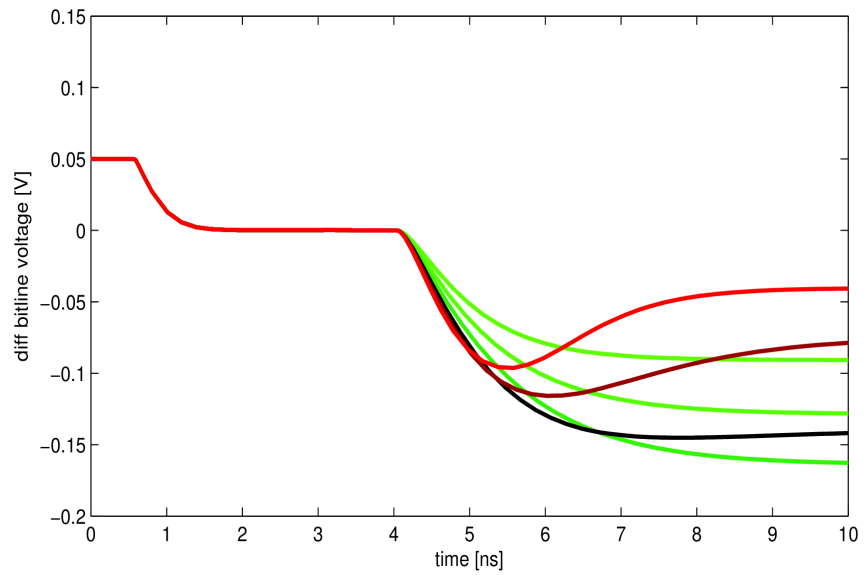
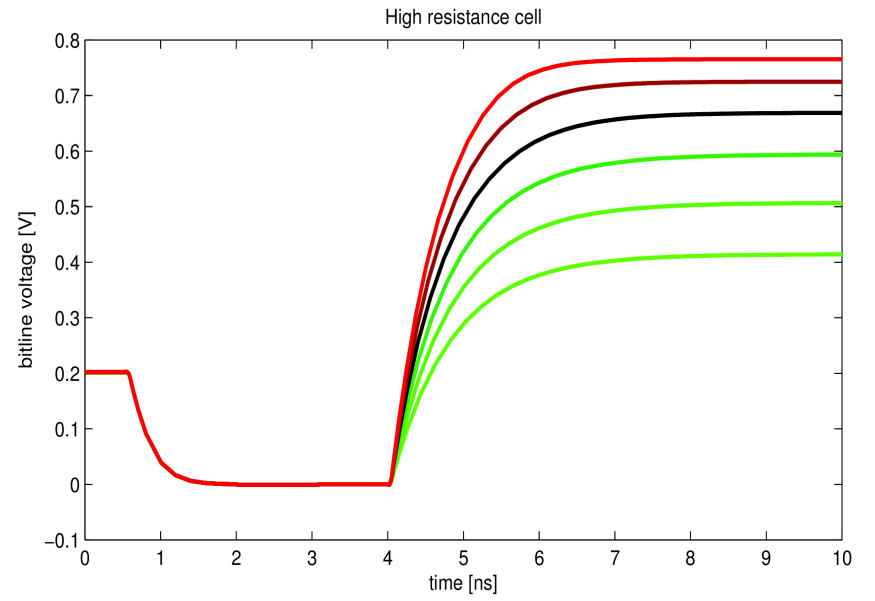
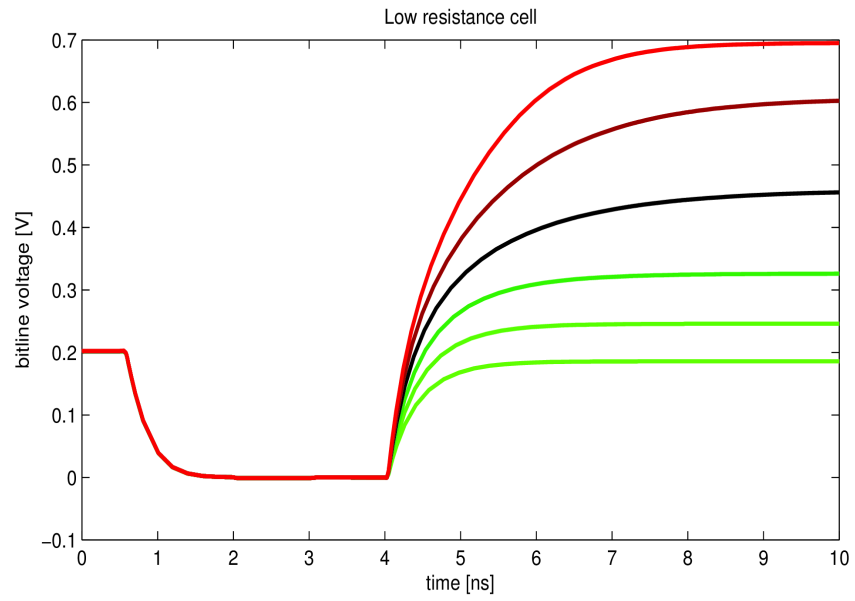
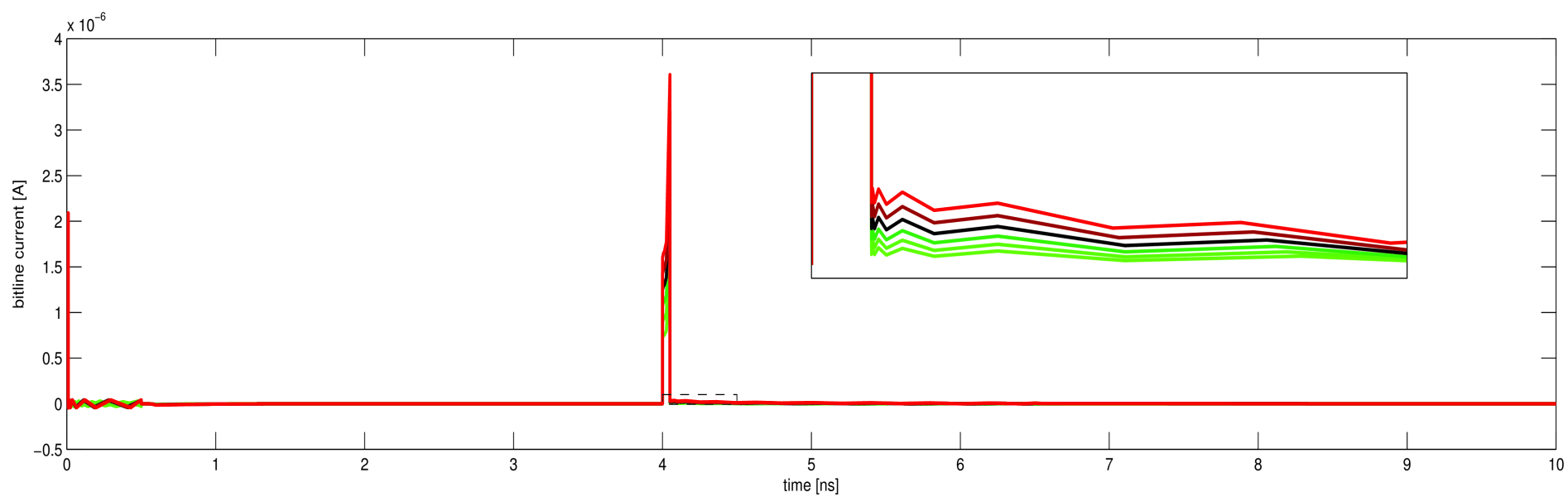
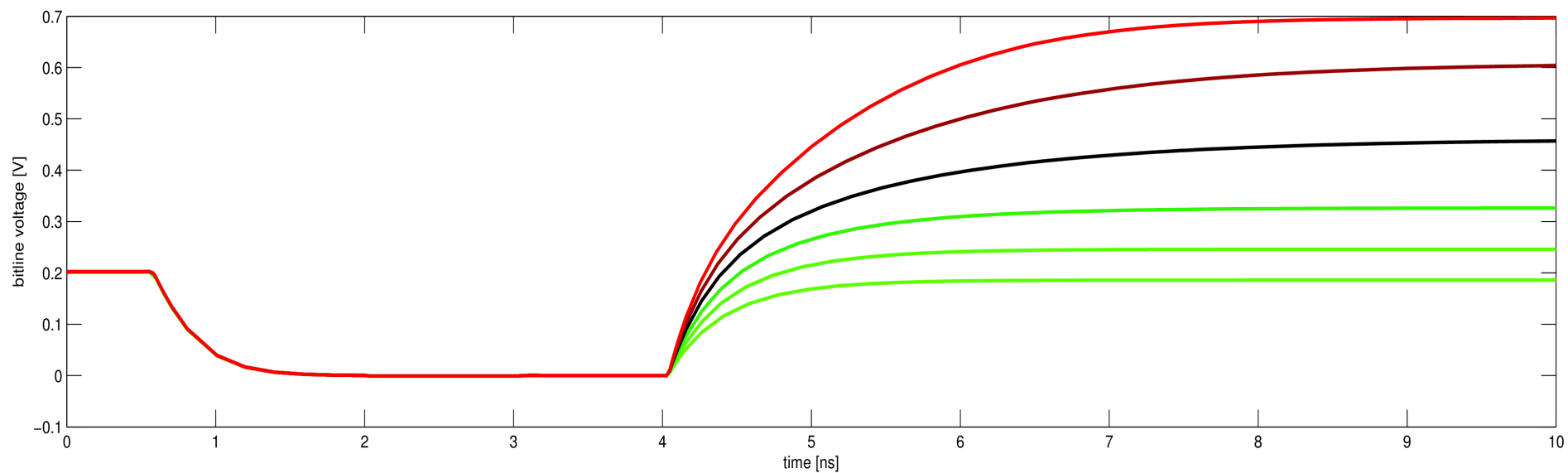


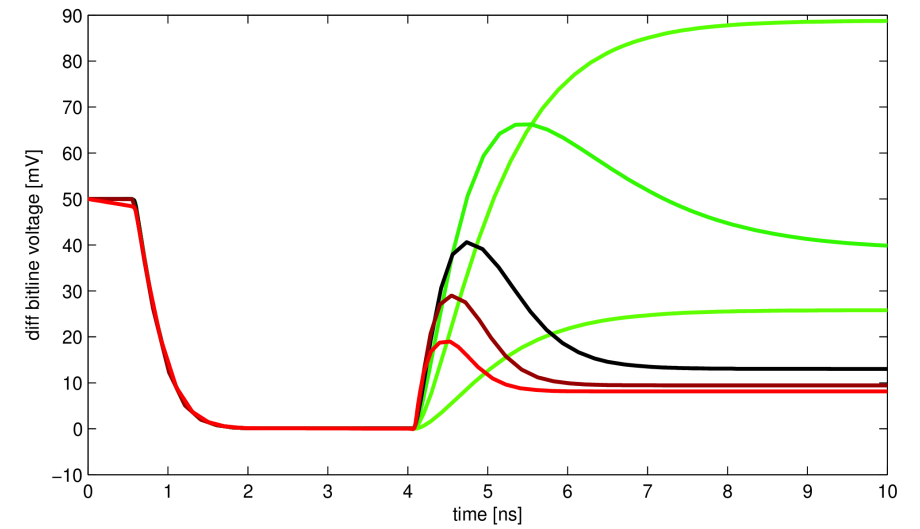
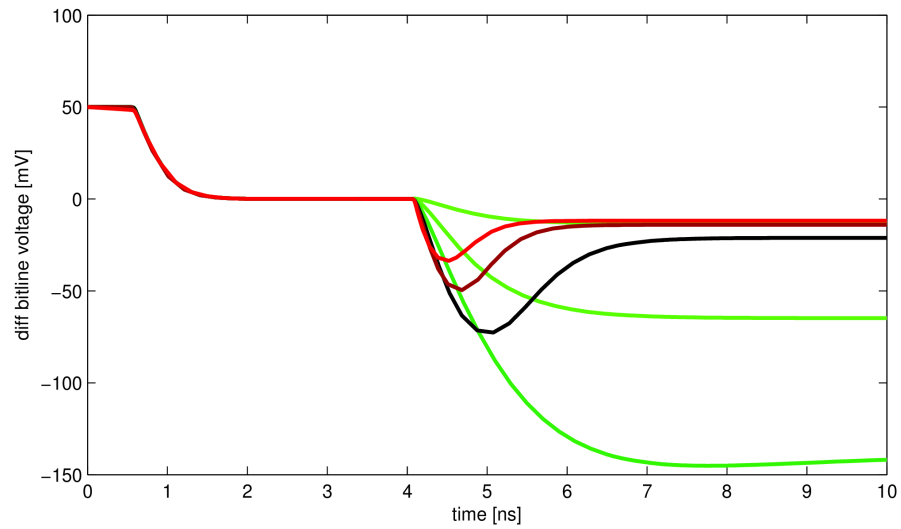
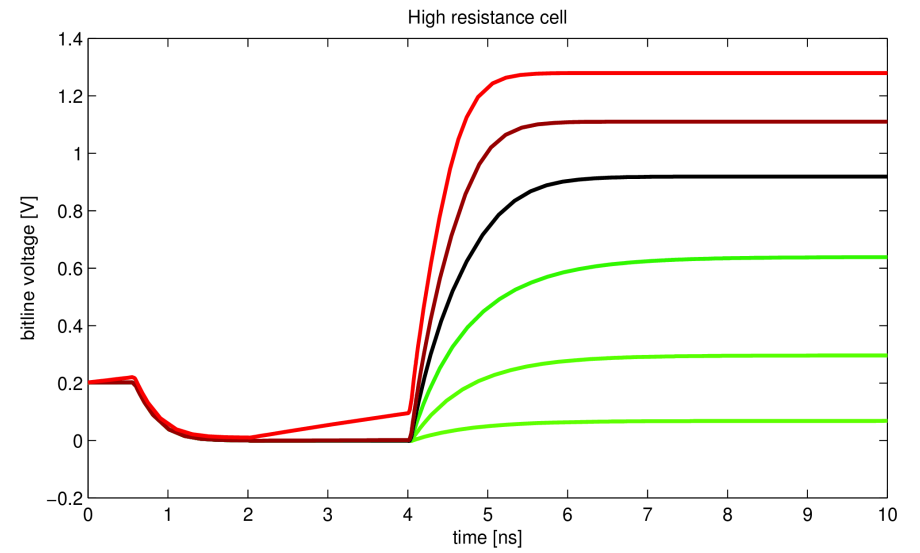
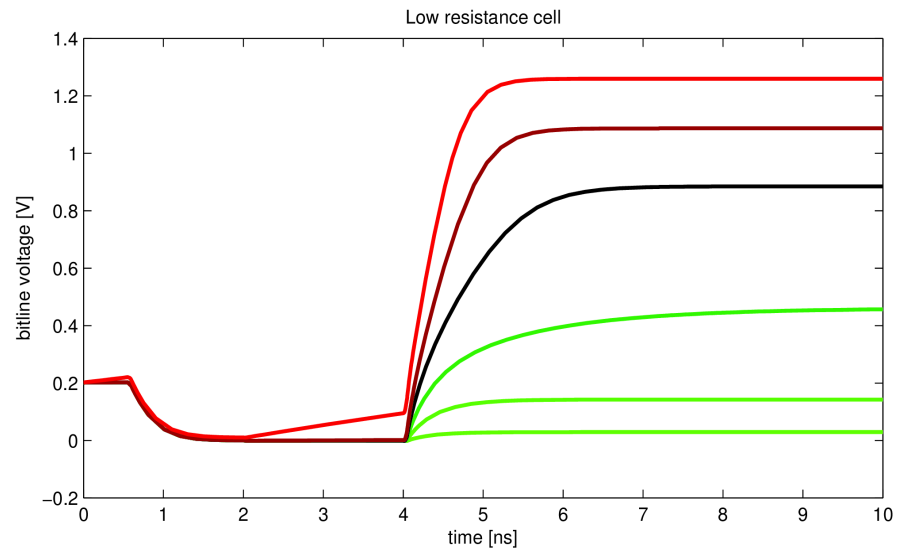
# Effect of sizing Pmos switch



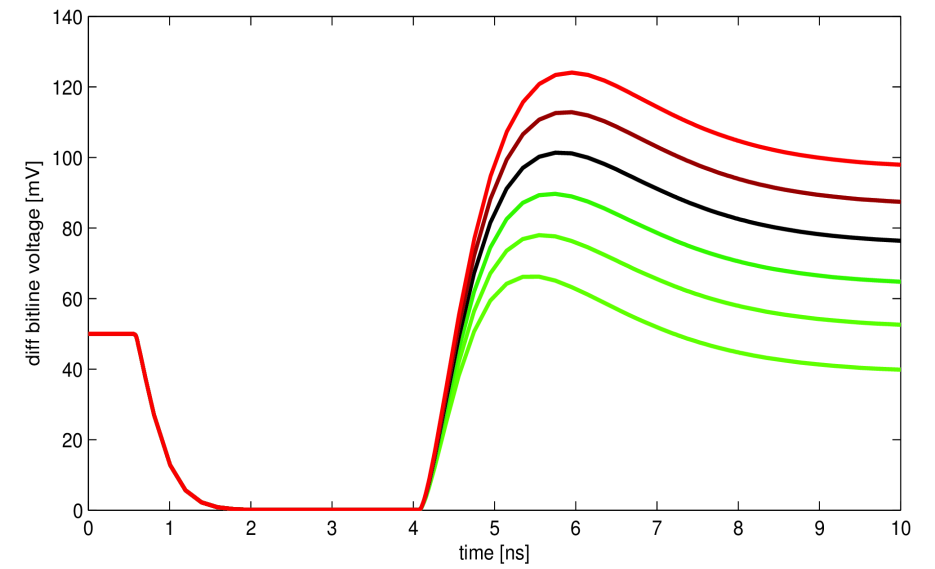
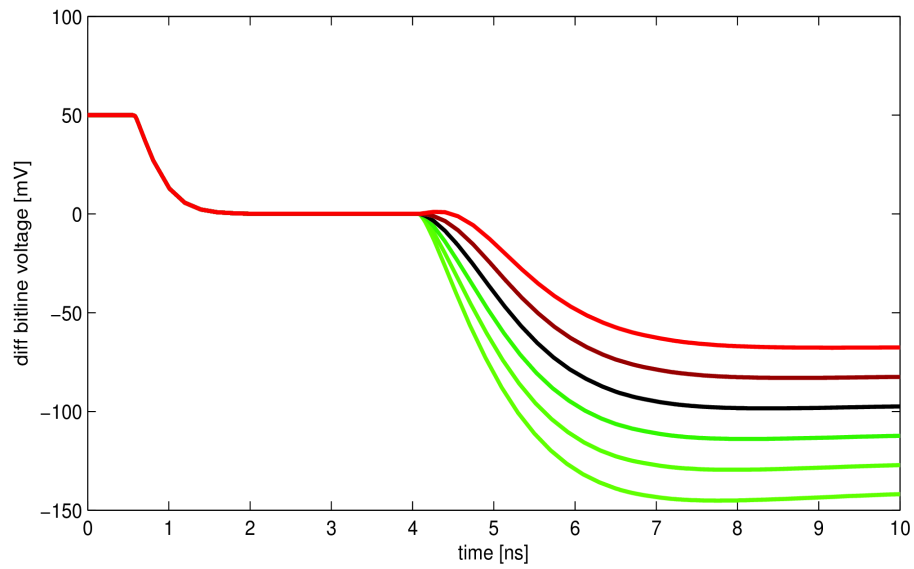
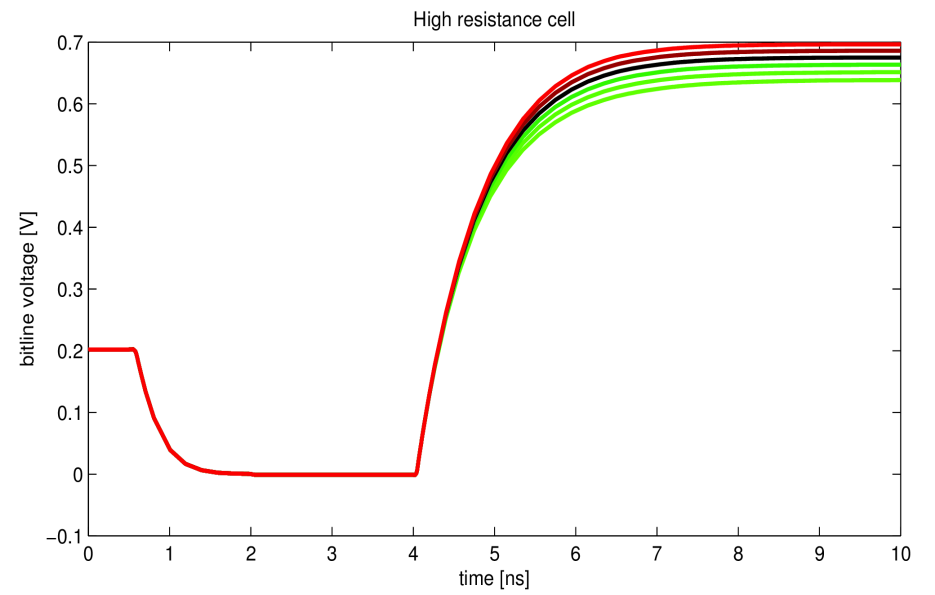
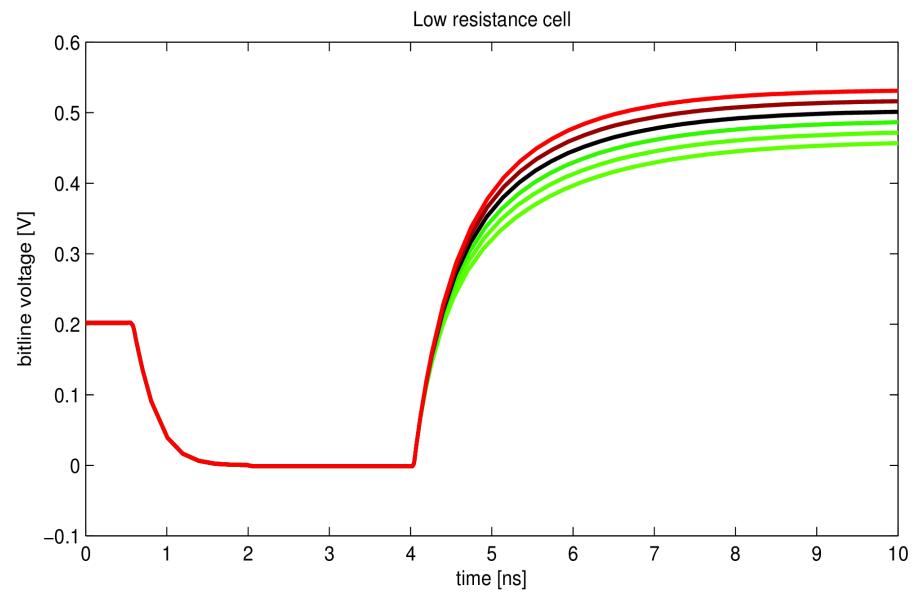
Effect of sizing Pmos switch (2)



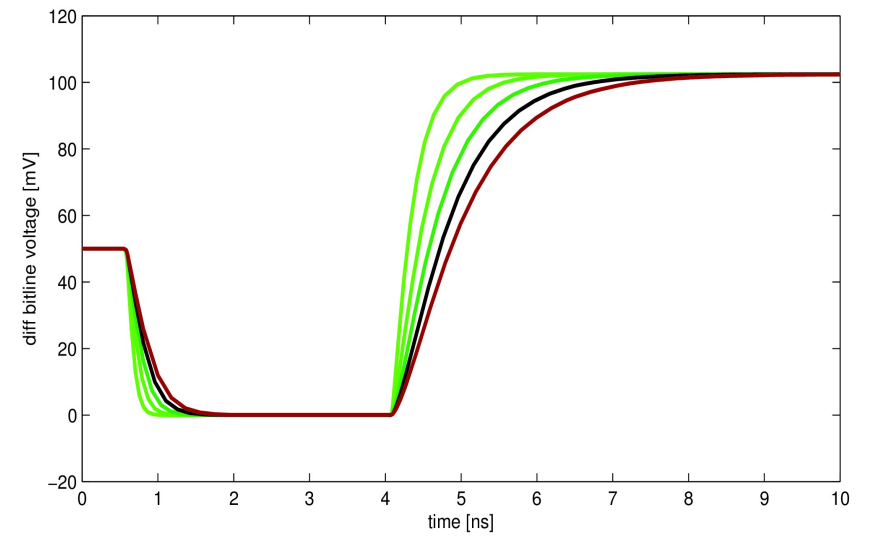
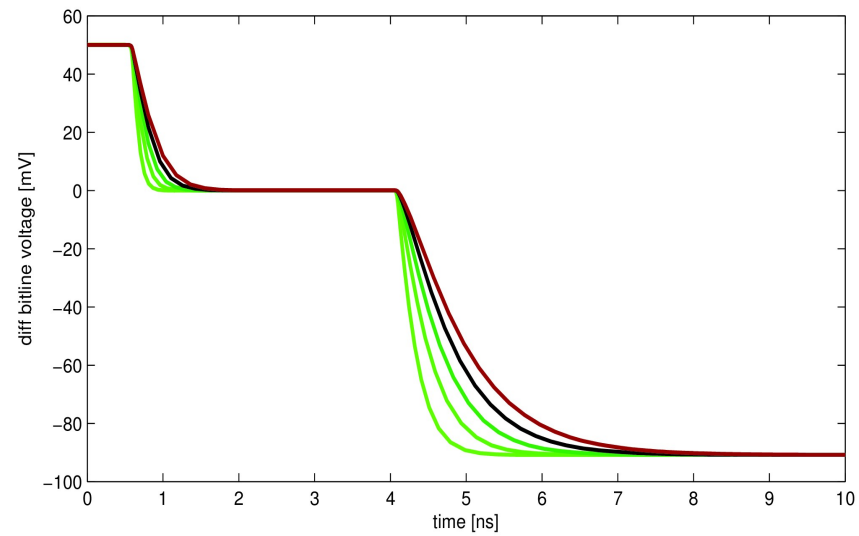
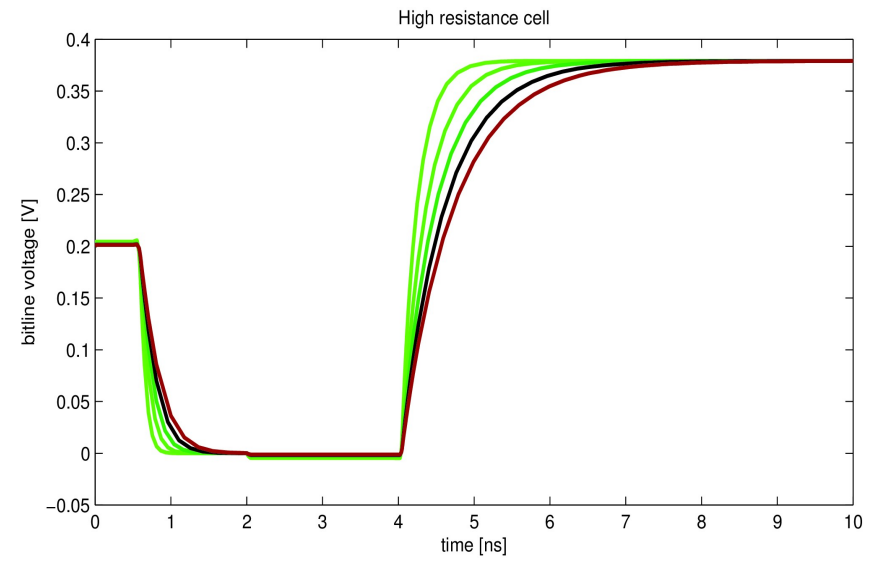
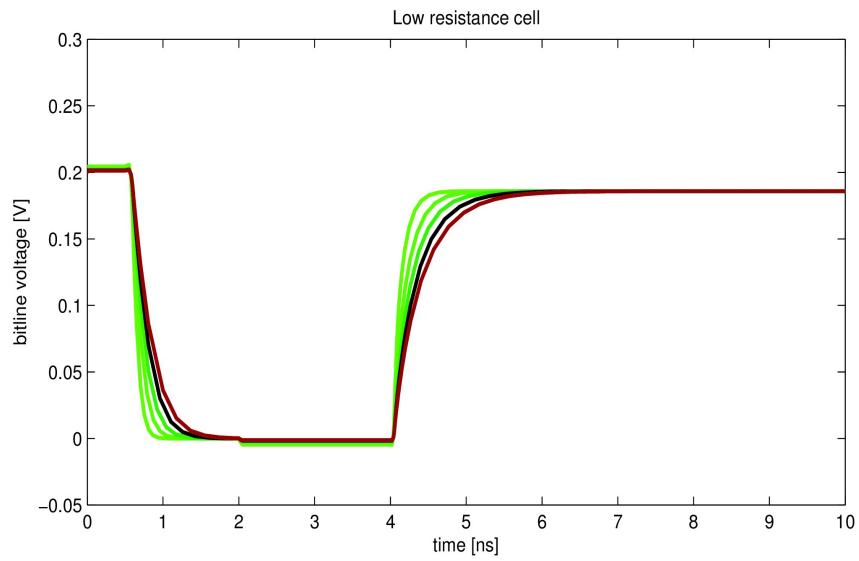
## Effect of Vload

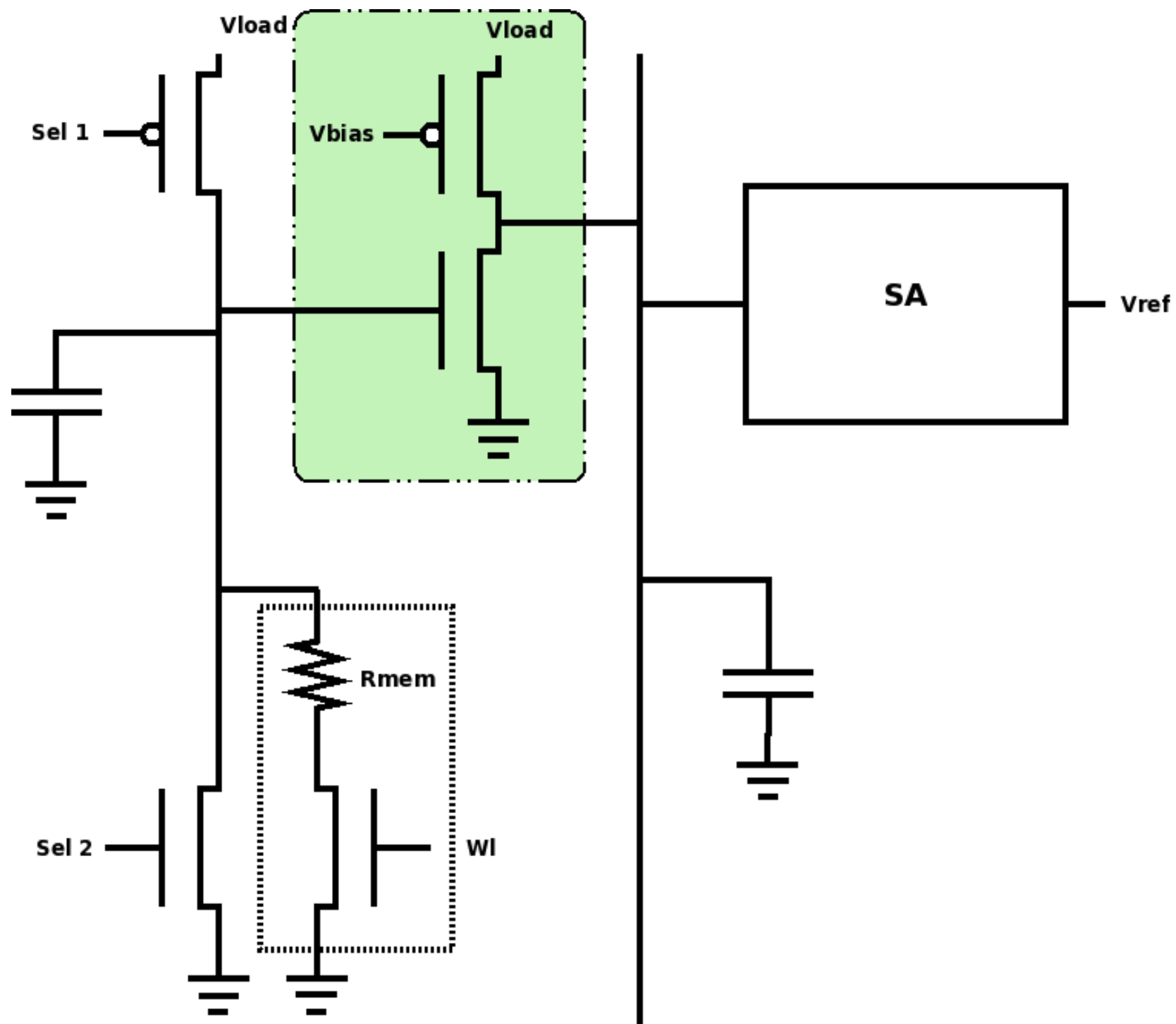


## Effect of sizing mem resistance

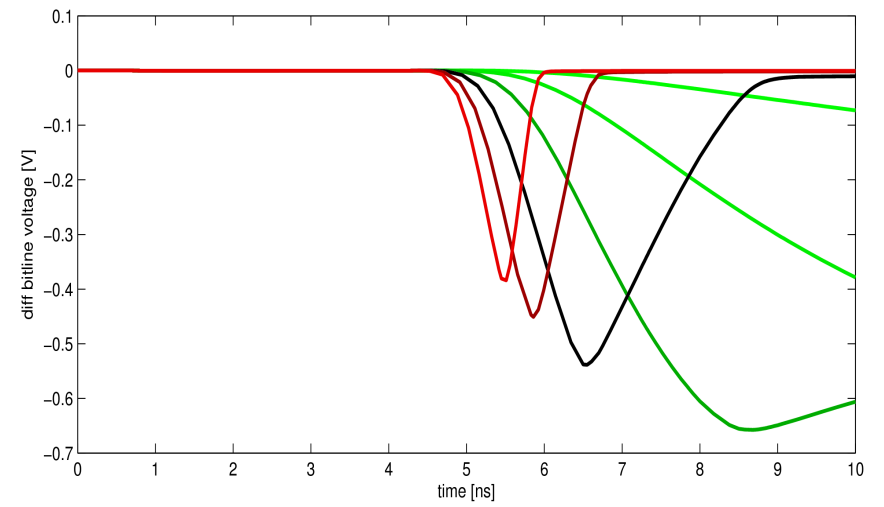
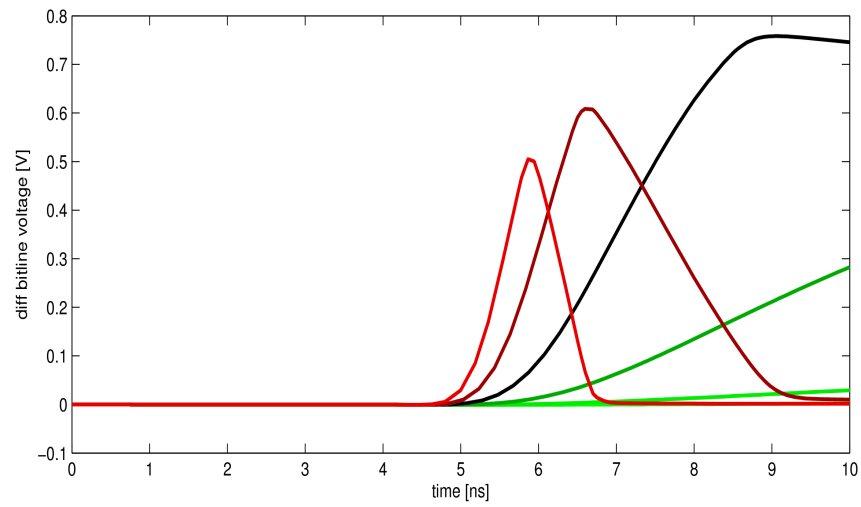
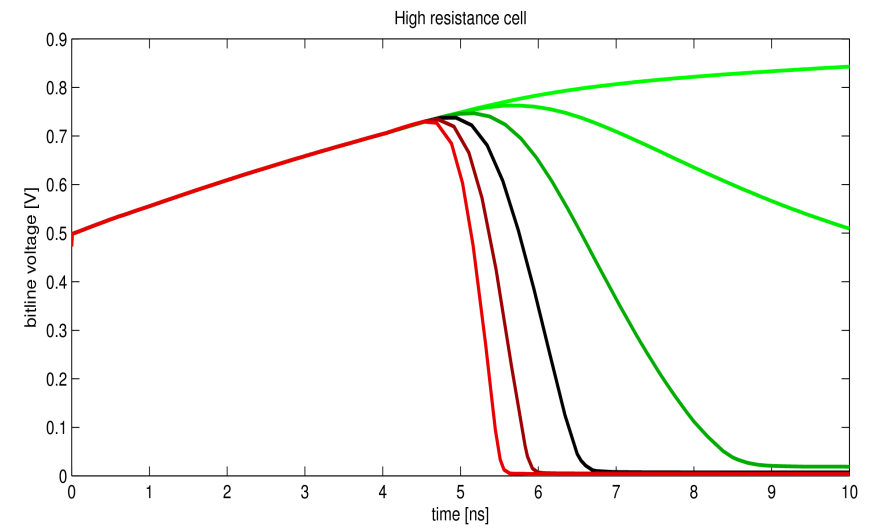
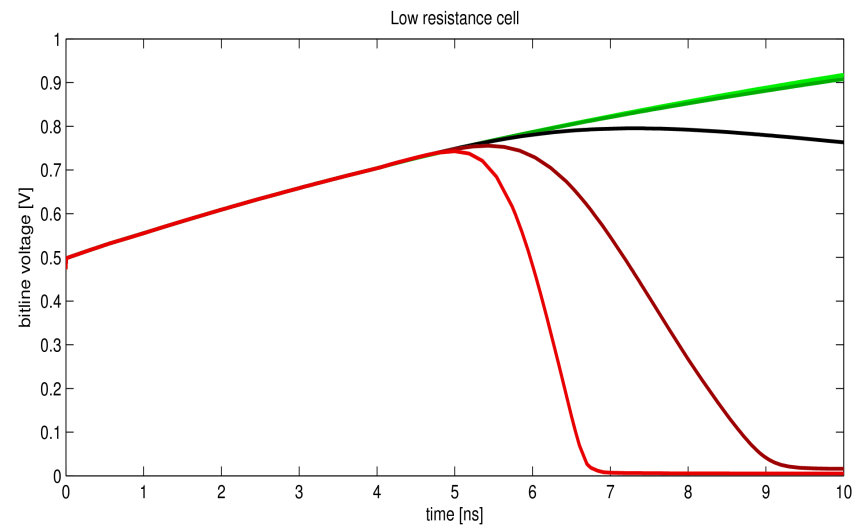


## Effect of Cload





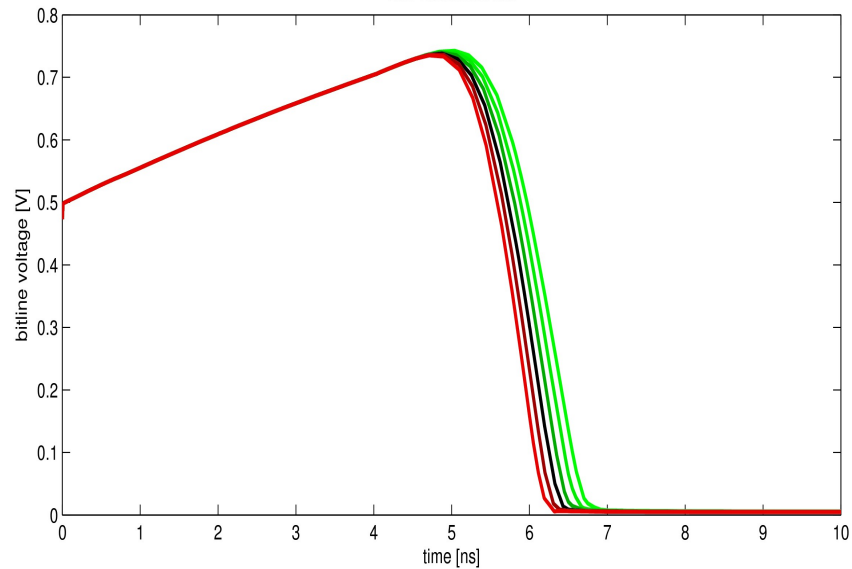
# Effect of sizing Pmos switch (1)





# Effect of sizing mem resistance

Low resistance cell



High resistance cell

