





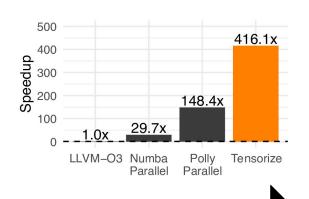


Tensorize: Fast Synthesis of Tensor Programs from Legacy Code using Symbolic Tracing, Sketching and Solving

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Lifting to Tensor DSLs gives great Performance

Loop-level Program









NumPy API

```
mean = np.sum(data, axis=0) / data.shape[0]
data = data - mean

cov = np.dot(data.T, data)
cov = cov / (data.shape[0] - 1.)
```

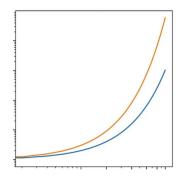
MLIR StableHLO

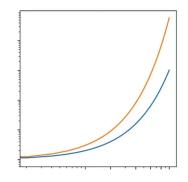


```
%0 = stablehlo.constant dense<0.000000e+00>
%1 = stablehlo.reduce(%arg1 init: %0) across 0
: (tensor<1400x1200xf32>, tensor<f32>)
```

Challenges in Existing Approaches





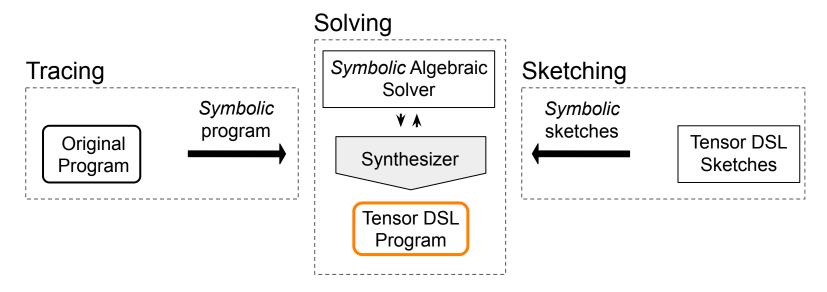


Pattern Matching
Robust X

Synthesis
Robust Scalable



Tensorize – Decomposing the Synthesis Problem



- Tracing: Captures program semantics as symbolic equations
- **Sketching:** Generates sketches from Tensor DSL grammar
- **Solving:** Recursively simplifies the symbolic trace

Symbolic Tracing



$$\left(egin{array}{c} cov_{0,0} & \cdots \\ cov_{1,0} & \cdots \\ \vdots & \ddots \\ cov_{m-1,0} & \cdots \end{array}
ight)=$$

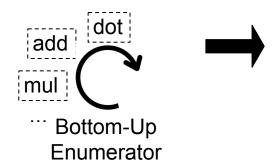
$$\frac{(A_{0,0}-B_{0})(A_{0,0}-B_{0})+(A_{1,0}-B_{0})(A_{1,0}-B_{0})+\cdots+(A_{n-1,0})}{C} \\
\underline{(A_{0,1}-B_{1})(A_{0,0}-B_{0})+(A_{1,1}-B_{1})(A_{1,0}-B_{0})+\cdots+(A_{n-1,0})}_{C} \\
\vdots \\
\underline{(A_{0,m-1}-B_{m-1})(A_{0,0}-B_{0})+(A_{1,m-1}-B_{m-1})(A_{1,0}-B_{0})+\cdots+(A_{m-1,0})}_{C}$$

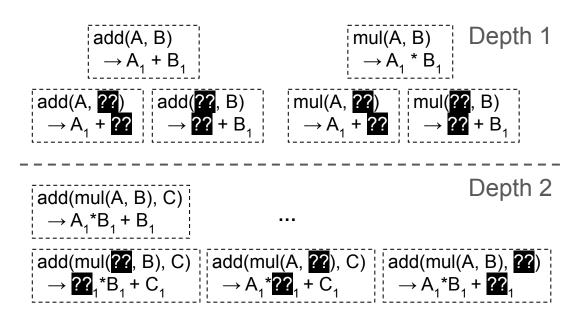
Symbolic Sketching

Tensor DSL Grammars

NumPy, StableHLO,

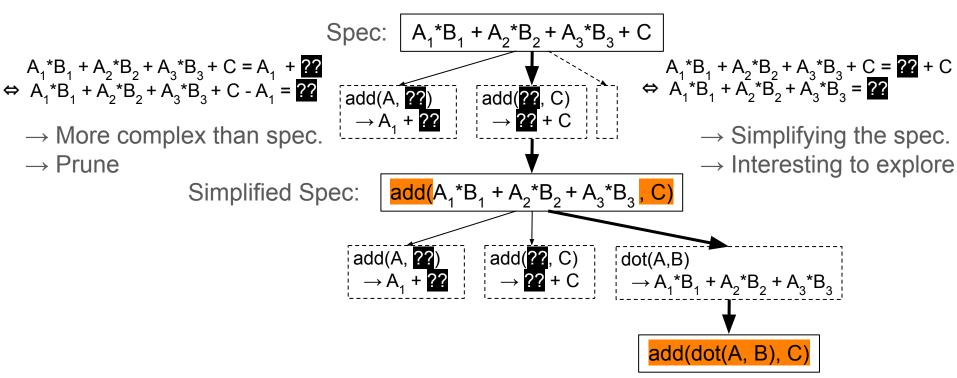
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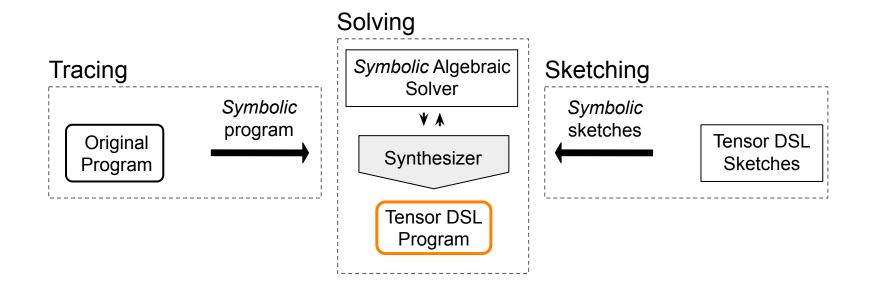


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Synthesis through Symbolic Simplification



Putting it Back Together



Comprehensive Benchmarks

Benchmarks

Suite	Workload Bench	ımarks
blas [16]	Linear Algebra	3
blend [6]	Image Processing	12
darknet [45]	Machine Learning	14
dsp [29]	Image Processing	15
dspstone [58]	Signal Processing	5
llama [10]	Machine Learning	11
makespeare [46]	Linear Algebra	1
mathfu [11]	Math	12
polybench [42]	Data Mining, Lin. Alg.	15
simpl_array [50]	Array Programming	5
utdsp [47]	Signal Processing	6
TOTAL		99

- Superset of benchmarks from related works
- Diverse complexity
 - Number of Operations
 - Loop Depth
 - Operation Types
- Covering multiple application domains

State-of-the-art Methods

Pattern Matching

MultiLevel Tactics (CGO'21)

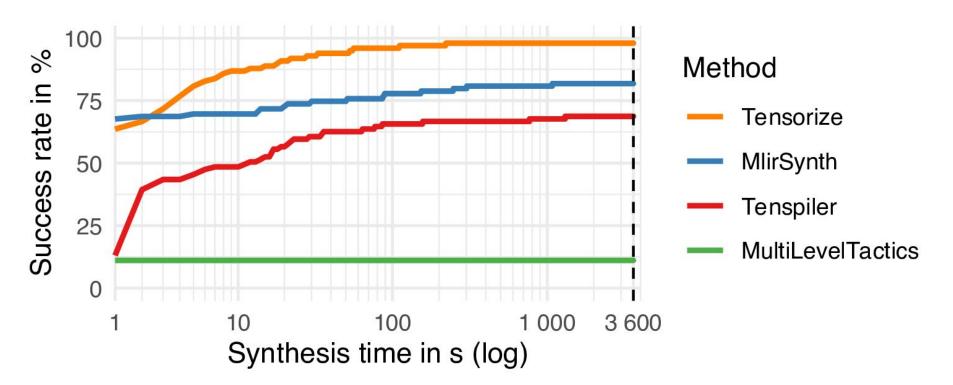
Bottom-Up Program Synthesis

MlirSynth (PACT'23)

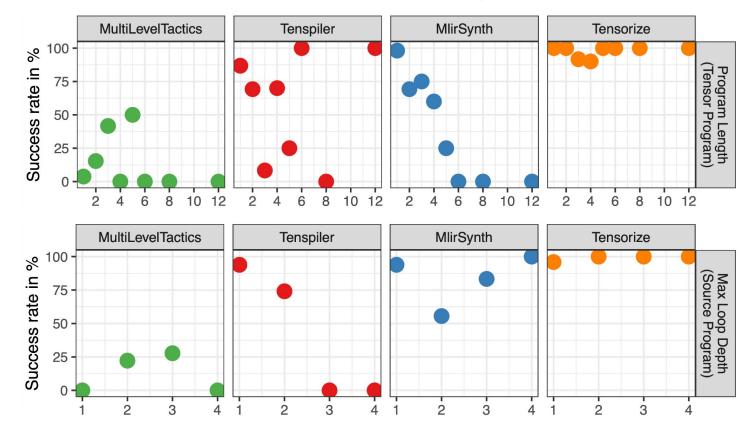
Verified Lifting

Tenspiler (ECOOP'24)

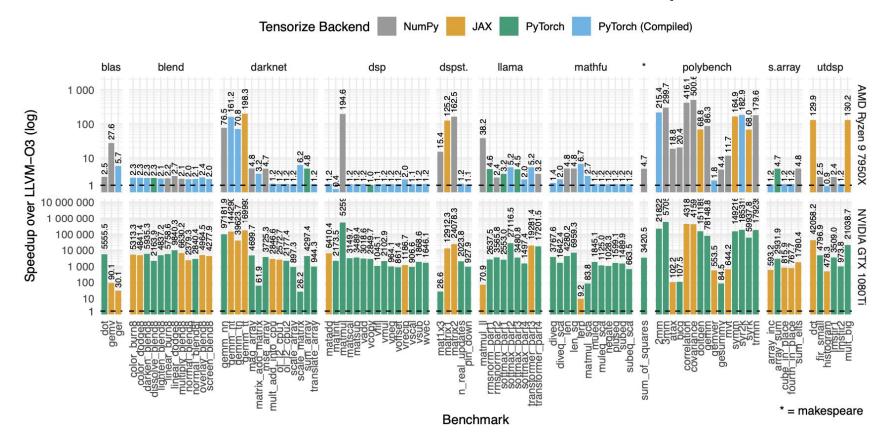
Tensorize Lifts More Benchmarks Faster



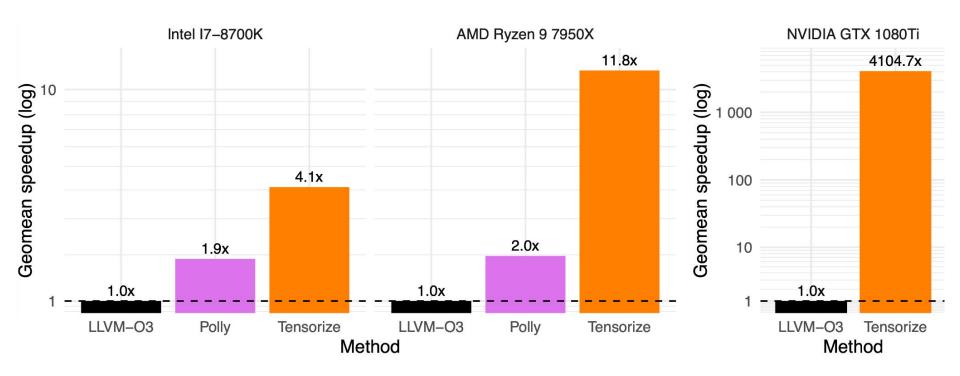
Tensorize Scales to Complex Programs



Tensorize Enables Various Tensor DSL Compilers



Tensorize Achieves Significant Speedups



Conclusions

- Better scalability than any previous tensor program lifting method through problem decomposition
- Runtime speedup of 2.1x (Intel), 5.8x (AMD) over state-of-the-art CPU compilers
- Retargetability to GPUs and hardware accelerators enable further speeups

Future Work

- Faster synthesis through C++ based implementation
- More benchmarks for more extensive evaluation

Tensorize is Open-Source!



- Artifact: https://zenodo.org/records/14095398
- Development repo: https://github.com/alexanderb14/tensorize
- Implementation: MLIR, JAX, SymPy
- Setup: Batteries-included
- Usage: Integrates in MLIR based flows

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