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Simple Simulation of the Micromegas Trigger at High Rates, and a Proposal for Improving the ϕ Measurement

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Abstract

We simulated the MM Trigger Processor at high rates. We can improve the non-precision measurement dramatically.

1 Introduction

Introduction.

2 Micromegas detector and electronics

Micromegas detector and electronics.
Test stand measurements.

3 High luminosity LHC

HL-LHC conditions.

4 Simulation

Standalone simulation.

5 Small stereo roads

Proposal and performance of small stereo roads.

6 Hardware resources

FPGA resources.

7 Conclusion

Conclusion.

References

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