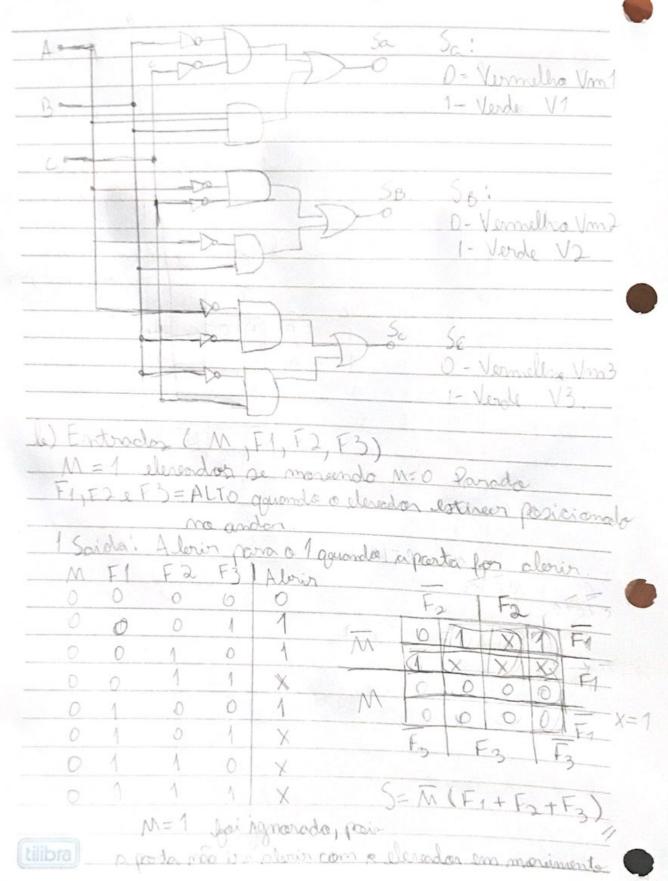
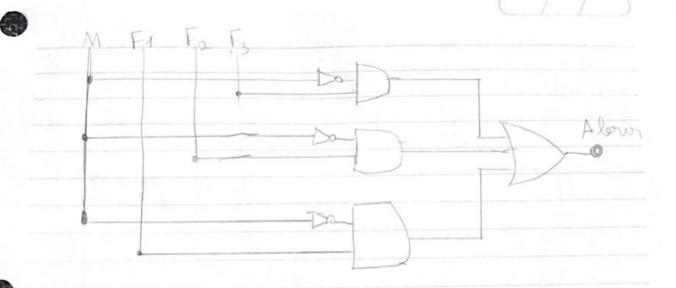
Entrados:  A, B, C  Presença de covos 1  Ausinicia = 0  Simal Vermelha p/ruo, A, B,  Vm1, V2, N  A B C Sa SB Sc - B B  O O O 1 1 1 A M O O O  O 1 0 0 1 A W O O A  O 1 0 0 1 O B B  I O O 1 O O A O O O  I O O O D O O O  I O O O O O O O  I O O O O O O O						
A, B, C  Presence de Corros 1  Ausinaia = 0  Simal Vermelha p/rue, A, B, C  VI, V2, I  Ausinaia = 0  Simal Vermelha p/rue, A, B,  Vm1, Vn  A B C SA SB SE B B  O O O 1 1 1 1 A 10 0 0 0  O 1 0 0 1 A WO (17)  O 1 0 0 1 0 B B  1 0 0 1 0 0 A 0 0 0 0  1 1 0 1 0 0 E B B  A C D O O O E C C			Soll	los:		
Ausinicia = 0  Simal Vermelhio p/rus, A, B,  A B C S S S E B B  O O O 1 1 1 1 A 11 0 0 0 0  O 1 0 0 1 A W O 1 A  O 1 0 0 1 0 B B  I 0 0 1 0 0 A 0 0 0 0  I 1 0 1 0 0 E C C  I 1 1 1 1 0 0 E B B  A C C C	A, B, C		Sim	el Verde	e/ rue	AB,C
Ausinaire = 0  Simal Vermelha p/rus, A, B,  ABCSaSBSe - BBB  00011111AM  01000  0110010  011000  BBB  1001000  1101000  ADDOO	Bresenca de a	avoro 1				
ABC SA SB SE - B B  000 1 1 1 1 A 110 0 0 0  0 0 1 0 0 1 A W 0 1 A  0 1 0 0 1 0 B B  100 1 0 0 A 0 0 0 0  1 1 0 1 0 0 E C C  1 1 1 1 1 0 0 - B B  A 0 0 0 0 0	Ausinaia.	= 0				, , , , , ,
ABC SA SB SE - B B  000 1 1 1 1 A 110 0 0 0  0 0 1 0 0 1 A W 0 1 A  0 1 0 0 1 0 B B  100 1 0 0 A 0 0 0 0  1 1 0 1 0 0 E C C  1 1 1 1 1 0 0 - B B  A 0 0 0 0 0			Sima	l Vermel	lim al	nua A. B.
000 1 1 1 1 A 100 0 0 0 0 0 0 0 0 0 0 0					0 17	
000 1 1 1 1 A 10000 001 0 0 1 A WO 17 010 0 1 0 B B 100 1 0 0 A 0 0 0 0 110 1 0 0 T A 0 0 0 0 110 1 0 0 T B B	ABC	SA SB	SE -	B	B	VIII) VIII
010010		1 1	1 A	Mo		
0 1 1 0 1 0 B B 100 1 0 0 A 0 0 0 0 110 1 0 0 E C C 110 1 0 0 E C C	001	0 0	1 A	Wo	11	
100 100 A0000 1010 100 A0000 110 100 ECC	010	0 1	0		1	
101000 A0000 1101000 ECC	0 1 1	0 1	0	. B	B	
110 100 ECC		1 0	0 A	000	(10)	
111100 BBB AIGHDOLO		0 0	1 A	00	00	
Alatololo	110	1 0	0	[ (	T	
C ==	1 1 1	1 0		B	B	
	(, - 0 7 . 4		A	1 TH	00	
SA=BC+AB AODOO	JA-BU+F	113	A	-	0 0	
て し で	6 7	0 - 7 /-		7 1	C E	
Sc=A·C+AB=A(C+B)		15 - A ( (.	+ B)			

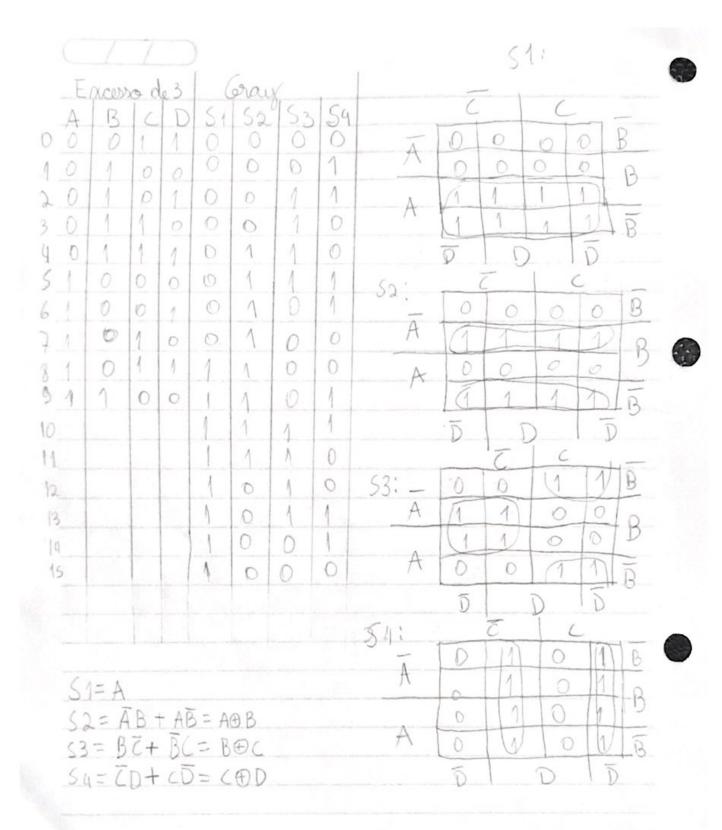
tilibra



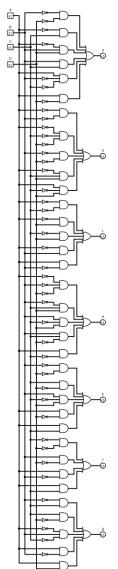


Decimal	Excesso 3	Gray	
0	0011	0000	
1	0100	0001	
2	0101	10011	
3	0110	0010	
4	0111	0110	
5	1000	0111	
6	1001	10101	
7	1010	10100	
8	1011	11100	
9	1100	1101	
6		1111	
41		1110	
12		1010	
13		1011	
14		1001	
15		1000	

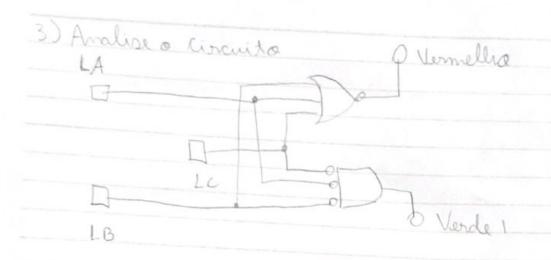
tilibra



b) Circuito decodificador de binário de 4 bits (0000 a 1111) para display de 7 segmentos. Neste caso, o circuito será grande, utilize o simulador Logisim!



A	В	С	D	a	b	С	d	е	f	g
0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	0	1	1	0	0	0	0
0	0	1	0	1	1	0	1	1	0	1
0	0	1	1	1	1	1	1	0	0	1
0	1	0	0	0	1	1	0	0	1	1
0	1	0	1	1	0	1	1	1	0	1
0	1	1	0	1	0	1	1	1	1	1
0	1	1	1	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1	1	1	1
1	0	0	1	1	1	1	1	0	1	1
1	0	1	0	1	1	1	0	1	1	1
1	0	1	1	0	0	1	1	1	1	1
1	1	0	0	1	0	0	1	1	1	0
1	1	0	1	0	1	1	1	1	0	1
1	1	1	0	1	0	0	1	1	1	1
1	1	1	1	1	0	0	0	1	1	1



Entradas: LA, LB, LC > 1 para estendidos
O pora satoridos.

Vermelho = LA+LB+LC Saidos: Vermelho. Verde = LA·LB·Lc Verde.

LA	LB	Lc	Vermelho	Verde
0	0	D	1	1
0	0	1	0	0
0	1	0	0	0
0	4	1	0	0
1	0	0	0	0
1	0	1	0	0
1.	1	0	0	0
1	1	1	0	0

Obrando LA, LB e La latineerem em D, du seja, tretraides, a lug recomelha e a lug recode lataram acesas,

