

Keith Alexander, Paul Brodhead, Richard Bustamante
CSC345-01
Project 3

Project Requirements:

1. All programs able to correctly read the input logical addresses
2. All programs able to translate the logical addresses into physical addresses
3. All programs able to retrieve data stored in physical addresses referenced to by the input logical addresses
4. All programs able to correctly count and output the number of page faults and TLB hits
5. main.c implements FIFO based TLB update w/o page replacement
6. main_pr1.c implements FIFO based TLB update w/ page replacement
7. main_pr2.c implements LRU based TLB update w/ page replacement