ECE/MAE 5310

Designing W Rest Lacos

Obtaining a map of how the closed-loop poles of a system TCD vary bosed on the open loop system (LOCS) is interesting, but is it) Jofaso

Simple Example

K C/6)= K

We want a system that weets the following specifications!

Steady State Error to a Step Input

T-5-wn wn word of some of some

Steady State Error to a Unit Ramp Import To <2.5 &

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Con meet the specifications would a gain (12):

$$\left(\frac{11}{601/5ml}\right) = \left(\frac{11}{2}\left(\frac{11}{601/5ml}\right) + 1\right)^{2}$$

ensures (P.O. C 5%

using 0=45°

9= cos 1.69=46,4

5-08

18. 39S

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PeakTime

Zb-1 Lum - d

Sil = non tool

407 = 2,12 = 2,12

TT = 2.150c.

2,12/1-5

stady-State Error to a Step is zero (way?)

Steady-State Error to a Ramp is

5-70 52 (+1665) = 1124 1 - 1 - 1 - 1124 1 - 5-30 5 - 1-1665) = 1124 1

570 \$ 52+35+K

What is K at the apparating paint?

16=(2,12)2-45

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1000 to

Howdid we do? (show simulation)

an adjustance on in Note that we old us have to Harate or quees at The system (admitedly canned) meets the specifications Using outy

What happens when we can't week specification with a gain? Example Again 95, Ramp error too high! What would I have to do to improve this specito.1

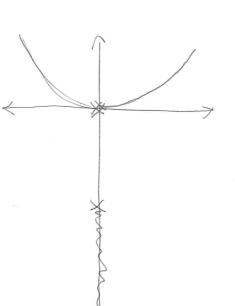
7 - (5 = .2739

see graph p. 49 (Rishling on a balloon) 40 to 50% overshoot rawge

1.595 A

to become the designers,

(1) Add on integrator



(2)