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Layton Payden

Embedded Systems Engineer

1523 Lasagna Street
Sydney, NSW 2024
(+61) 4395147787
LayPay1983@LPayden.com

EXPERIENCE

ORG-A, Sydney— *Senior Embedded Systems Engineer*

Oct 2022 - PRESENT

Supplied components to numerous external entities, including ORG-B and ORG-C.

Developed and managed teams to produce embedded systems for clients.

Collaborated with cross-functional teams to ensure successful project delivery.

ORG-B, Sydney — *Cyber Threat Intelligence Analyst*

Jan 2019 - Dec 2021

Analyzed and monitored cyber threats to safeguard organizational assets.

Provided intelligence reports and recommendations to improve security posture.

Worked closely with ORG-A and other external entities to address emerging cyber threats.

ORG-C, Sydney — Embedded Systems Engineer

Oct 2005 - Jan 2019

Designed and developed embedded systems used in the rail industry, including control units for train operation and monitoring.

Contributed to the design of fault-tolerant, safety-critical systems for train automation, implementing real-time processing capabilities.

Worked on the integration of onboard systems with external control networks to facilitate remote diagnostics and operational monitoring.

EDUCATION

TUWA, Perth — *Bachelor of Computer Science*

Mar 2001 - Sep 2005

First-class honours student, majoring in Embedded Systems and Cyber Security.

SKILLS

Embedded Systems Development.

RTOS.

Safety-critical systems.

Project coordination.

AWARDS

TUWA Student Security Award - 2002

TUWA Helen Maree Thesis Award - 2005

LANGUAGES

English, Russian