```
protected type Interiupt Interfaceline Id : interrupt Id;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            System, Stornge_Elements, To Address(8#150002#1;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            System.Storage Rlements.To_Address(8#150000v):
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     -- specifies the address of the control register
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       for Control Register' Dit order use tow Order First;
                                                                                                                                                                                                                                                                                                                                                                                  specifies the Tayout of the control register
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         entry Ened(Chan : Channel; W : out Measurement);
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            entry Done (Chan : Channel; M : out Neasuroment);
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                Adc_Printity : constant interrupt Priority := 63;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         -- appoilies the address of the data register
with ada, Interrupts, Names; use ada. Interrupts;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    type Inta_Register is range 0 - Max_Monsure;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         for Control_Register Size use Hits_In_Word;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     Cr : access Control Register;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             for Control Reg' Address use Contr_Reg Addr,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         for Control Register Alignment use Word,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            for Data Register' Size use Bils in Mord;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          progna Atlach Handler (Handler, Int_14);
                                                                                         Word : constant := 2; -- bytes in word
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   for Data Reg'Address use Data Brg Addr.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 Control Reg - aliased Control Register-
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  Contr_Reg_Addr : constant Address :=
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  Data Hng Addr : constant Address :-
                                                                                                                                                                                                                                                                                                                                                                                                                                                                           at 0-Word range 8..11; at 0-Word range 15..15;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     - the register is 16 bits long
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    - the register is 16 bits long
                                             package body Adu Davice Drivez is
                                                                                                                                                                                                                                                                                                                                                                                                        Ad Start at 0 Word range 0..0;
                                                                                                                                                                                                                                                                                                                                                                                                                                    at 0+Word range 5..6;
                                                                                                                                                                                                                                                                                                                                                                                                                                                          at 0 - Word range 7..7;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           Date Rog r aliased Date Register:
                                                                                                                                                                   type Control Hagistur is record
                                                                                                                                                                                                                                                                                                                                                          for Control Register use record
                                                                   Bita_In Word : constant :- 16;
                                                                                                                   type Flag is (Down, Set);
                   with System; use System;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         off a World boundary
                                                                                                                                                                                                                                                                    : Channel;
                                                                                                                                                                                         Ad_Start : Fing:
                                                                                                                                                                                                               Tlags
                                                                                                                                                                                                                                                                                          : Flag:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      procedure Handler;
                                                                                                                                                                                                                                              Fiani
                                                                                                                                                                                                                                                                                                                and record;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 and record:
                                                                                                                                                                                                                                                                                                                                                                                                                                                      Done
                                                                                                                                                                                                                                                                                        Brrog
                                                                                                                                                                                                                                        Done
                                                                                                                                                                                                                                                                                                                                                                                                                                                                             Ch
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          private
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12.2 Model of interrupts
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-- see Chapter 13 for discussion on priorities
                                                                                                                                                                                                                                                                                                                                           entry Read(Chan : Channel; M : out Measurement)
                                                                                                                                                                                                  Control Reg'Access, Data Reg'Access);
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             when Interrupt_Occurred is
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     procedure Read(Ch : Channel; M : out Mcssurement) is
                                                                                                                                                                                                                                                                                                                                                                                                                                                               Shadow_Register := (Ad_Start => Set, 18 => Set,
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              Done -> Down, Ch -> Chan, Error -> Down);
                                                                                                                                                                                                                                   -- this assumes that 'Adc' is registered as an
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              entry Done (Chan : Channel; M : out Measurement)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                if Cr.Done = Set and Cr. Error = Down then
                                                                                                                                                                          Mdc_Interlace : Interrupt_Interface(Names.Adc,
pragma Interrupt_Priority(Adu_Priority);
                                                                                                                                                                                                                                                             -- Interrupt Id in Ada. Interrupts. Mammas
                                                        Interrupt Occurred : Boolean :- False;
                                                                                                                                                                                                                                                                                                                                                                                                           Shadow Register + Control Register;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            raise Conversion_Error;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    Interrupt_Occurred :- False;
                                                                                                                                                                                                                                                                                                                   protected body Interrupt Interface is
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          M := Measurement (Dr.all);
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              When Conversion_Error -> null;
                                                                                Mext Request : Spolean := True;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         Adc Interface Read [Ch, M];
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           Interrupt Occurred := True;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          Cr.all :- Shadow_Register:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   Next Request := Falser
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  procedure Handler is
                                                                                                                                                                                                                                                                                                                                                                            when Next_Request is
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 raise Conversion Strory
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      Mext. Request := True;
                                                                                                                 end Interrupt Interface;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        end Interrupt_Interface;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         for I in 1..3 100p
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          and adc Device Driver;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    requeue Donny
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        end Handlerr
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  exception
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       end loop;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        return;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 and if:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                end Done
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                end Read:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         9189
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