<u>``</u>	Msgs														
/testbench/Reset 0															
/testbench/WR 0															
/ /testbench/Clock 1															
/ /testbench/RD 1								_							
<pre>/testbench/CE_SR 0</pre>															
/ /testbench/CE_UART 1															
→ /testbench/ReadData H															
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🛨 👉 /testbench/StringData Hello, world!	H	iello, wor	·ld!												

∳	Msgs															
/testbench/Reset	0															
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<u> </u>	UART Victory!	UART V	lictory!													