processing_system7_0

ZYNQ7 Processing System

- DDR
- FIXED_IO
- IIC_1
- USBIND_0
- M_AXI_GP0
- M_AXI_GP0_ACLK
- TTC0_WAVE0_OUT
- TTC0_WAVE1_OUT
- TTC0_WAVE2_OUT
- TTC1_WAVE0_OUT
- TTC1_WAVE1_OUT
- TTC1_WAVE2_OUT
- WDT_RST_OUT
- FCLK_CLK0
- FCLK_RESET0_N

DDR
FIXED_IO
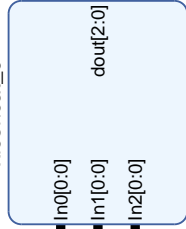
vio_0

VIO (Virtual Input/Output)

- clk
- probe_in0[2:0]

xlconcat_0

Concat

- In0[0:0]
- In1[0:0]
- In2[0:0]
- dout[2:0]

SC0720_0

TE0720 System Controller 1.0

- EMIO_I2C1
- PL_pin_K16
- PL_pin_K19
- PL_pin_M15
- PL_pin_N15
- PL_pin_P16
- PL_pin_P22
- EXT_I2C
- PHY_LED0
- PHY_LED1
- PHY_LED2
- PL_pin_L16
- PL_pin_K20
- PL_pin_N22

PL_pin_L16
PL_pin_K20
PL_pin_N22

PL_pin_K16
PL_pin_K19
PL_pin_M15
PL_pin_N15
PL_pin_P16
PL_pin_P22