# UNISONIC TECHNOLOGIES CO., LTD

# **ULN2803**

## LINEAR INTEGRATED CIRCUIT

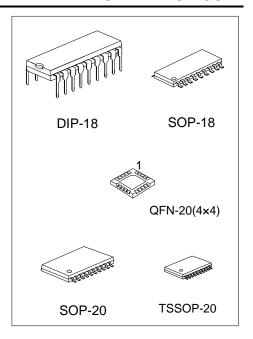
# **EIGHT DARLINGTON ARRAYS**

#### **■ DESCRIPTION**

The UTC **ULN2803** is high-voltage, high-current Darlington drivers comprised of eight NPN Darlington pairs.

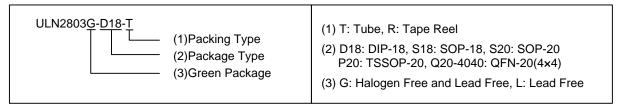
#### ■ FEATURES

- \*Output current (single output) 500mA MAX.
- \*High sustaining voltage output 50V MIN.
- \*Output clamp diodes
- \*Inputs compatible with various types of logic

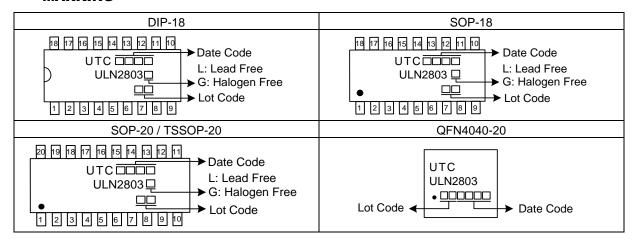


#### ORDERING INFORMATION

Orderin	g Number	Doolsono	Packing	
Lead Free	Halogen Free	Package		
ULN2803L-D18-T	ULN2803G-D18-T	DIP-18	Tube	
ULN2803L-S18-R	ULN2803G-S18-R	SOP-18	Tape Reel	
ULN2803L-S20-R	ULN2803G-S20-R	SOP-20	Tape Reel	
ULN2803L-P20-R	ULN2803G-P20-R	TSSOP-20	Tape Reel	
ULN2803G-Q20-4040-R	ULN2803G-Q20-4040-R	QFN-20(4×4)	Tape Reel	

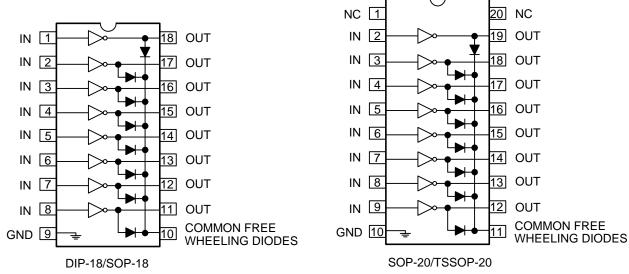


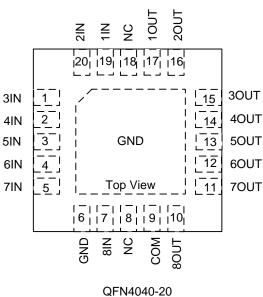
#### **■ MARKING**



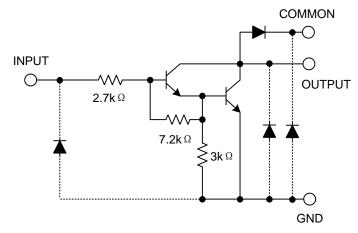
www.unisonic.com.tw 1 of 6

#### **■ PIN CONFIGURATIONS**





## ■ SCHEMATICS (EACH DRIVER)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

#### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Input Voltage		$V_{IN}$	-0.5~30	V
Output Sustaining Voltage		V <sub>CE (SUS)</sub>	-0.5~50	V
Output Current		I <sub>OUT</sub>	500	mA/ch
Clamp Diode Reverse Voltage		VR	50	V
Clamp Diode Forward Current		I <sub>F</sub>	500	mA
Power Dissipation	DIP-18	P <sub>D</sub>	1.47	W
	SOP-18		0.54/0.625 (Note)	W
	SOP-20		0.56	W
	TSSOP-20		0.52	W
	QFN4040-20		0.6	W
Operating Temperature		T <sub>OPR</sub> -40 ~ +85		°C
Storage Temperature		$T_{STG}$	-40 ~ <b>+</b> 150	°C

Notes: 1. On glass epoxy PCB (30x30x1.6mm Cu 50%)

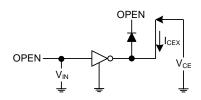
# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CIRCUIT	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Output Leakage Current		I <sub>CEX</sub>	1	V <sub>CE</sub> =50V,T <sub>A</sub> =25°C V <sub>CE</sub> =50V,T <sub>A</sub> =85°C			50 100	μΑ
Collector-Emitter Saturation Voltage		V <sub>CE(SAT)</sub>	2	I <sub>OUT</sub> =350mA,I <sub>IN</sub> =500μA I <sub>OUT</sub> =200mA,I <sub>IN</sub> =350μA I <sub>OUT</sub> =100mA,I <sub>IN</sub> =250μA		1.3 1.1 0.9	1.6 1.3 1.1	V
Input Current	ON	I <sub>IN(ON)</sub>	3	V <sub>IN</sub> =3.85V, I <sub>OUT</sub> =350mA		0.93	1.35	mΑ
	OFF	I <sub>IN(OFF)</sub>	4	I <sub>OUT</sub> =500μA, T <sub>A</sub> =85°C	50	65		μΑ
Input Voltage (output on)		V <sub>IN(ON)</sub>	5	V <sub>CE</sub> =2.0V I <sub>OUT</sub> =200mA I <sub>OUT</sub> =250mA I <sub>OUT</sub> =300mA			2.4 2.7 3.0	V
Clamp Diode Reverse Current		I <sub>R</sub>	6	V <sub>R</sub> =50V, T <sub>A</sub> =25°C V <sub>R</sub> =50V, T <sub>A</sub> =85°C			50 100	μΑ
Clamp Diode Forward Vo	ltage	V <sub>F</sub>	7	I <sub>F</sub> =350mA			2.0	V
Input Capacitance		C <sub>IN</sub>				15	25	pF
Turn-On Delay		t <sub>ON</sub>	8	$V_{OUT}=50V,R_L=125\Omega,C_L=15pF$		0.1	1	μS
Turn-Off Delay		t <sub>OFF</sub>	8	$V_{OUT}=50V,R_L=125\Omega,C_L=15pF$		0.2	1	μS

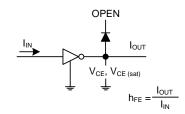
<sup>2.</sup> Absolute maximum ratings are stress ratings only and functional device operation is not implied. The device could be damaged beyond Absolute maximum ratings.

## **■ TEST CIRCUIT**

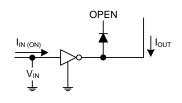
1. I<sub>CEX</sub>



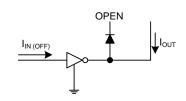
 $2.\;V_{CE\;(sat)},\;h_{FE}$ 



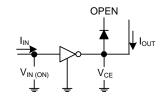
3. I<sub>IN (ON)</sub>



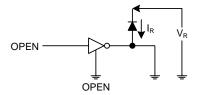
4. I<sub>IN (OFF)</sub>



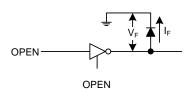
5. V<sub>IN (ON)</sub>



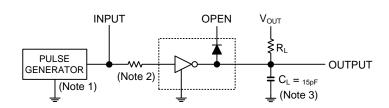
6. I<sub>R</sub>

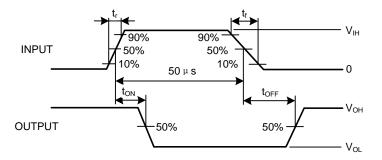


7. V<sub>F</sub>



 $8. t_{ON}, t_{OFF}$ 





Notes: 1. Pulse width  $50\mu s$ , duty cycle 10%

Output impedance  $50\Omega$ , tr<=5ns, tf<=10ns

- 2. R1: 0, V<sub>IH</sub>: 3V
- 3.  $C_L$  includes probe and jig capacitance.

100

οL

0

100

200

300

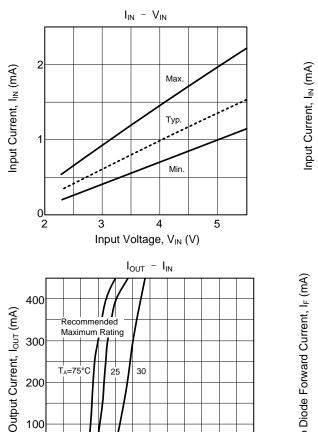
Input Current, I<sub>IN</sub> (µA)

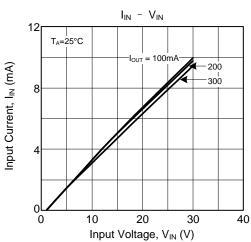
400

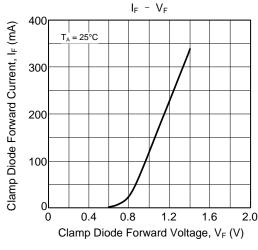
500

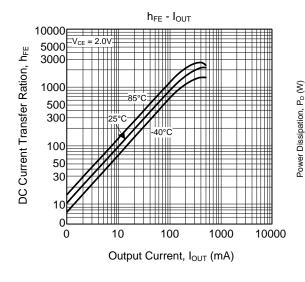
600

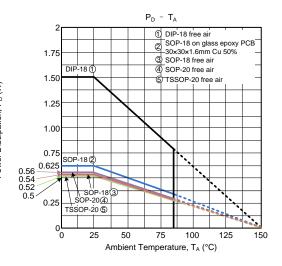
#### TYPICAL CHARACTERISTICS











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