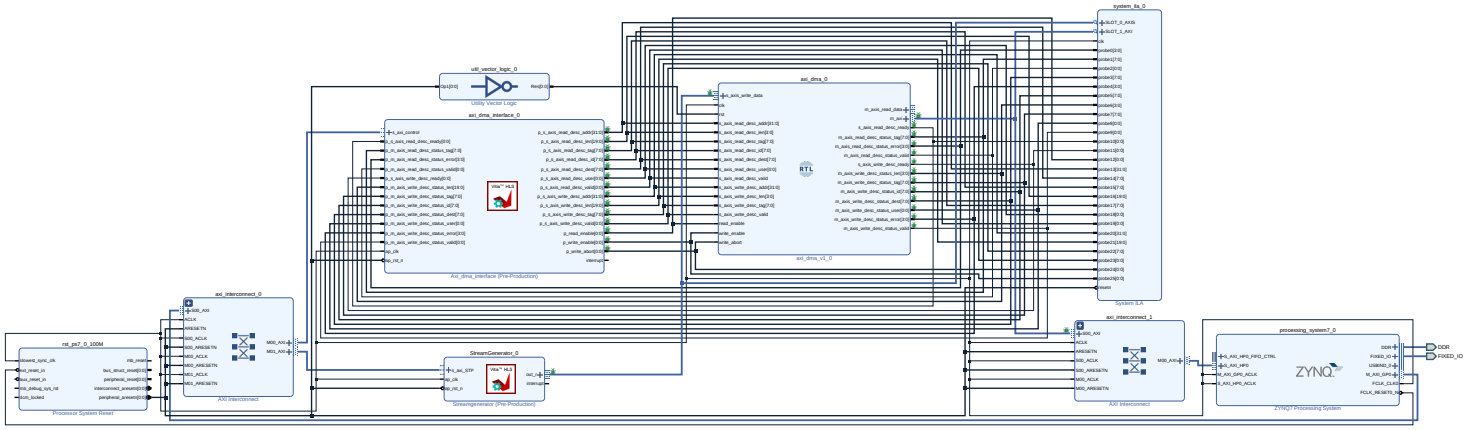
system_ila_0
System ILA
util_vector_logic_0
Utility Vector Logic
axi_dma_interface_0
Axi_dma_interface (Pre-Production)
axi_dma_0
axi_dma_v1_0
axi_interconnect_0
AXI Interconnect
rst_ps7_0_100M
Processor System Reset
StreamGenerator_0
Streamgenerator (Pre-Production)
axi_interconnect_1
AXI Interconnect
processing_system7_0
ZYNQ7 Processing System
DDR
FIXED_IO