Frequency Up-Conversion Schemes for Controlling Superconducting Qubits

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High-fidelity control of superconducting qubits requires the generation of microwave-frequency pulses precisely tailored on nanosecond timescales. These pulses are most commonly synthesized by up-converting and superimposing two narrow-band intermediate-frequency signals referred to as the in-phase (I) and quadrature (Q) components. While the calibration of their DC-offsets, relative amplitude and phase allows one to cancel unwanted sideband and carrier leakage, this IQ mixing approach suffers from the presence of additional spurious frequency components. Here, we experimentally study an alternative approach based on double frequency conversion, which overcomes this challenge and circumvents the need for IQ-calibration. We find a spurious-free dynamic range of more than 70 dB and compare the quality of pulse generation against a state-of-the-art IQ mixing scheme by performing repeated single-qubit randomized benchmarking on a superconducting qubit.

I. INTRODUCTION

Superconducting qubits have emerged as one of the most promising platforms for building error-corrected quantum computers [1–4], which have the potential to solve problems beyond the reach of classical computers [5]. Large-scale quantum computing crucially relies on classical electronics hardware to orchestrate the control and measurement signals. Of particular importance are devices for generating nanosecond-long microwave pulses, which are used for control [6, 7], measurement [8–10], and entangling operations [11, 12] of superconducting qubits and which also play an important role for operating semi-conductor spin qubits [13, 14] and trapped ions [15, 16].

Microwave pulse generation typically employs a radio frequency mixer to up-convert pulses generated by an arbitrary waveform generator (AWG) at an intermediate megahertz frequency to the typical gigahertz transition frequency range of the superconducting qubit [17]. Compared to the direct digital synthesis (DDS) of control pulses [18, 19], frequency up-conversion allows for using AWGs with lower sampling rate, which relaxes resource requirements and thereby eases the scale-up to large channel numbers [20].

Conventional frequency up-conversion schemes utilize an IQ mixer, which up-converts an in-phase and a quadrature component to destructively interfere unwanted sideband and carrier leakages, which can ultimately limit the achievable single-qubit gate fidelity. However, realistic IQ mixers require extensive calibration of the IQ components to achieve optimal performance, and the up-converted output spectrum exhibits additional spurious frequency components, which cannot be canceled by the interference mechanism.

Here, we control a superconducting qubit with an alternative microwave pulse generation scheme, which makes use of two frequency conversion stages and uses analog filters to remove the unwanted sideband and carrier leakages from the up-converted control pulse. We compare the signal quality of this double frequency conversion scheme to a conventional IQ mixing scheme, and find the output of the double-conversion stage to exhibit smaller spurious frequency components and to be less affected by variations in the ambient temperature. We also find a slight improvement in the single-qubit gate fidelity when using the double frequency conversion scheme to generate the control pulses. For our study, we use a high-density IQ converter (HDIQ) and a super-high-frequency signal generator (SHFSG) to investigate the two schemes.

II. FREQUENCY UP-CONVERSION

High-fidelity single-qubit control is achieved with resonant microwave pulses of the general form

$$V_d(t) = \tilde{v}_{\rm I}(t)\cos(\omega t + \phi) + \tilde{v}_{\rm Q}(t)\sin(\omega t + \phi), \quad (1)$$

where ω is the transition frequency of the qubit, ϕ a global phase, and $\tilde{v}_{\rm I}(t)$ and $\tilde{v}_{\rm Q}(t)$ are two independent pulse envelope functions. A common choice for $\tilde{v}_{\rm I}(t)$ and $\tilde{v}_{\rm Q}(t)$ is the Gaussian DRAG pulse parametrization, which allows to avoid leakage into non-computational states [6, 7, 21].

To generate the control pulse at frequency ω by upconversion, we multiply an intermediate frequency signal generated by an AWG with a local oscillator continuously running at frequency ω_{LO} in the gigahertz range. The IF signal consists of the two independent pulse envelope functions $\tilde{v}_{\text{I}}(t)$ and $\tilde{v}_{\text{Q}}(t)$ defined in software and modulated digitally at frequency ω_{IF} , see Appendix A for details. We multiply the IF signal with the LO using a radio frequency mixer resulting in an output spectrum $S(\omega)$ featuring two

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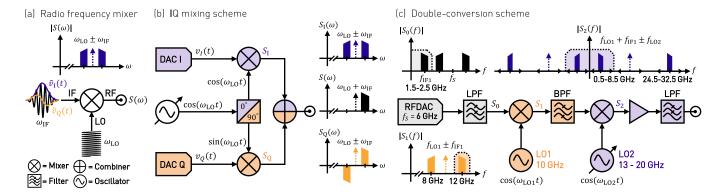


FIG. 1. Frequency up-conversion schemes. (a) Diagram and schematic output spectrum $S(\omega)$ of a radio frequency mixer used for up-conversion, where both sidebands at frequencies $\omega_{\text{LO}} \pm \omega_{\text{IF}}$ are equally present. The frequency of the LO carrier is indicated with a dashed arrow. (b) Sketch of an IQ mixer, for which the outputs of two parallel up-conversion paths, with an LO signal shared through a 90°-hybrid splitter, are superimposed in a microwave combiner. The IF signals for the IQ mixer are generated by two independent digital-to-analog converters (DAC). (c) Sketch of a double frequency conversion scheme with two separate mixing stages fed by two different LO signals. The analog filtering after each stage is indicated by a dashed box in the respective spectrum $S_i(f)$.

sidebands centered around the frequencies $\omega_{\rm LO} \pm \omega_{\rm IF}$, see Fig. 1(a). Throughout the paper, we use the convention of driving the qubit with the upper sideband at frequency $\omega = \omega_{\rm LO} + \omega_{\rm IF}$ and consider the frequency component $\omega_{\rm LO} - \omega_{\rm IF}$ the undesired image of the control pulse, which, when present in the spectrum of the qubit drive pulse, induces a gate error.

III. IQ MIXING SCHEME

An established method to eliminate the image component is IQ mixing [17, 22], as shown in Fig. 1(b). Here, two parallel up-conversion paths, for two IF signal waveforms $v_{\rm I}(t)$ and $v_{\rm O}(t)$, are equipped with radio frequency (RF) mixers which share a local oscillator signal through a hybrid splitter which adds a 90°-phase shift to the signal driving the Q port mixer. The signals from the two parallel mixing paths are superimposed in a microwave combiner, where ideally one of the two up-converted sidebands is canceled out by destructive interference, see Appendix A. The spectra in Fig. 1(b) illustrate this mechanism for the case in which both $v_{\rm I}(t)$ and $v_{\rm Q}(t)$ have a real-valued Fourier transform. In this particular case, the destructive interference in the microwave combiner is visible from the opposite signs of the lower-sideband signals at frequency $\omega_{\rm LO} - \omega_{\rm IF}$, see the spectra $S_{\rm I}$ and $S_{\rm O}$ after the respective RF mixers in Fig. 1(b). To generate the control pulse described by Eq. (1) with an IQ mixer, we follow the procedure described in Appendix A and compute in software two IF signals $v_{\rm I}(t)$ and $v_{\rm O}(t)$, which, when up-converted using an ideal IQ mixer, result in an image-free spectrum.

Under realistic conditions, however, the implementation of an IQ mixer is subject to imperfections, such as amplitude and phase imbalances between the two parallel mixing paths [17, 23]. These imperfections result in an up-converted spectrum that exhibits unwanted sideband and carrier leakage, which we compensate for by calibrating the relative phase, amplitude and DC offsets of the IF signals, as described in the following.

First, the LO carrier signal leaks through each of the two RF mixers with amplitude $L_{\rm I}$, $L_{\rm Q}$ and phase $\theta_{\rm I}$, $\theta_{\rm Q}$, respectively, such that the LO signal at the output of the IQ mixer is [23]

$$y_{\rm LO}(t) = L_{\rm I}\cos(\omega_{\rm LO}t + \theta_{\rm I}) + L_{\rm Q}\sin(\omega_{\rm LO}t + \theta_{\rm Q}).$$
 (2)

In many basic applications, this effect is not compensated for, but an additional DC bias at the IQ mixer input can help to decrease the amplitude of $y_{\rm LO}(t)$. For this purpose, we apply DC voltages $V_{\rm I}$ and $V_{\rm Q}$ to the IF-ports of the IQ mixer, which create additional signals of the form $V_{\rm I}\cos(\omega_{\rm LO}t) + V_{\rm Q}\sin(\omega_{\rm LO}t)$ at the output. These signals interfere destructively with $y_{\rm LO}(t)$ to result in a total amplitude

$$Y_{LO} = |V_{I} + L_{I}e^{i\theta_{I}} - iV_{Q} - iL_{Q}e^{i\theta_{Q}}|$$
(3)

of carrier leakage at the output of the mixer. $Y_{\rm LO}$ is minimized for one specific combination of DC compensation voltages. To find this combination experimentally, we measure the amplitude $Y_{\rm LO}$ with an FPGA-based acquisition system, see Appendix B, while varying the DC offsets $V_{\rm I}$ and $V_{\rm Q}$ with coarse resolution, see Fig. 2(a). We fit Eq. (3) to the measured data to obtain optimal compensation parameters, which are not limited to the resolution between the discrete values of the underlying measurement data and further enhance the calibration accuracy, see Fig. 2(b).

Second, imbalances in the relative amplitude and phase between the two parallel mixing paths modeled by the scaling factor $\tilde{\alpha} \cdot e^{i\tilde{\phi}}$ can result in an imperfect destructive interference of the image component at frequency $\omega_{\text{IM}} =$

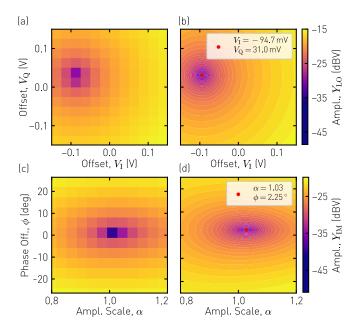


FIG. 2. **IQ** mixer calibration. (a) Measured amplitude $Y_{\rm LO}$ of the LO leakage when varying the DC offsets, $V_{\rm I}$ and $V_{\rm Q}$, and (b) a fit of Eq. (3) to the measured data set. The red data point marks the compensation parameters obtained from the fit. (c) Measured amplitude $Y_{\rm IM}$ of the undesired image at frequency $\omega_{\rm LO} - \omega_{\rm IF}$ as a function of the amplitude scale α and the phase offset ϕ and (d) a fit of Eq. (5) to the measured data set.

 $\omega_{\rm LO}-\omega_{\rm IF},$ such that the residual amplitude of the image signal is [17]

$$y_{\rm IM}(t) = \frac{1}{2}\cos(\omega_{\rm IM}t) - \frac{\tilde{\alpha}}{2}\cos(\omega_{\rm IM}t + \tilde{\phi}). \tag{4}$$

We compensate for this imperfection by modifying the IF signals $v_{\rm I}(t) = \cos(\omega_{\rm IF}t)$ and $v_{\rm Q} = \sin(\omega_{\rm IF}t)$ to $v_{\rm I}'(t) = \cos(\omega_{\rm IF}t)$ and $v_{\rm Q}'(t) = \alpha^{-1}\sin(\omega_{\rm IF}t - \phi)$ where α and ϕ are the compensation parameters [17, 24] resulting in a total image amplitude

$$Y_{\rm IM} = \frac{1}{2} \left| 1 - \frac{\tilde{\alpha}}{\alpha} e^{i(\tilde{\phi} - \phi)} \right| \tag{5}$$

at the output of the mixer, which vanishes for $\tilde{\alpha}/\alpha=1$ and $\phi-\tilde{\phi}=2\pi n$ with $n\in\mathbb{N}$. To find the optimal compensation parameters, we measure the amplitude $Y_{\rm IM}$ for varying α and ϕ , see Fig. 2(c), and fit Eq. (5) to the measured data set similarly as in the calibration procedure of the carrier leakage, see Fig. 2(d).

IV. DOUBLE FREQUENCY CONVERSION

An alternative approach to cancel the image component uses analog filtering instead of destructive interference. For conventional RF mixing schemes, however, this would require bandpass filtering with a narrow bandwidth on the order of the IF frequency, which is challenging to realize. In addition, the center frequency of this analog filter would require tunability for addressing qubits in multiple frequency bands. An implementation that overcomes these technical challenges uses a double frequency conversion scheme [25], which makes use of two separate mixing stages fed by two different local oscillators, see Fig. 1(c). By keeping the frequency of the first LO fixed and having the second LO tunable, a bandpass filter with a fixed center frequency can be used to effectively eliminate sideband and carrier leakage, as described in detail below.

To generate the qubit control signal, we digitally define the control pulse at an intermediate frequency of $f_{\rm IF1} = 1.5$ to 2.5 GHz, see Fig. 1(c). For that purpose, we employ an RF digital-to-analog converter (RFDAC) with a sampling rate of $f_{\rm S} = 6\,{\rm GSa/s}$ resulting in a first Nyquist frequency of $f_{\rm S}/2 = 3\,\rm GHz$. We use a subsequent 2.5 GHz low-pass filter to remove the alias spectra which arise from defining the signal with the RFDAC, and we up-convert the filtered signal with a fixed-frequency LO, which is continuously running at $f_{LO1} = 10 \,\text{GHz}$, see spectrum $S_1(f)$ in Fig. 1(c). We remove the emerging image component at the frequency $f_{\rm LO1}-f_{\rm IF1}$ using an analog bandpass filter with a pass-band frequency of approximately $12\,\mathrm{GHz} \pm 500\,\mathrm{MHz}$. Then, we down-convert the filtered signal using a second LO with an adjustable frequency range of $f_{LO2} = 13$ to 20 GHz. The image component emerging in the down-converted spectrum ranges from 24.5 to 32.5 GHz and is thus far detuned from the targeted frequency band of 0.5 to 8.5 GHz, see spectrum $S_2(f)$ in Fig. 1(c). Due to the large detuning, we can use a subsequent low-pass filter to suppress unwanted image components in the qubit control pulse spectrum.

We note that the up-converted spectrum of the qubit control signal is mirrored compared to the spectrum of the initial IF signal since the final down-conversion uses an LO which is higher in frequency than the signal in the previous stage, see also the sketch of the spectrum $S_2(f)$ in Fig. 1(c). We correct for this effect in software by inverting the sign of the IF frequency $f_{\rm IF1}$ prior to the digital up-conversion before the RFDAC.

Compared to the IQ mixing approach, which relies on the destructive interference of two up-converted spectra, the elimination of the image component by analog filtering does not require calibration of the control signals to compensate for RF mixer imperfections. In addition, the carrier leakage of the local oscillators through the RF mixers at $f_{\rm LO1}=10\,{\rm GHz}$ and $f_{\rm LO2}\geq 12\,{\rm GHz}$ can also effectively be eliminated by means of filtering in the double-conversion scheme.

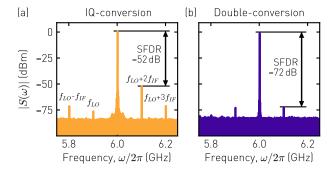


FIG. 3. **Up-converted spectra & SFDR.** (a) Typical output spectrum $S(\omega)$ of a calibrated IQ mixer and (b) output spectrum of the double frequency conversion scheme. We indicate the spurious-free dynamic range (SFDR) in both panels with a double arrow.

V. VERIFICATION OF SIGNAL QUALITY

A. Spurious-free dynamic range

We verify the signal quality of both conversion schemes at room temperature by measuring the corresponding output spectra using an HP8563A spectrum analyzer while generating a continuous wave signal at 6 GHz with a power of 0 dBm. These are typical parameters for achieving fast single-qubit control, see Appendix B for details. We then evaluate the spurious-free dynamic range (SFDR), which is defined as the power ratio between the fundamental signal and the strongest spurious frequency component, see Fig. 3.

We perform IQ mixer calibration, as described in Sec. III, and measure the resulting output spectrum $S(\omega)$ for the input frequencies $f_{\rm IF}=100\,{\rm MHz}$ and $f_{\rm LO}=5.9\,{\rm GHz}$. Here, the level of the LO leakage and the unwanted sideband are about 70 dB smaller than the desired up-converted signal at $f_{\rm LO}+f_{\rm IF}=6\,{\rm GHz}$, see Fig. 3(a). However, the SFDR is limited to 52 dB by a spurious tone at frequency $f_{\rm LO}+2f_{\rm IF}$. This tone arises from an imperfection of the RF mixer, which under realistic conditions only implements an approximate multiplication of the input signals and additionally creates higher-order polynomial products [24, 26]. These higher-order products give rise to an output spectrum which contains additional harmonic components at frequencies [24]

$$n \cdot f_{\text{LO}} + m \cdot f_{\text{IF}}$$
 with $n, m \in \mathbb{Z}$. (6)

Higher order polynomial products with $|n|, |m| \neq 1$ are not suppressed by the interference mechanism in the IQ mixer and thus degrade the achievable SFDR.

For the double-conversion scheme, we generate a continuous wave signal at $f_{\rm IF1}=2\,\rm GHz$ with the RFDAC and use the frequency $f_{\rm LO2}=18\,\rm GHz$ to synthesize a 6 GHz output signal. Besides the desired signal at 6 GHz, we observe two spurious frequency components at frequencies $6\pm0.1\,\rm GHz$, which result from crosstalk of the

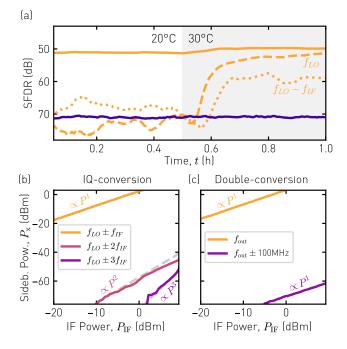


FIG. 4. Spurious frequency components. (a) Response of the SFDR to a step change in ambient temperature for the IQ mixing (orange) and the double-conversion scheme (purple). The dashed (dotted) orange line displays the suppression of LO leakage (image) in the IQ mixing scheme. (b) IF input power dependence of all spurious IQ mixing frequency components from Fig. 3(a). The dashed gray lines depict a fit with a linear function for each of the power dependencies with a slope corresponding to the order m of the IF harmonic. (c) IF input power dependence of all spurious frequency components of the double-conversion scheme from Fig. 3(b).

 $100\,\mathrm{MHz}$ reference used in the instrument. In contrast to the IQ mixing scheme, the higher harmonic frequency components from Eq. (6) can effectively be filtered in the double-conversion scheme, and we attain an SFDR of $72\,\mathrm{dB}$.

To study the stability of both frequency conversion schemes, we measure the SFDR when varying the ambient temperature. For this purpose, we install both systems in a controlled temperature chamber and record the SFDR over the course of 1 h when abruptly changing the ambient temperature from 20 °C to 30 °C after 30 minutes, see Fig. 4(a). For the IQ mixer, we record, in addition to the SFDR (orange line), the level of unwanted sideband and carrier leakage (dashed and dotted orange lines). We find that shortly after the ambient temperature is changed, the suppression of both sideband and carrier leakage degrades significantly, while the SFDR imposed by the second IF harmonic at frequency $f_{\rm LO} + 2 \cdot f_{\rm IF}$ is only weakly affected. This behavior can be traced back to the temperature dependence of the IQ mixer imperfections, which result in a drift of the optimal IQ calibration parameters, see Eq. (3) and (5). The SFDR of the double-conversion scheme (purple), however, is

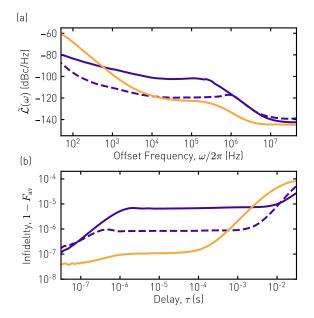


FIG. 5. Single-sideband phase noise. (a) Measured single-sideband phase noise $\tilde{\mathcal{L}}(\omega)$ at 5 GHz for the IQ mixing scheme (orange), and the double-conversion scheme with (dashed purple) and without (solid purple) using an LO with improved phase noise. (b) Process infidelity $1-F_{\rm av}$ in a Ramsey experiment with pulse delay τ calculated based on the noise spectra in (a).

unaffected by the temperature change since it does not rely on a the destructive interference of two signals.

We also investigate the peak intensity of the spurs shown in Fig. 3 in dependence on the IF input power P. For the IQ mixing scheme, we find a polynomial power dependence $\propto P^m$ where m is the polynomial order from Eq. (6), see Fig. 4(b). The power dependence of the $100\,\mathrm{MHz}$ reference spurs in the double-conversion scheme is linear with $\propto P$ such that the SFDR is unaffected.

Due to the polynomial power dependence $\propto P^m$, a general strategy for suppressing higher harmonic frequency components is the reduction of IF input power. To still achieve the desired output power, an RF-amplifier can therefore be added after the IQ mixer. In addition, harmonics of the LO with order $|n| \neq 1$ can be suppressed with a bandpass filter after the RF amplifier. Both measures are implemented for the IQ-conversion module used in this study with a 4 to 8 GHz bandpass filter and an RF amplifier with 21 dB gain.

B. Single-sideband phase noise

An additional metric of signal quality is the phase noise of the qubit control signal, which can affect the average single-qubit gate fidelity. We measure the single-sideband phase noise $\mathcal{L}(\omega)$ for both frequency conversion schemes at 5 GHz with a signal analyzer (AnaPico APPH). The

orange line in Fig. 5(a) shows the phase noise at the output of the IQ mixer, which is dominated by the noise of the external LO used in the experiment (R&S SGS100A). The phase noise of the signal synthesized with the doubleconversion scheme is dictated by the noise of the second LO, which for offset frequencies greater than 1 kHz is larger than the noise from the IQ-conversion module due to the high LO frequency. To estimate the impact of phase noise on gate fidelities, we follow the procedure described in Ref. [27] and compute the infidelity $1 - F_{\rm av}$ in a Ramsey experiment as a function of the Ramsey delay time τ . For both frequency conversion schemes, the calculated infidelity is more than one order of magnitude smaller than the limit imposed by qubit coherence, which we determine in the following section, see Fig. 5(b). To assure that phase noise is not a limiting factor once qubit coherence increases, we have improved the circuit design of the LO used in the double frequency conversion scheme, see dashed purple line in Fig. 5, and find a fidelity improvement of about one order of magnitude in the relevant delay regime between micro- and milliseconds.

VI. VERIFICATION ON QUANTUM HARDWARE

To verify the performance of both frequency conversion schemes in controlling quantum hardware, we perform repeated randomized benchmarking (RB) of single-qubit gates [28, 29].

For this purpose, we combine the output of the IQ mixing module (Zurich Instruments, HDIQ) and of the double frequency conversion module (Zurich Instruments, SHFSG) with a microwave combiner and connect them to the drive line of a superconducting qubit, which is installed in a cryogenic measurement setup, see Appendix B for details. For each conversion scheme, we perform standard time-domain calibration of the 50-ns-duration qubit drive pulse using a DRAG pulse parametrization [6, 7] with a truncated Gaussian envelope. We quantify the amount of leakage into the non-computational second excited state $|f\rangle$ by performing dispersive readout which discriminates between all three states $|g\rangle$, $|e\rangle$, and $|f\rangle$, see Appendix B.

To account for the effect of slow drifts in the system parameters such as the qubit coherence time, we measure the single-qubit gate error repeatedly and alternate 25 times between using the IQ mixer and the double frequency conversion module for applying the RB pulse sequence. We measure the probability P_g of returning to the ground state after the RB sequence and observe P_g to closely follow the limit imposed by the measured qubit decoherence, which we have calculated according to Ref. [30], see dashed line in Fig. 6(a) for an example result.

The measured errors per Clifford range from 0.14% to 0.19% in both cases and are close to the coherence limit of 0.13% (dashed line), see histogram of errors obtained from RB in Fig. 6(b). For the IQ mixing scheme, we find an average error per Clifford of $\bar{p}_e = 0.169 \pm 0.007\%$,

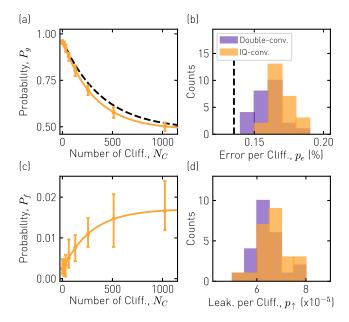


FIG. 6. Randomized benchmarking. (a) Measured probability P_g of returning to the ground state vs. the number of Clifford gates N_C in a single-qubit randomized benchmarking experiment and estimated coherence limit (dashed black line). (b) Histogram of the measured error per Clifford for 25 repeated single-qubit randomized benchmarking experiments in comparison to the coherence limit (dashed line). (c) Measured leakage probability P_f vs. N_C and (d) histogram of the leakage probability per cycle p_{\uparrow} for the same set of experiments as shown in (b).

and $0.161 \pm 0.008\,\%$ for the double-conversion scheme. In addition to the probability P_g in the RB experiment, we also measure the accumulated leakage P_f into the non-computational state $|f\rangle$. By fitting P_f with a rate equation model, as detailed in Ref. [21], we can extract the leakage up-rate p_{\uparrow} per Clifford, see Fig. 6(c). From the distribution of leakage rates shown in Fig. 6(d), we find an average rate of $\overline{p}_{\uparrow} = 6.8 \pm 0.7 \times 10^{-5}$ for the IQ-conversion scheme and $6.3 \pm 0.7 \times 10^{-5}$ for the double-conversion scheme. For both the measured error and leakage per Clifford, the double-conversion scheme results in values about one standard deviation smaller, which suggests that the double-conversion scheme performs at least as well as the conventional IQ mixing scheme.

VII. CONCLUSION AND DISCUSSION

By employing a double frequency conversion scheme to synthesize signals for controlling qubits which are used in superconducting quantum processors, we have demonstrated a promising alternative to the more common IQ mixing approach. The major advantage of this scheme is an output spectrum with a very low level of spurious frequency components, which does not require extensive

calibration and is robust to variations in the laboratory temperature. The benchmarking experiment on quantum hardware has shown a slight improvement in the single-qubit gate fidelity when control pulses are generated with the double-conversion scheme compared to the conventional IQ mixing scheme, which motivates further study of the underlying gate error mechanism. In addition, the large SFDR of the double-conversion scheme makes it a promising candidate for activating high-fidelity two-qubit gate interactions with RF pulses, such as those used in parametric or cross-resonance gates [11, 12]. The double frequency conversion scheme can also find applications in the readout of superconducting qubits, for which both an up- and down-conversion stage can be realized analogously to the scheme presented in this work.

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APPENDIX A. IQ PULSE GENERATION

A generic time-dependent voltage $V_d(t)$, used e.g. for controlling the qubit state, can be parametrized as in Eq. (1) as

$$V_d(t) = \tilde{v}_{\rm I}(t)\cos(\omega t + \phi) + \tilde{v}_{\rm Q}(t)\sin(\omega t + \phi), \quad (A1)$$

where ω is the carrier frequency, ϕ is the initial phase of the control voltage, and $\tilde{v}_{\rm I}(t)$ and $\tilde{v}_{\rm Q}(t)$ are two independent pulse envelope functions.

To generate the control voltage $V_d(t)$ from (A1) with the aid of an IQ mixer, we apply to the IF ports the input voltages $v_{\rm I}(t)$ and $v_{\rm Q}(t)$, which we compute in software according to

$$\begin{pmatrix} v_{\rm I} \\ v_{\rm Q} \end{pmatrix} = \begin{pmatrix} \cos(\omega_{\rm IF}t + \phi) & \sin(\omega_{\rm IF}t + \phi) \\ -\sin(\omega_{\rm IF}t + \phi) & \cos(\omega_{\rm IF}t + \phi) \end{pmatrix} \begin{pmatrix} \tilde{v}_{\rm I} \\ \tilde{v}_{\rm Q} \end{pmatrix}$$
(A2)

Using the IQ mixer, the IF signals modulate the continuous wave LO signal such that the signal at the output ideally is

$$V_d(t) = \left(\cos(\omega_{\text{LO}}t), \sin(\omega_{\text{LO}}t)\right) \begin{pmatrix} v_{\text{I}} \\ v_{\text{Q}} \end{pmatrix}.$$
 (A3)

With the trigonometric identities, $2\cos x \cos y = \cos(x+y) + \cos(x-y)$ and $2\sin x \sin y = \cos(x-y) - \cos(x+y)$, we can simplify Eq. (A3) and express the signal at the output of the IQ mixer as

$$V_d(t) = \tilde{v}_{\rm I}(t)\cos\left((\omega_{\rm LO} + \omega_{\rm IF})t + \phi\right) + \tilde{v}_{\rm Q}(t)\sin\left((\omega_{\rm LO} + \omega_{\rm IF})t + \phi\right). \tag{A4}$$

When comparing Eq. (A1) to Eq. (A4), it becomes apparent that the output signal is generated at a frequency of $\omega = \omega_{\rm LO} + \omega_{\rm IF}$. When inverting the sign of the IF frequency $\omega_{\rm IF}$ in Eq. (A2), the up-converted signal is created at a frequency of $\omega = \omega_{\rm LO} - \omega_{\rm IF}$ instead.

APPENDIX B. EXPERIMENTAL SETUP

A. Qubit fabrication and properties

We have fabricated the superconducting transmon qubit in a process similar to the one described in Ref. [4]. Here, we sputter a Niobium thin film onto a high-resistivity intrinsic Silicon substrate and pattern the Niobium layer using photolithography and reactive-ion etching. After patterning the Niobium base layer, we fabricate airbridges to establish well-connected ground planes and to enable crossings of signal lines. We fabricate Josephson junctions by shadow evaporation of aluminum through a resist mask defined by electron-beam lithography.

We have extracted the qubit and readout circuit parameters, summarized in Table A1, using standard spectroscopy and time-domain measurements.

Parameters	Value
Qubit idle frequency, $\omega_{\rm q}/2\pi$ (GHz)	6.143
Qubit anharmonicity, $\alpha_{\rm q}/2\pi({\rm MHz})$	-178
Lifetime, $T_1 (\mu s)$	23.7
Ramsey decay time, $T_2^*(\mu s)$	11.7
Echo decay time, $T_2^{\rm e}$ (μ s)	16.6
Readout resonator frequency, $\omega_{\rm r}/2\pi({\rm GHz})$	7.141
Readout linewidth, $\kappa_{\rm eff}/2\pi ({\rm MHz})$	10
Dispersive Shift, $\chi/2\pi$ (MHz)	-2.4
Thermal population, $P_{\rm th}$ (%)	3.6
3-level readout assignment prob. (%)	97.2

TABLE A1. Measured parameters of the qubit and of the readout circuit.

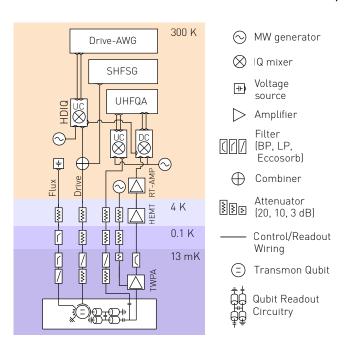


FIG. A1. Experimental Setup. Schematic of the control electronics and wiring setup. For details see Appendix B.

B. Wiring and instrumentation

We have installed the superconducting qubit in a cryogenic measurement setup with a base plate temperature of 13 mK. We have connected the qubit to the control and measurement electronic setup located at room temperature as indicated in Fig. A1. We control the qubit frequency with a magnetic flux, which is generated by a nearby current flowing through a dedicated flux control line. We achieve single-qubit XY-control through a dedicated drive line, which is capacitively coupled to the gubit and carries the microwave pulses, which are created by the room temperature electronic setup as explained in the main text. As a local oscillator source for the IQ-conversion scheme, we use the Rohde & Schwarz SGS100A signal generator. To enable the calibration of the IQ mixer, we install an additional switchable RF line which can bypass the cryogenic measurement setup and feed the up-converted spectrum directly into the downconversion module of the readout line, see Fig. A1.

We perform qubit readout using an FPGA-based measurement system with a sample rate of 1.8 GSa/s (Zurich Instruments UHFQA). The readout pulse is up-converted to the readout resonator frequency and routed through a highly attenuated (60 dB) RF line to the qubit chip. After propagating through the readout circuitry of the qubit chip, the readout signal is amplified by a near-quantum-limited traveling wave parametric amplifier (TWPA) [31], which is mounted at the base plate of the cryostat. After the TWPA, the readout signal is further amplified by a high-electron mobility transistor (HEMT) at 4 K, and low-noise amplifiers at room temperature (RT-AMP). The amplified readout signal is down-converted to an interme-

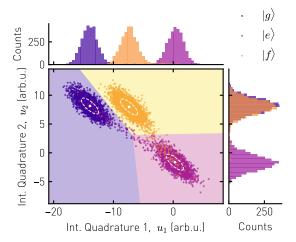


FIG. A2. Three-state single-shot readout. Measured integrated readout signals u_1 and u_2 of the qubit prepared 3000 times in each of the three states $|g\rangle$, $|e\rangle$ and $|f\rangle$, after preselection. The histogram distributions of u_1 and u_2 are shown in the top and right panel, respectively. The mean value (dot) and 1σ confidence ellipse (dashed) of each fitted Gaussian distribution is indicated in white. The colored regions indicate the assigned state in the integrated quadrature plane spanned by u_1 and u_2 .

diate frequency before being digitized and integrated by the weighted integration units of the UHFQA.

C. Single-qubit gates

To achieve single-qubit control, we generate microwave pulses with the frequency conversion schemes presented in the main text. We choose the bias of the flux control line to maximize the qubit transition frequency, at which the qubit is to first order insensitive to flux noise, frequently called the upper sweet spot [32]. The microwave pulses for single-qubit control follow a Gaussian DRAG pulse parametrization [6, 7]. with a pulse width of $\sigma=10\,\mathrm{ns}$ and are truncated at $\pm 2.5\sigma=50\,\mathrm{ns}$.

D. Three-state single-shot readout

To dispersively read out the state of the transmon qubit, we apply a square pulse at frequency $\omega_{\rm r}$ of duration $\tau=1~\mu{\rm s}$ with a Gaussian rising and falling edge ($\sigma=10~{\rm ns}$) to the readout resonator of the qubit [33]. We multiply the digitized readout signal s(t) in real-time on the UHFQA with two sets of complex-valued integration weights $w_1(t)$ and $w_2(t)$ to optimally distinguish between the first three states of the transmon qubit. We record the integrated readout signals

$$u_i = \operatorname{Re}\left\{ \int_0^{1.2 \, \mu s} s(t) w_i(t) dt \right\}$$
 (A5)

and evaluate the readout performance by preparing the qubit 3000 times in each of the three states $|g\rangle$, $|e\rangle$ and $|f\rangle$. Prior to the state preparation pulse, we prepend an additional readout, which allows us to condition each experimental repetition on the qubit being initially in the ground state $|g\rangle$. From this, we find a thermal population of 3.6%. We analyze the preselected integrated readout signal histogram by fitting a trimodal Gaussian mixture model which allows us to assign qubit states to the integrated measurement results, see Fig. A2. From this experiment, we find the readout assignment probabilities $(p_{|g\rangle}, p_{|e\rangle}, p_{|f\rangle}) = (99.7, 95.8, 96.0)\%$ resulting in an average three-state readout error of 2.8%.

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