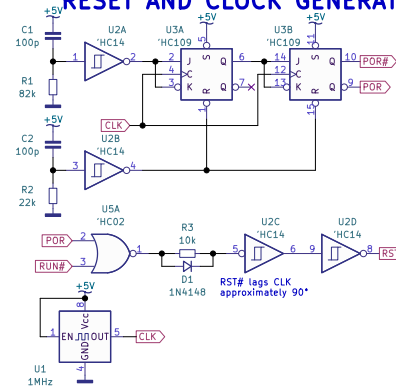
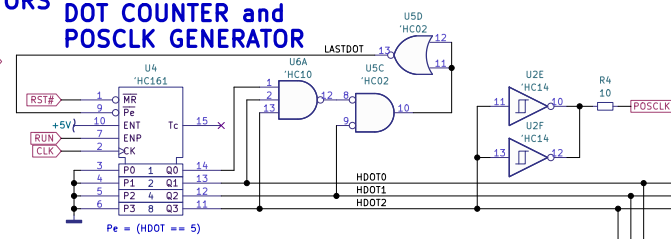


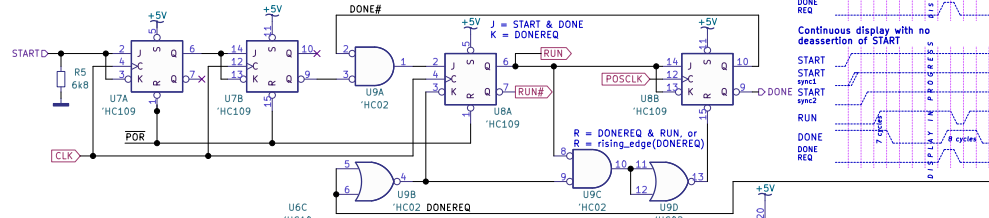
RESET AND CLOCK GENERATORS



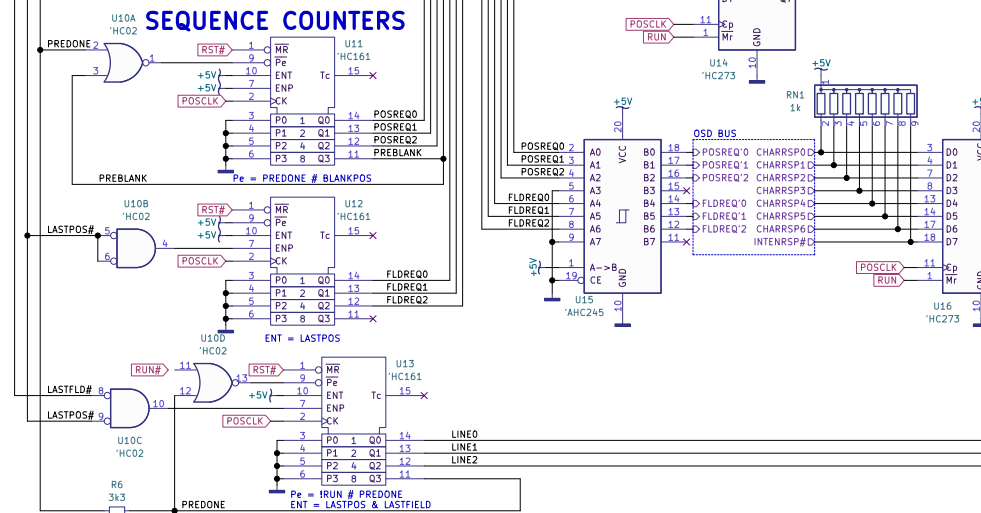
DOT COUNTER and POSCLK GENERATOR



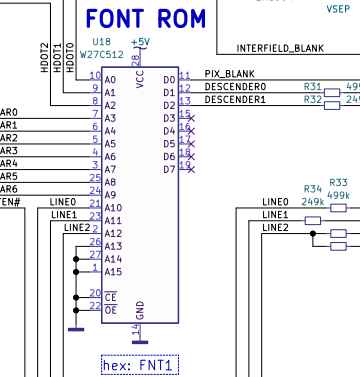
START/DONE CONTROL



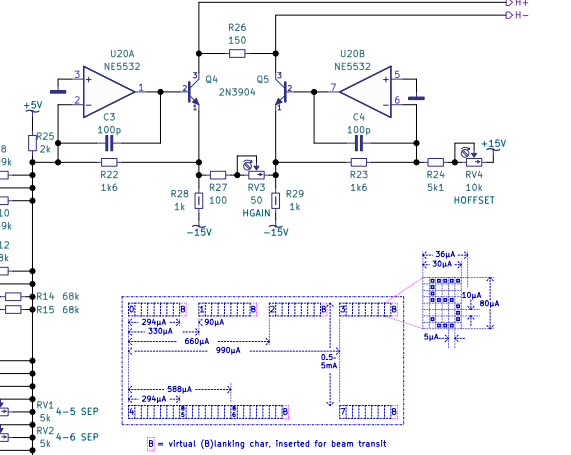
SEQUENCE COUNTERS



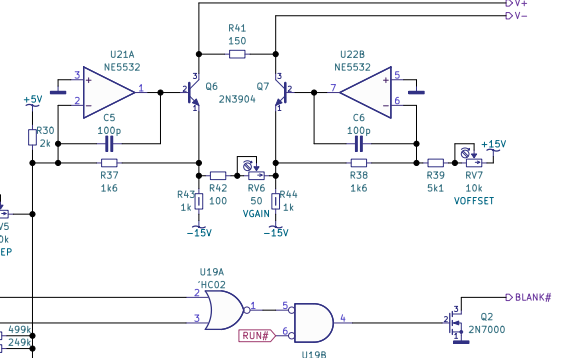
FONT ROM



HORIZONTAL DAC



VERTICAL DAC



Direction of clock distribution

