

## 6 Starting Your First HPS Design

Users can refer to the document My\_first\_HPS.pdf found in the DE1-SoC System CD manual folder.

This document describes the complete C/C++ design flow, including:

1. Create and build a C/C++ II project.
2. Copy files to Linux running on DE1-SoC.
3. Launch execution files on Linux running on DE1-SoC.

## 7 Getting Help

For further discussion, support, and resources, please go to:

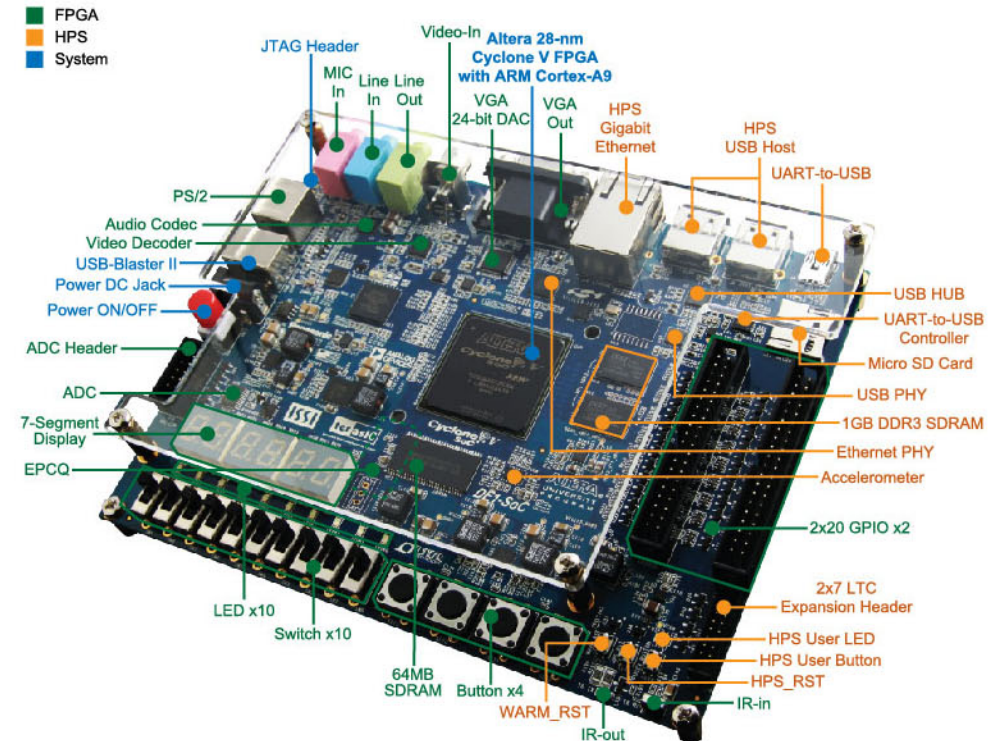
<http://de1-soc.terasic.com>

terasic  
www.terasic.com

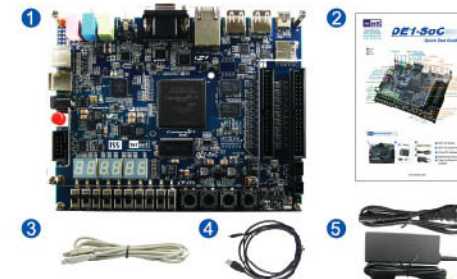
ALTERA  
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PROGRAM

# DE1-SoC

Quick Start Guide ►►



## 1 What's in the Box?



- 1 DE1-SoC Board
- 2 DE1-SoC Quick Start Guide
- 3 Type A to B USB Cable
- 4 Type A to Mini-B USB Cable
- 5 Power DC Adapter (12V)



If you encounter any problems, please contact us via

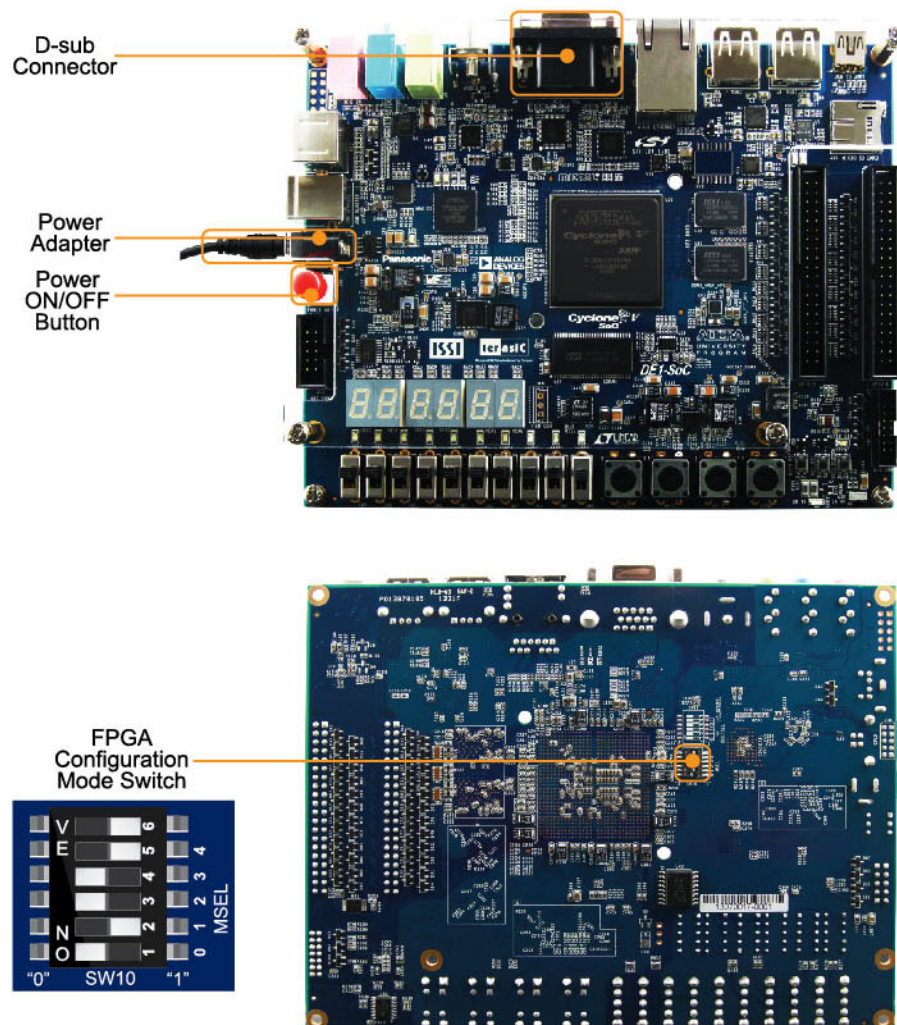
Email: [support@terasic.com](mailto:support@terasic.com)

Tel: +886-3-575-0880

Users can download the System CD from the following link:

<http://de1-soc.terasic.com>


## 2 Performing Power-on Test



1. Make sure MSEL[4:0] = 10010 in Fast AS Mode.
2. Connect the power adapter to the power jack on the DE1-SoC.
3. Press the power on button.
4. All the FPGA User LEDs are flashing and 7-Segment Displays are working.
5. Connect a VGA monitor to D-sub connector(J9) and the VGA monitor will display the board image.

## 3 Contents of the DE1-SoC System CD

Users can download the DE1-SoC System CD from this link:

 <http://de1-soc.terasic.com>

### DE1-SoC System CD Contents

Directory Name	Contents
User Manual	Contains the DE1-SoC documentations
Demonstrations	Contains design examples for DE1-SoC application
Datasheet	Contains the datasheets of the components on the DE1-SoC
Schematic	Contains the schematics of DE1-SoC
Tools	Contains the design and testing tools for DE1-SoC

## 4 Getting Started with the DE1-SoC Board

User can refer to the document DE1-SoC\_QSG.pdf found in the DE1-SoC System CD manual folder. This guide contains a quick overview of the hardware and software setup including step-by-step procedures from installing the necessary software tools to using the DE1-SoC board.

The main topics that this guide covers are listed below:

1. Software Installation: Installing Quartus II and EDS.
2. Development Board Setup: Powering on the DE1-SoC.
3. Perform FPGA System Test: Downloading a FPGA SRAM Objective File (.sof).
4. Running Linux on DE1-SoC Board.

## 5 Starting Your First FPGA Design

User can refer to the document My\_first\_FPGA.pdf found in the DE1-SoC System CD manual folder.

This document describes the complete FPGA design flow, including:

1. Creating a new Quartus II project.
2. Adding user logic and utilizing mega-core IPs.
3. Downloading a .sof file to the FPGA to view the result.