which is Less (3) 2 > (3) 1 si Avilu · Vedor dods overcom the shortcoming of Lamport dods finding the maximum of each item. clad-vedor with the clade-vedor in the merrage, eti especess receives a message, it merces : its where n= number of processes. It is no essence in lampost docks for each process, but each process beeps track of the clock of each other process. · A vedor clock is similar to the lampost clock above. Vector clocks $TC^{\times} = TC^{\times} + I$ 32/3 ICY = ICY no change (1) $f(L_x+1)$) $f(L_y+1)$ ahoo sandr its logical clack LCx Then a processor × sends a message to N it (2) An event occums on processor [] = LC; + I (2) Foch processor/core is has a logical clock [Lamport clades in local as well as distributed systems. that enable a basic partial ordering of events, Legical clocks are constantly updated timestamps Definition Logical Clocks

· However, Vedor ache more expensive in terms of boundrieth.

· Vectors docher capture consodity, Mich Lamport docher do not.